MSKSEMI















ESD

TVS

TSS

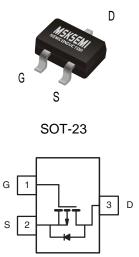
MOV

GDT

PLED

Broduct data sheet





Features

- -30V, -1.5A, $RDS(ON) = 900m\Omega@VGS = -10V$
- Fast switching
- Green Device Available
- Suit for -2.5V Gate Drive Applications

Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-Held Instruments

BVDSS	RDSON	ID
-30V	900m $Ω$	-1.5A

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-30	V
V _G s	Gate-Source Voltage	±12	V
	Drain Current – Continuous (T _A =25°C)	- 1.5	Α
ID	Drain Current – Continuous (T _A =70°C)	-1.0	А
Ірм	Drain Current – Pulsed¹	-5.5	А
	Power Dissipation (T _A =25°C)	1.11	W
P _D	Power Dissipation – Derate above 25°C	0.012	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
Reja	Thermal Resistance Junction to ambient		80	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-30			V
△BV _{DSS} /△T _J	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =-1mA		-0.03		V/°C
la a a	Drain Source Leakage Current	V _{DS} =-30V , V _{GS} =0V , T _J =25°C			-1	uA
IDSS	Drain-Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =125°C			-10	uA
Igss	Gate-Source Leakage Current	V _{GS} =±12V , V _{DS} =0V			±100	nA

On Characteristics

	Static Drain-Source On-Resistance	V _{GS} =-10V , I _D =-1.5A		900	1200	mΩ
R _{DS(ON)}		V _{GS} =-4.5V , I _D =-1.0A		1200	1500	mΩ
		V _{GS} =-2.5V , I _D =-0.5A		1500	1800	mΩ
V _{GS(th)}	Gate Threshold Voltage			- 0.9	- 1.5	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=-250uA$		3		mV/°C
gfs	Forward Transconductance	V _{DS} =-10V , I _D =-1A		5.4		S

Dynamic and switching Characteristics

•					
0	Total Gate Charge ^{2, 3}	V _{DS} =-15V , V _{GS} =-10V , I _D =-1A	 12.5		
Qg	Total Gate Charge-15		 8	-	nC
Qgs	Gate-Source Charge ^{2, 3}	V _{DS} =-15V , V _{GS} =-4.5V , I _D =-1A	 1.9		IIC
Q_{gd}	Gate-Drain Charge ^{2, 3}		 1.4		
T _{d(on)}	Turn-On Delay Time ^{2, 3}		 5.4		
Tr	Rise Time ^{2,3}	V_{DD} =-15 V , V_{GS} =-10 V , R_{G} =6 Ω	 19.4		no
T _{d(off)}	Turn-Off Delay Time ^{2, 3}	I _D =-0.5A	 45.9		ns
T _f	Fall Time ^{2,3}		 12.4		
Ciss	Input Capacitance		 810		
Coss	Output Capacitance	V _{DS} =-15V , V _{GS} =0V , F=1MHz	 85		pF
Crss	Reverse Transfer Capacitance		 50		

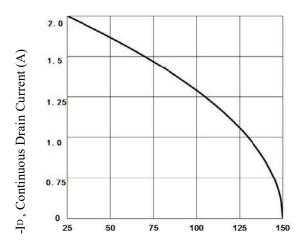
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V =V =0V Force Current			- 1.5	Α
Ism	Pulsed Source Current	V _G =V _D =0V , Force Current			-3.0	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-0.5A , T _J =25°C			-1.3	V
t _{rr}	Reverse Recovery Time	V _R =-30V, I _S =-1A		115		ns
Qrr	Reverse Recovery Charge	di/dt=100A/µs, Tյ=25°C		150		nC

Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width $\leqq 300 us$, duty cycle $\leqq 2\%.$
- 3. Essentially independent of operating temperature.





 T_C , Case Temperature (°C)

Fig.1 Continuous Drain Current vs. Tc

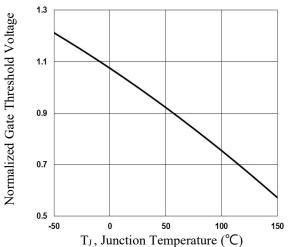


Fig.3 Normalized V_{th} vs. T_J

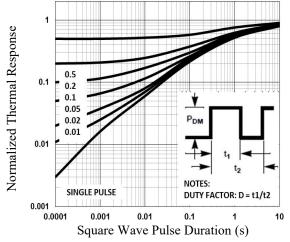


Fig.5 Normalized Transient Impedance

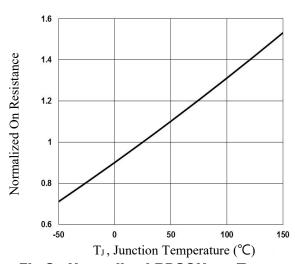


Fig.2 Normalized RDSON vs. T_J

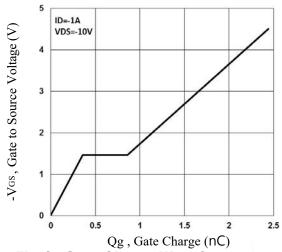


Fig.4 Gate Charge Waveform

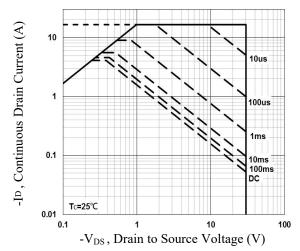
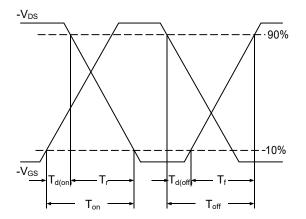


Fig.6 Maximum Safe Operation Area





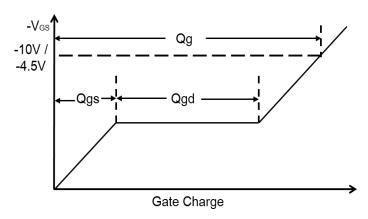
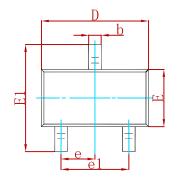


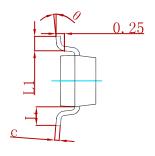
Fig.7 Switching Time Waveform

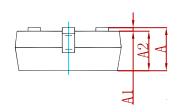
Fig.8 Gate Charge Waveform



PACKAGE MECHANICAL DATA

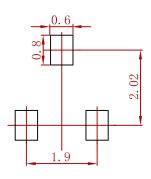






Symbol	Dimensions	Dimensions In Millimeters		s In Inches
Symbol	Min	Max	Min	Max
Α	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
С	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
Е	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
е	0.950 TYP		0.037	7 TYP
e1	1.800	2.000	0.071	0.079
L	0.550) REF	0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

Suggested Pad Layout



- 1.Controlling dimension:in millimeters.2.General tolerance:± 0.05mm.3.The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
FDV304P	SOT-23	3000



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