MSKSEMI















ESD

TVS

TSS

MOV

GDT

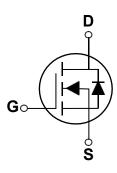
PLED

Broduct data sheet





SOT-23



Features

- 30V,0.25A, RDS(ON) =1.5Ω@VGS=4V
- Improved dv/dt capability
- Fast switching
- Green Device Available

Applications

- Motor Drive
- Power Tools
- LED Lighting

BVDSS	RDSON	ID
30V	1.5Ω	0.25A

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _G s	Gate-Source Voltage	±16	V
	Drain Current – Continuous (T _C =25°C)	0.25	А
ID	Drain Current – Continuous (Tc=100°C)	0.1	Α
I _{DM}	Drain Current – Pulsed ¹	1.0	Α
Б	Power Dissipation (T _C =25°C)	0.35	W
P _D	Power Dissipation – Derate above 25°C	0.003	W/℃
T _{STG}	Storage Temperature Range	-50 to 150	°C
TJ	Operating Junction Temperature Range	-50 to 150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
Reja	Thermal Resistance Junction to ambient		357	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =1mA		0.04		V/℃
l	Prain Source Leakage Current	V _{DS} =30V , V _{GS} =0V , T _J =25°C			1	uA
IDSS	Drain-Source Leakage Current	V _{DS} =20V , V _{GS} =0V , T _J =125°C			100	uA
Igss	Gate-Source Leakage Current	$V_{GS} = \pm 16V$, $V_{DS} = 0V$			±5	uA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =4V , I _D =0.2A		1.5	3.5	Ω
V _{GS(th)}	Gate Threshold Voltage	VV I2500A	0.8	1.1	1.6	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	V _{GS} =V _{DS} , I _D =250uA		-4		mV/℃
gfs	Forward Transconductance	V _{DS} =10V , I _D =0.1A		0.24		S

Dynamic and switching Characteristics

_ J				
Q_g	Total Gate Charge ^{2, 3}		 1.1	
Qgs	Gate-Source Charge ^{2, 3}	V_{DS} =30V , V_{GS} =10V , I_{D} =0.2A	 0.1	 nC
Q _{gd}	Gate-Drain Charge ^{2, 3}		 0.23	
T _{d(on)}	Turn-On Delay Time ^{2, 3}		 3	
Tr	Rise Time ^{2, 3}	V_{DD} =30 V , V_{GS} =10 V , R_{G} =6 Ω	 5	 no
T _{d(off)}	Turn-Off Delay Time ^{2, 3}	I _D =0.2A	 14	 ns
Tf	Fall Time ^{2, 3}		 9	
Ciss	Input Capacitance		 30.6	
Coss	Output Capacitance	V_{DS} =10V , V_{GS} =0V , F=1MHz	 5.5	 pF
Crss	Reverse Transfer Capacitance		 4	

Drain-So	urce Diode Characteristi	cs and Maximum Ratings				
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	V =V =0V Force Current			0.25	Α
I _{SM}	Pulsed Source Current	V _G =V _D =0V , Force Current			0.5	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =0.2A , T _J =25°C			1.4	V
t _{rr}	Reverse Recovery Time ²	V _G s=30V,Is=0.2A , dI/dt=100A/μs				ns
Q _{rr}	Reverse Recovery Charge ²	T _J =25℃				nC

- Repetitive Rating: Pulsed width limited by maximum junction temperature. 1.
- 2. V_{DD} =25V, V_{GS} =10V,L=1mH, I_{AS} =7A., R_{G} =25 Ω ,Starting T_{J} =25 $^{\circ}$ C
- The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%. Essentially independent of operating temperature. 3.



Semiconductor Compia

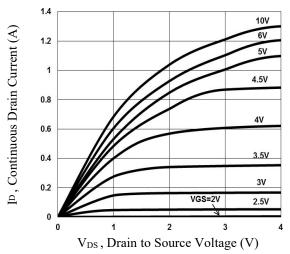


Fig.1 Output Characteristics

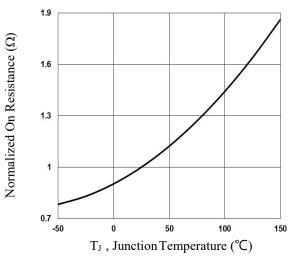


Fig.3 Normalized RDSON vs. TJ

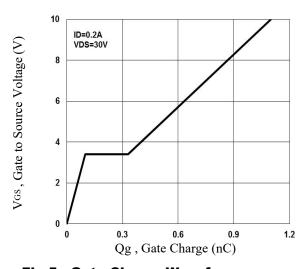


Fig.5 Gate Charge Waveform

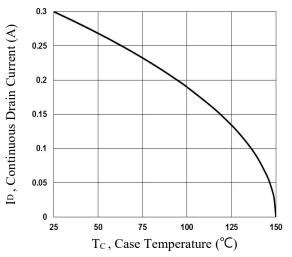


Fig.2 Continuous Drain Current vs. Tc

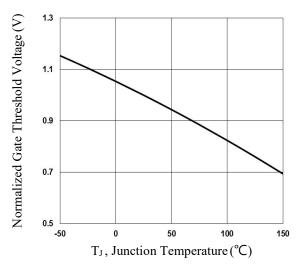


Fig.4 Normalized V_{th} vs. T_J

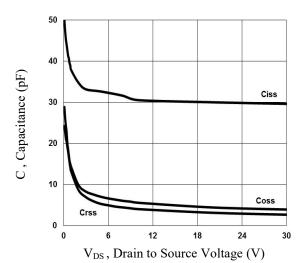


Fig.6 Capacitance Characteristics



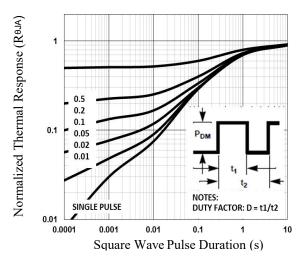


Fig.7 Normalized Transient Impedance

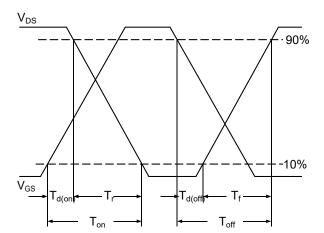


Fig.9 Switching Time Waveform

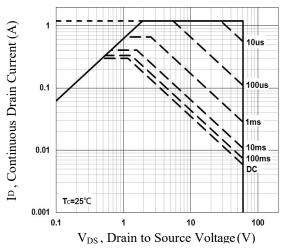
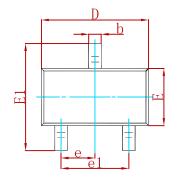
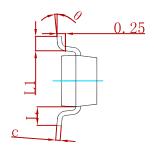


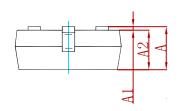
Fig.8 Maximum Safe Operation Area



PACKAGE MECHANICAL DATA

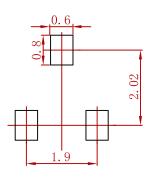






Symbol	Dimensions	Dimensions In Millimeters		s In Inches	
Symbol	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
Е	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037	7 TYP	
e1	1.800	2.000	0.071	0.079	
L	0.550) REF	0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

Suggested Pad Layout



- 1.Controlling dimension:in millimeters.2.General tolerance:± 0.05mm.3.The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
FDV301N	SOT-23	3000



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