

Description

The SI2318 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

 $V_{DS} = 40V I_D = 5A$ $R_{DS(ON)} < 38m\Omega @ V_{GS} = 10V$ $R_{DS(ON)} < 52m\Omega @ V_{GS} = 4.5V$

Application

Battery protection

Load switch

Uninterruptible power supply

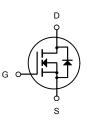
Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
SI2318	SOT-23	2318	3000

Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Symbol	Parameter Rating		Units	
Vds	Drain-Source Voltage	40	V	
Vgs	Gate-Source Voltage	±12	V	
	Drain Current – Continuous (T _c =25 $^{\circ}$ C)	5	А	
lo	Drain Current – Continuous (Tc=70 °C)	4.2	А	
PD	Power Dissipation (T _c =25 $^{\circ}$ C)	1.56	W	
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150 °C		
R _{0JA}	Thermal Resistance Junction to ambient	80 °C/W		





N-Channel MOSFET



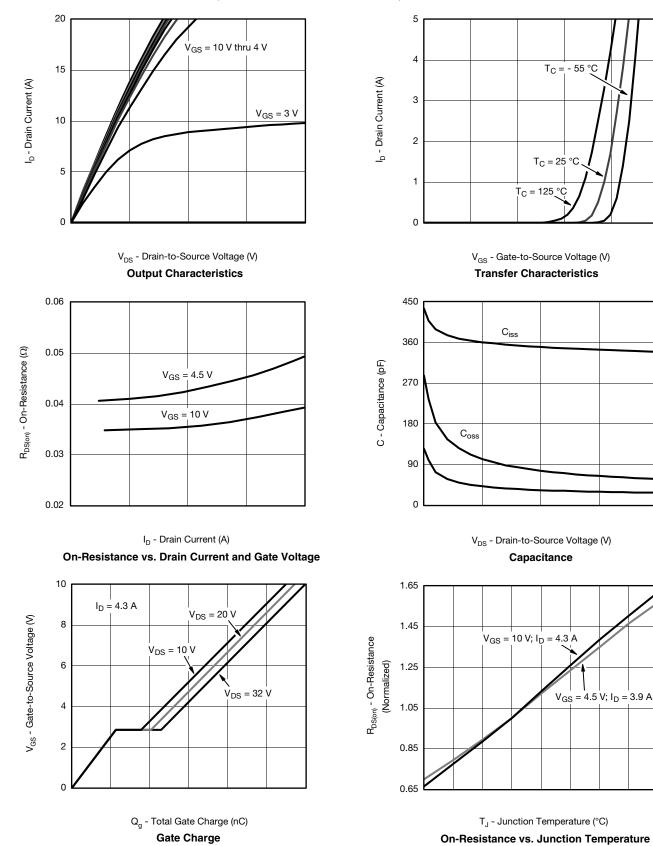
Symbol	Parameter	Condition	Min	Тур	Max	Unit		
Static Elec	Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)							
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	Vgs=0V Ib=250µA	40			V		
	Zero Gate Voltage Drain Current(T₄=25℃)	(T _A =25°C) VDS=40V, VGS=0V			1	μΑ		
DSS	Zero Gate Voltage Drain Current(T _A =125 $^{\circ}$ C)	Vds=40V, Vgs=0V			100	uA		
I _{GSS}	Gate-Body Leakage Current	Vgs=±20V, Vds=0V			±100	nA		
$V_{GS(TH)}$	Gate Threshold Voltage	Vds=Vgs, Id=250µA	0.7	1.2	2.0	V		
R _{DS(ON)}	Drain-Source On-State Resistance②	Vgs=10V, Id=5A		30	38	mΩ		
R _{DS(ON)}	Drain-Source On-State Resistance②	Vgs=4.5V, Id=4A		36	52	mΩ		
Dynamic I	Electrical Characteristics @ Tյ = 25°C (ι	unless otherwise state	d)		•			
C _{iss}	Input Capacitance			340		pF		
C _{oss}	Output Capacitance	VDS=20V, VGS=0V, f=1MHz		60		pF		
C _{rss}	Reverse Transfer Capacitance			30		pF		
R_{g}	Gate Resistance	f=1MHz		7.8		Ω		
Q _g	Total Gate Charge	V _{DS} =20V		5.8		nC		
Q_{gs}	Gate Source Charge	ID=5A,		0.4		nC		
Q_{gd}	Gate Drain Charge	Vgs=10V		2		nC		
	Characteristics @ TJ = 25°C (unless ot	herwise stated)	-		-			
t _{d(on)}	Turn on Delay Time			4.1		ns		
t _r	Turn on Rise Time	Vdd=20V, Id=3.5A,		11.6		ns		
t _{d(off)}	Turn Off Delay Time	Rg=1Ω, Vgs=4.5V	-	24		ns		
t _f	Turn Off Fall Time	VGG-T.VV		7.6		ns		
Source Dr	ain Diode Characteristics @ TJ = 25°C (unless otherwise state	ed)					
I _{SD}	Source drain current(Body Diode)	T _A =25℃			1.75	А		
V _{SD}	Forward on voltage②	Tj=25℃, IsD=3.5A, Vgs=0V		0.79	1.2	V		

Notes:

1 Pulse width limited by maximum allowable junction temperature

@Pulse test ; Pulse width ${\leq}300\mu s,$ duty cycle ${\leq}2\%.$

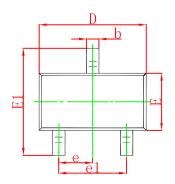


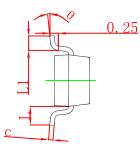


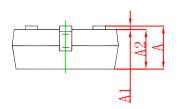
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



SOT-23 Package Outline Dimensions

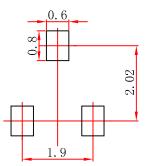






Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

SOT-23 Suggested Pad Layout



Note: 1.Controlling dimension:in millimeters.

2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.



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