

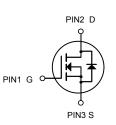
#### Description

The HXY3400MI uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = 30V,I<sub>D</sub> = 5.8A R<sub>DS(ON)</sub> < 28mΩ @ V<sub>GS</sub>=10V R<sub>DS(ON)</sub> < 34mΩ @ V<sub>GS</sub>=4.5V





#### Application

High power and current handing capability Lead free product is acquired Surface mount package PWM applications Load switch Power management

### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXY3400MI	SOT23-3L	X0VX	3000PCS

### Absolute Maximum Ratings (T<sub>A</sub>=25<sup>°</sup>C unless otherwise noted)

Symbol	Parameter	Limit	Unit
Vds	Drain-Source Voltage	30	V
Vgs	Gate-Source Voltage	±12	V
lo	Drain Current-Continuous	5.8	А
Ідм	Drain Current-Pulsed (Note 1)	30	А
PD	Maximum Power Dissipation	1.4	W
Тј,Тѕтб	Operating Junction and Storage Temperature Range	-55 To 150	°C
Reja	Thermal Resistance, Junction-to-Ambient (Note 2)	89	°C <b>/W</b>

N-Channel MOSFET



Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics				ı	1	ı
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	30	33	-	V
Zero Gate Voltage Drain Current	IDSS	$V_{DS}$ =30V, $V_{GS}$ =0V	-	-	1	μA
Gate-Body Leakage Current	Igss	V <sub>GS</sub> =±12V,V <sub>DS</sub> =0V	-	-	±100	nA
Gate Threshold Voltage	VGS(th)	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	0.7	0.9	1.4	V
	Rds(on)	V <sub>GS</sub> =2.5V, I <sub>D</sub> =4A	-	41	55	mΩ
Drain-Source On-State Resistance		$V_{GS}$ =4.5V, $I_D$ =5A	-	23	34	mΩ
		V <sub>GS</sub> =10V, I <sub>D</sub> =5.8A	-	21	28	mΩ
Forward Transconductance	gfs	V <sub>DS</sub> =5V,I <sub>D</sub> =5A	10	-	-	S
Input Capacitance	Clss		-	825	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V, F=1.0MHz	-	100	-	PF
Reverse Transfer Capacitance	Crss		-	78	-	PF
Turn-on Delay Time	td(on)		-	3.3	-	nS
Turn-on Rise Time	tr	$V_{DD}$ =15V, RL=2.7 $\Omega$	-	4.8	-	nS
Turn-Off Delay Time	td(off)	V <sub>GS</sub> =10V,R <sub>GEN</sub> =3Ω - 26		26	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	4	-	nS
Total Gate Charge	Qg		-	10	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =15V,I <sub>D</sub> =5.8A,	-	1.6	-	nC
Gate-Drain Charge	Qgd	V <sub>GS</sub> =4.5V	-	3.1	-	nC
Diode Forward Voltage (Note 3)	Vsd	V <sub>GS</sub> =0V,I <sub>S</sub> =5.8A	-	-	1.2	V
Diode Forward Current (Note 2)	ls		-	-	5.8	Α

# Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

**2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.

**3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.

4. Guaranteed by design, not subject to production



# **Typical Electrical and Thermal Characteristics**

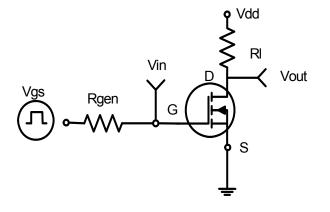
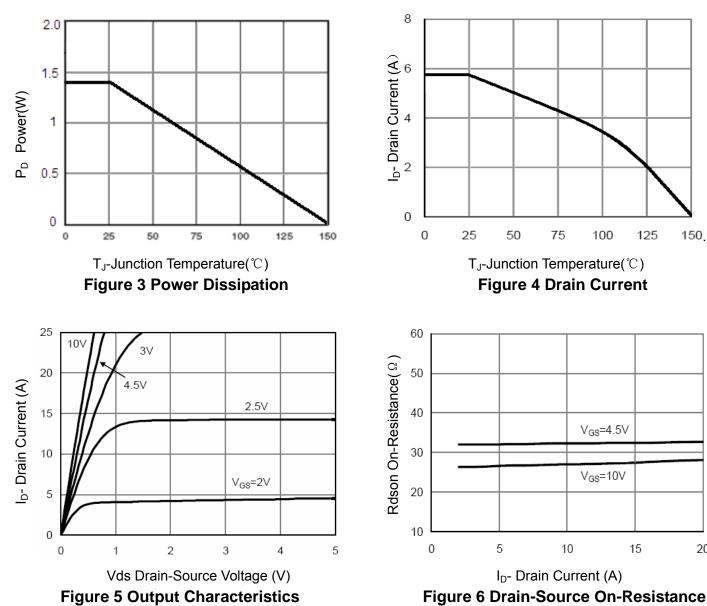


Figure 1:Switching Test Circuit



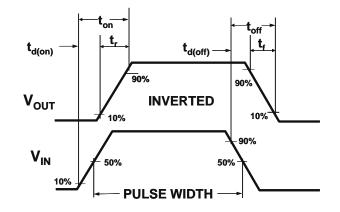
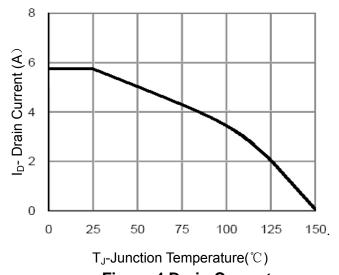
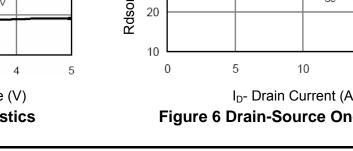


Figure 2:Switching Waveforms

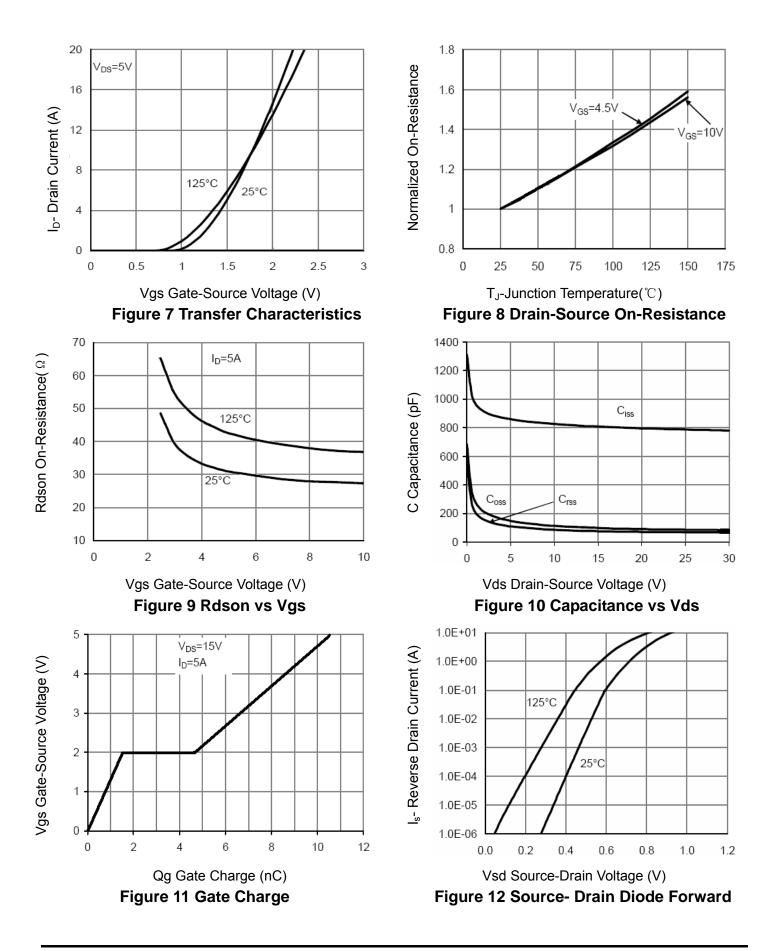


**Figure 4 Drain Current** 



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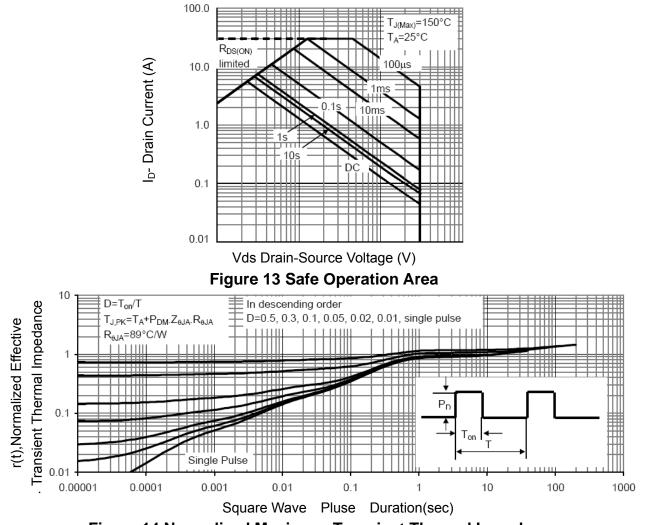
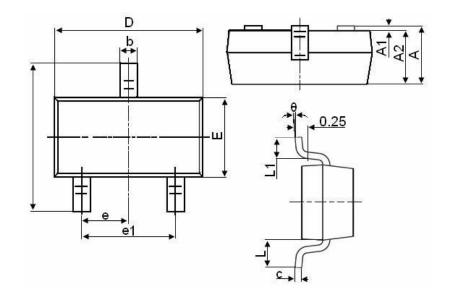


Figure 14 Normalized Maximum Transient Thermal Impedance



# SOT23-3L Package Information



Symbol	Dimensions in Millimeters		
	MIN.	MAX.	
А	1.050	1.250	
A1	0.000	0.100	
A2	1.050	1.150	
b	0.300	0.500	
с	0.100	0.200	
D	2.800	3.000	
E	1.500	1.700	
E1	2.650	2.950	
е	0.950TYP		
e1	1.800	2.000	
L	0.550REF		
L1	0.300	0.600	
θ	0°	8°	



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