

### Description

The HXY100P03NF uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a

Battery protection or in other Switching application.

### **General Features**

V<sub>DS</sub> = -30V I<sub>D</sub> =-100A

 $R_{DS(ON)} < 4 \text{ m}\Omega \text{ V}_{GS}$ =-10V

### Application

Battery protection

Load switch

Uninterruptible power supply

### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXY100P03NF	DFN5X6-8L	100P03 XXX YYYY	5000

### Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
Vds	Drain-Source Voltage	-30	V
Vgs	Gate-Source Voltage	±20	V
l₀@Tc=25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	-100	A
l⊳@Tc=100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	-70	А
Ідм	Pulsed Drain Current <sup>2</sup>	-250	А
EAS	Single Pulse Avalanche Energy <sup>3</sup>	80	mJ
las	Avalanche Current	-70	А
P₀@Tc=25°C	Total Power Dissipation <sup>4</sup>	120	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
Reja	Thermal Resistance Junction-Ambient <sup>1</sup>	50	°C/W
Rejc	Thermal Resistance Junction-Case <sup>1</sup>	1.6	°C/W





DFN5X6-8L

P-Channel MOSFET



## Electrical Characteristics (TJ=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-30			V	
D	Statia Duain Source On Desistance <sup>2</sup>	V <sub>GS</sub> =-10V , I <sub>D</sub> =-20A		3	4.0	mΩ	
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-15A		4.2	6.0	mΩ	
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_{D}=-250uA$	-1.2		-2.5	V	
	Drain-Source Leakage Current	V <sub>DS</sub> =-24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃			-1	uA	
I <sub>DSS</sub>		V <sub>DS</sub> =-24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃			-5		
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$			±100	nA	
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.2		Ω	
Qg	Total Gate Charge (-10V)			60			
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =-15V , V <sub>GS</sub> =-10V , I <sub>D</sub> =-18A		9		nC	
Q <sub>gd</sub>	Gate-Drain Charge			15			
T <sub>d(on)</sub>	Turn-On Delay Time			17			
Tr	Rise Time	$V_{DD}$ =-15V , $V_{GS}$ =-10V , $R_{G}$ =3.3 $\Omega$ ,		40			
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =-20A		55		ns	
T <sub>f</sub>	Fall Time			13			
C <sub>iss</sub>	Input Capacitance			3450			
Coss	Output Capacitance	V <sub>DS</sub> =-25V , V <sub>GS</sub> =0V , f=1MHz		255	pF		
C <sub>rss</sub>	Reverse Transfer Capacitance			140			

## **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0V$ , Force Current			-100	А
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25℃			-1.2	V
t <sub>rr</sub>	Reverse Recovery Time	IF=-20A , di/dt=100A/µs ,		22		nS
Qrr	Reverse Recovery Charge	T <b>J=25°</b> C		72		nC

Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

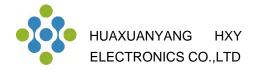
2.The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%

3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =-50V,  $V_{GS}$ =-10V, L=0.1mH, I<sub>AS</sub>=-40A

4. The power dissipation is limited by 150°C junction temperature

5. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications , should be limited by total power dissipation

6. The maximum current rating is package limited.



## **Typical Characteristics**

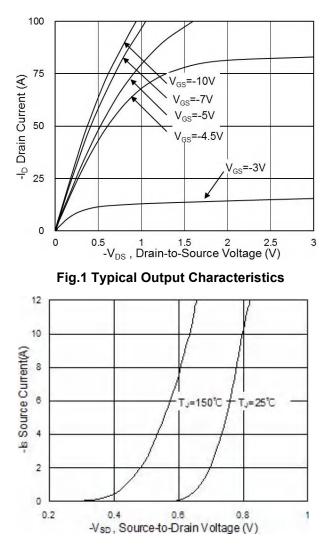


Fig.3 Source Drain Forward Characteristics

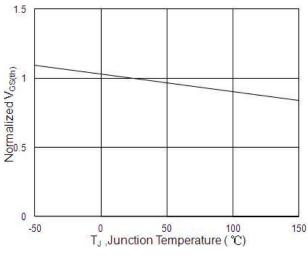


Fig.5 Normalized -V<sub>GS(th)</sub> vs  $T_J$ 

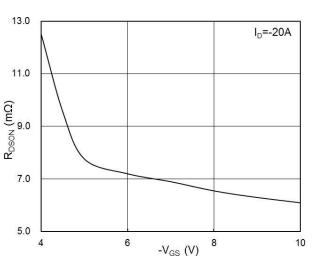


Fig.2 On-Resistance vs G-S Voltage

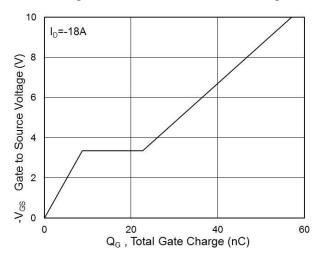
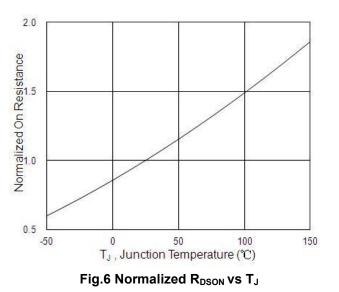
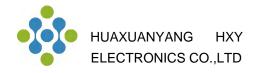


Fig.4 Gate-Charge Characteristics





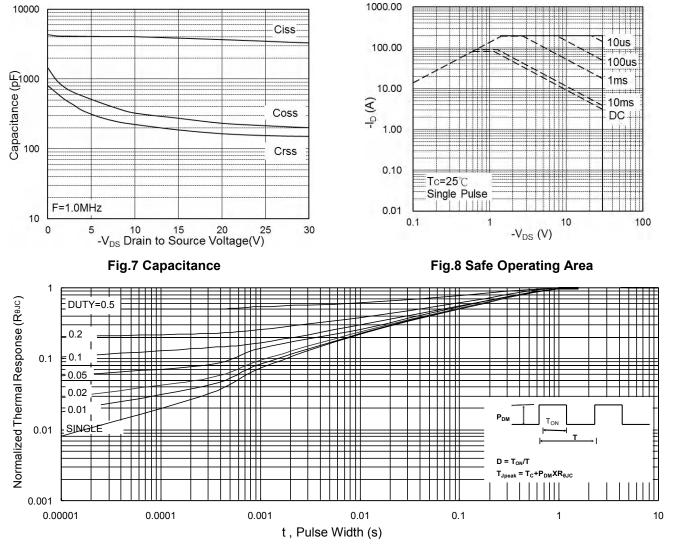


Fig.9 Normalized Maximum Transient Thermal Impedance

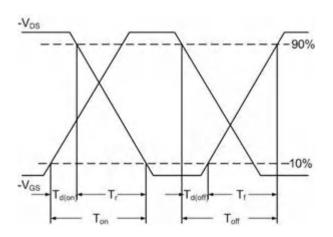


Fig.10 Switching Time Waveform

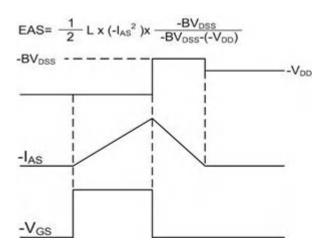
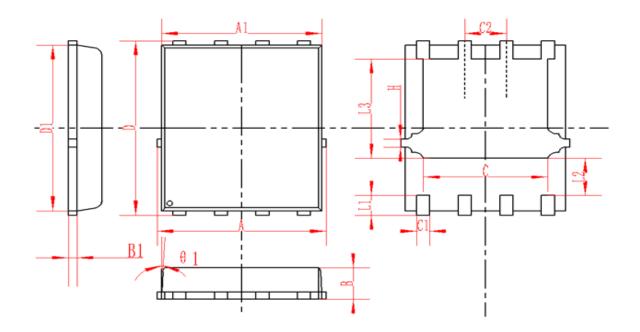


Fig.11 Unclamped Inductive Switching Waveform



# **DFN5X6-8L Package Information**



SYMBOL	MM			INCH			
	MIN	NOM	MAX	MIN	NOM	MAX	
А	4.95	5	5.05	0.195	0.197	0.199	
A1	4.82	4.9	4.98	0.190	0.193	0.196	
D	5.98	6	6.02	0.235	0.236	0.237	
D1	5.67	5.75	5.83	0.223	0.226	0.230	
В	0.9	0.95	1	0.035	0.037	0.039	
B1	0.254REF			0.010REF			
С	3.95	4	4.05	0.156	0.157	0.159	
C1	0.35	0.4	0.45	0.014	0.016	0.018	
C2	1.27TYP			0.5TYP			
θ1	8°	10°	12°	8°	10°	12°	
L1	0.63	0.64	0.65	0.025	0.025	0.026	
L2	1.2	1.3	1.4	0.047	0.051	0.055	
L3	3.415	3.42	3.425	0.134	0.135	0.135	
Н	0.24	0.25	0.26	0.009	0.010	0.010	



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