

### JIANGSU CHANGJING ELECTRONICS TECHNOLOGY CO., LTD

# JBSL Plastic-Encapsulate Bridge Rectifier

## JBSL510 General Purpose Bridge Rectifier

#### **Features**

• I<sub>F(AV)</sub> 5A

●VRRM 1000V

• High surge current capability

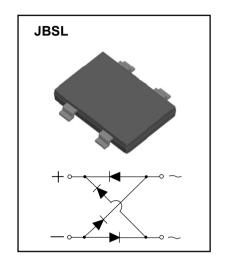
Glass passivated chip

#### **Applications**

 General purpose 1 phase Bridge rectifier applications

#### Marking

• JBSL510



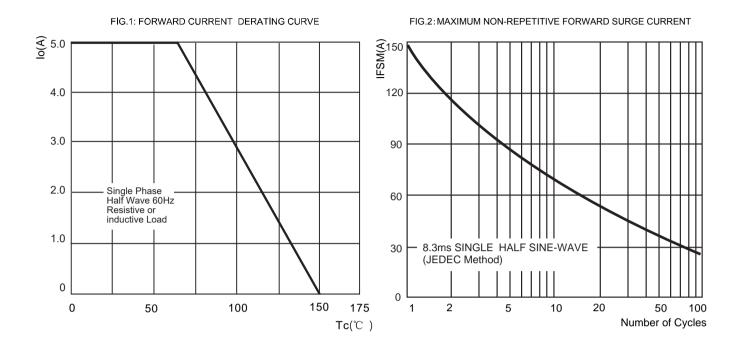
### **Limiting Values (Absolute Maximum Rating)**

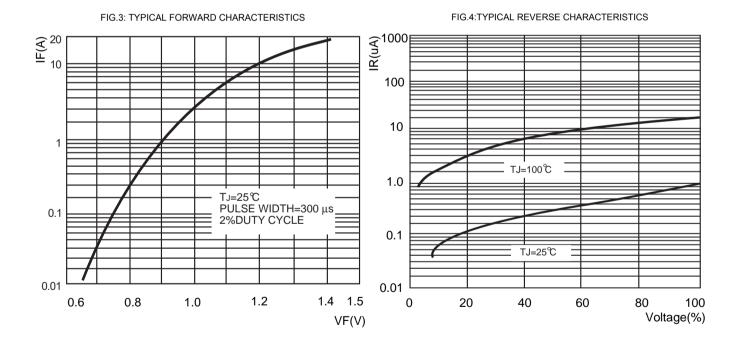
Item	Symbol	Unit	Conditions		JBSL510
Repetitive Peak Reverse Voltage	$V_{RRM}$	V			1000
Maximum RMS Voltage	V <sub>RMS</sub>	V			700
Maximum DC Blocking Voltage	$V_{RRM}$	V			1000
Average Rectified Output Current	lo	А	60Hz sine wave, R-load,Tc=80℃	On alumina substrate	5.0
Surge(Non-repetitive)Forward Current	I <sub>FSM</sub>	А	8.3ms sine wave, 1 cycle, T <sub>j</sub> =25℃		150
Current Squared Time	l <sup>2</sup> t	A <sup>2</sup> S	1ms≤t<8.3ms Tj=25℃,Rating of per diode		93
Operation Junction and Storage Temperature Range	$T_J$ , $T_{stg}$	$^{\circ}\!$			-55 ~+150

### Electrical Characteristics (T=25°C Unless otherwise specified)

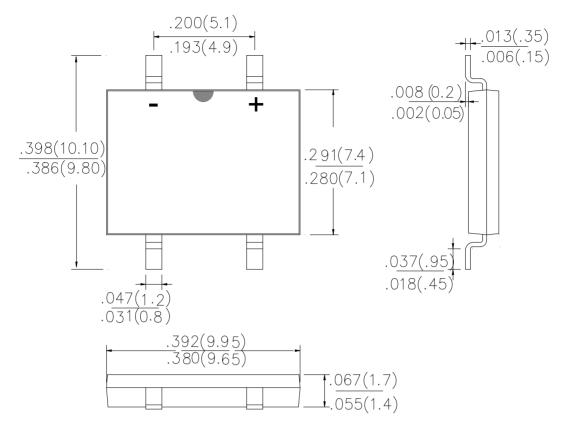
Item	Symbol	Unit	Test Condition	JBSL510
Maximum Peak Forward Voltage	V <sub>FM</sub>	V	I <sub>FM</sub> =5.0A, Pulse measurement, Rating of per diode	1.1
Maximum Peak Reverse Current	I <sub>RRM</sub>	μА	V <sub>RM</sub> =V <sub>RRM</sub> , Ta=25 °C	5
			V <sub>RM</sub> =V <sub>RRM</sub> , Ta=100 ℃	100
Typical junction capacitance	Сл	pF	Measured at 1MHz and applied reverse voltage of 4.0V D.C.	50
Thermal Resistance	$R_{\theta J-A}$	°C/W	Between junction and ambient, On alumina substrate	60

### **Typical Characteristics**



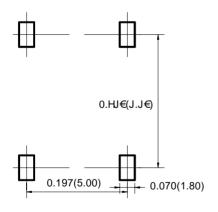


### **JBSL** Package Outline Dimensions



Dimensions in inches and (millimeters)

#### **JBSL Suggested Pad Layout**



#### Note:

- 1. Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

#### **NOTICE**

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