

General Features

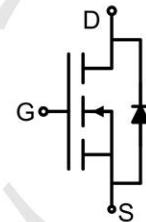
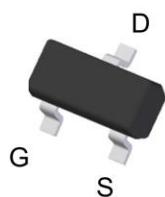
- $V_{DS} = 30V, I_D = 5.8A$
- $R_{DS(ON)} < 59m\Omega @ V_{GS}=2.5V$
- $R_{DS(ON)} < 45m\Omega @ V_{GS}=4.5V$
- $R_{DS(ON)} < 41m\Omega @ V_{GS}=10V$

Application

- Load/Power Switching
- Interfacing Switching
- Battery Management for Ultra Small Portable
- Logic Level Shift

Package and Pin Configuration

SOT23



Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|--|----------------|------------|------|
| Drain-Source Voltage | V_{DS} | 30 | V |
| Gate-Source Voltage | V_{GS} | ± 12 | V |
| Drain Current-Continuous | I_D | 5.8 | A |
| Drain Current-Pulsed ^(Note 1) | I_{DM} | 30 | A |
| Maximum Power Dissipation | P_D | 1.4 | W |
| Operating Junction and Storage Temperature Range | T_J, T_{STG} | -55 To 150 | °C |

Thermal Characteristic

| | | | |
|---|-----------------|----|------|
| Thermal Resistance, Junction-to-Ambient ^(Note 2) | $R_{\theta JA}$ | 89 | °C/W |
|---|-----------------|----|------|

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|--|----------------------------|--|-----|-----|-----------|------------------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$ | 30 | | - | V |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$ | - | - | 1 | μA |
| Gate-Body Leakage Current | I_{GSS} | $V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$ | - | - | ± 100 | nA |
| On Characteristics <small>(Note 3)</small> | | | | | | |
| Gate Threshold Voltage | $V_{\text{GS}(\text{th})}$ | $V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$ | 0.7 | 0.9 | 1.4 | V |
| Drain-Source On-State Resistance | $R_{\text{DS}(\text{ON})}$ | $V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=4\text{A}$ | - | 45 | 59 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=5\text{A}$ | - | 31 | 45 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=10\text{V}, I_{\text{D}}=5.8\text{A}$ | - | 28 | 41 | $\text{m}\Omega$ |
| Forward Transconductance | g_{FS} | $V_{\text{DS}}=5\text{V}, I_{\text{D}}=5\text{A}$ | 10 | - | - | S |
| Dynamic Characteristics <small>(Note 4)</small> | | | | | | |
| Input Capacitance | C_{iss} | $V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$ | - | 820 | - | PF |
| Output Capacitance | C_{oss} | | - | 99 | - | PF |
| Reverse Transfer Capacitance | C_{rss} | | - | 77 | - | PF |
| Switching Characteristics <small>(Note 4)</small> | | | | | | |
| Turn-on Delay Time | $t_{\text{d}(\text{on})}$ | $V_{\text{DD}}=15\text{V}, R_{\text{L}}=2.7\Omega$ $V_{\text{GS}}=10\text{V}, R_{\text{GEN}}=3\Omega$ | - | 3.3 | - | nS |
| Turn-on Rise Time | t_r | | - | 4.8 | - | nS |
| Turn-Off Delay Time | $t_{\text{d}(\text{off})}$ | | - | 26 | - | nS |
| Turn-Off Fall Time | t_f | | - | 4 | - | nS |
| Total Gate Charge | Q_g | $V_{\text{DS}}=15\text{V}, I_{\text{D}}=5.8\text{A}, V_{\text{GS}}=4.5\text{V}$ | - | 9.5 | - | nC |
| Gate-Source Charge | Q_{gs} | | - | 1.5 | - | nC |
| Gate-Drain Charge | Q_{gd} | | - | 3 | - | nC |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage <small>(Note 3)</small> | V_{SD} | $V_{\text{GS}}=0\text{V}, I_{\text{S}}=5.8\text{A}$ | - | - | 1.2 | V |
| Diode Forward Current <small>(Note 2)</small> | I_{S} | | - | - | 5.8 | A |

Typical Electrical and Thermal Characteristics

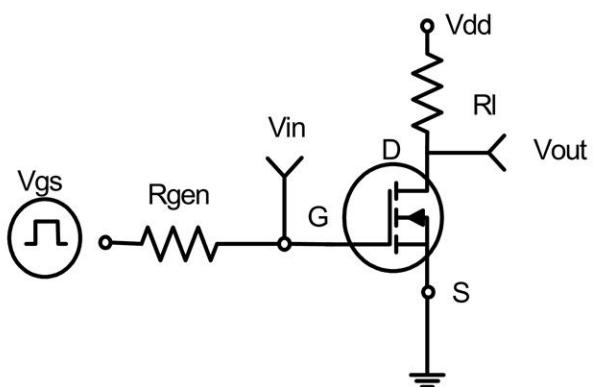


Figure 1:Switching Test Circuit

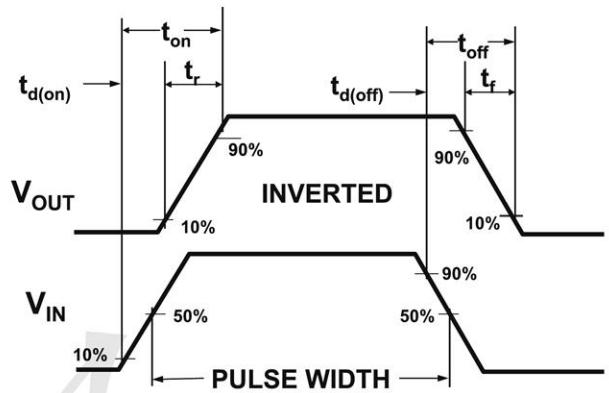


Figure 2:Switching Waveforms

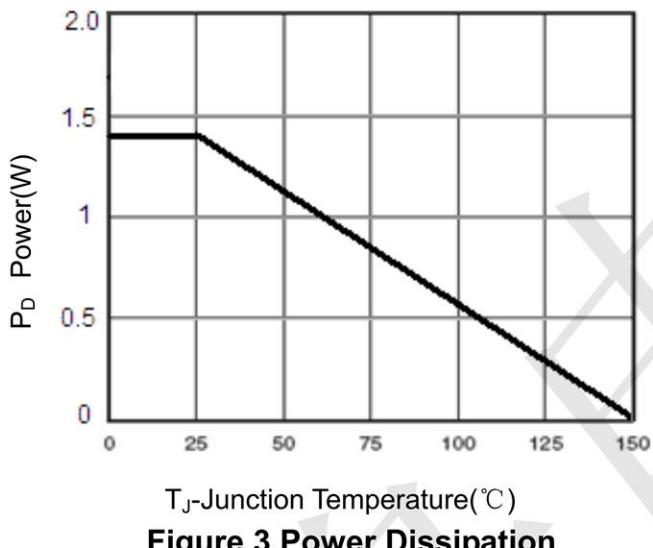


Figure 3 Power Dissipation

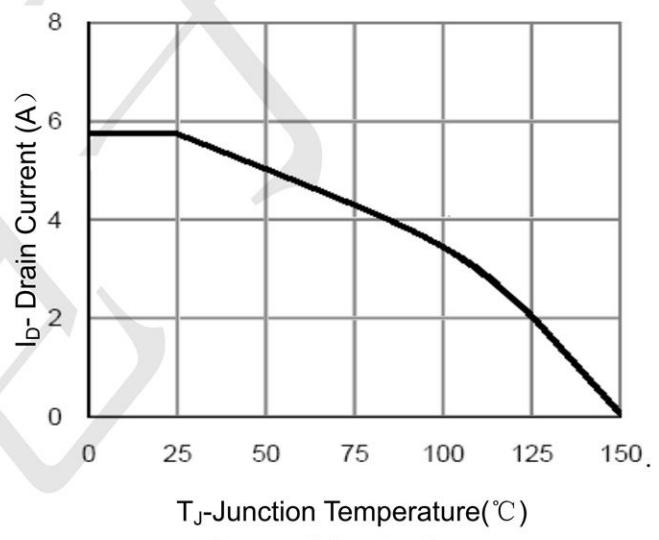


Figure 4 Drain Current

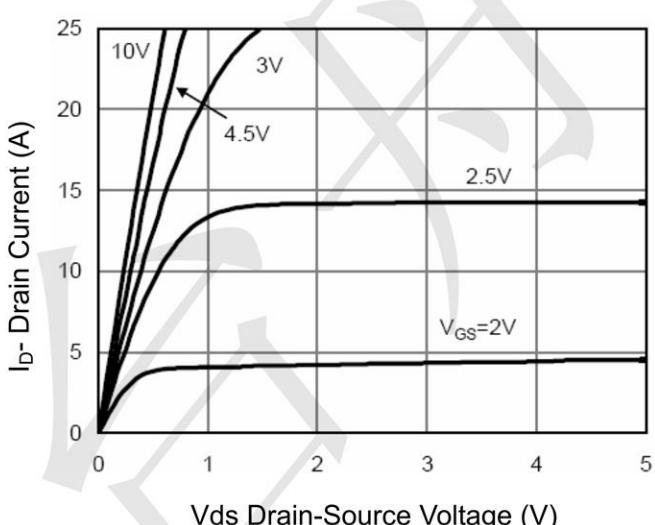


Figure 5 Output Characteristics

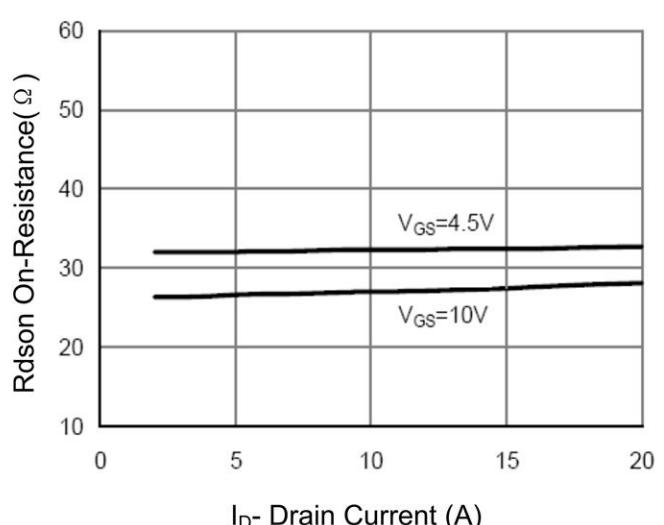


Figure 6 Drain-Source On-Resistance



TECH PUBLIC
台舟电子

TPM3400AS3

N-Channel Enhancement Mode Power MOSFET

www.sot23.com.tw

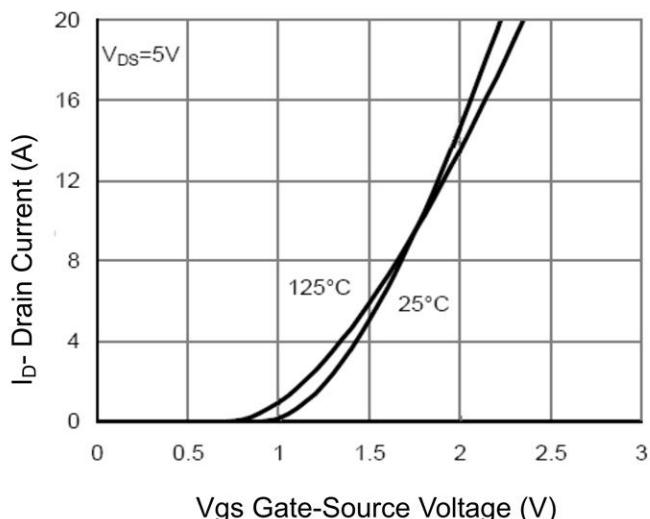


Figure 7 Transfer Characteristics

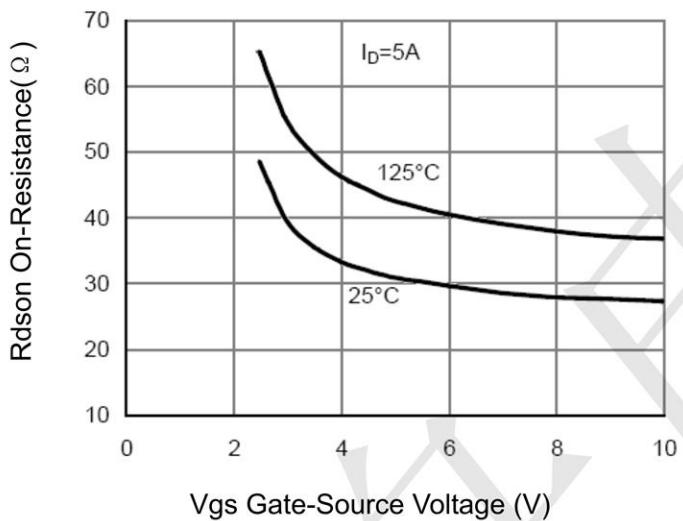


Figure 9 Output Characteristics

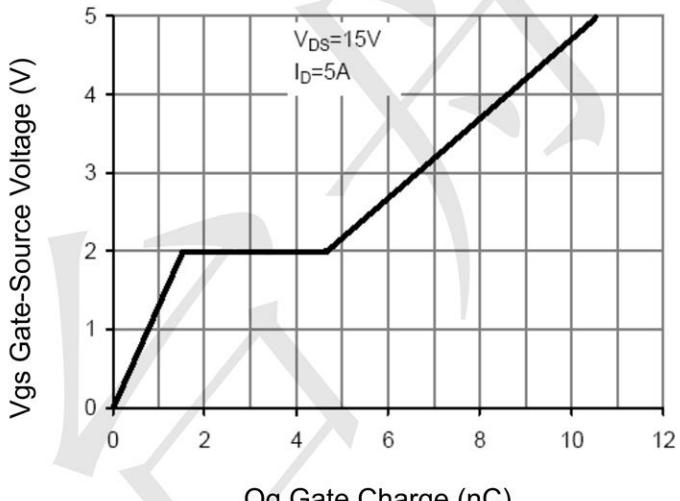


Figure 11 Gate Charge

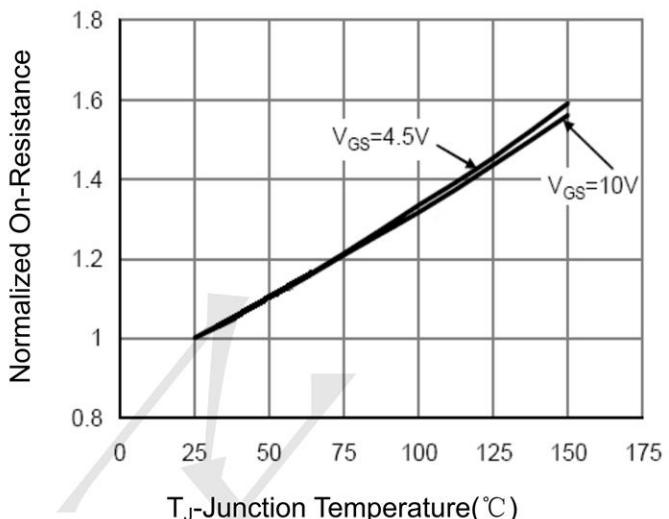


Figure 8 Drain-Source On-Resistance

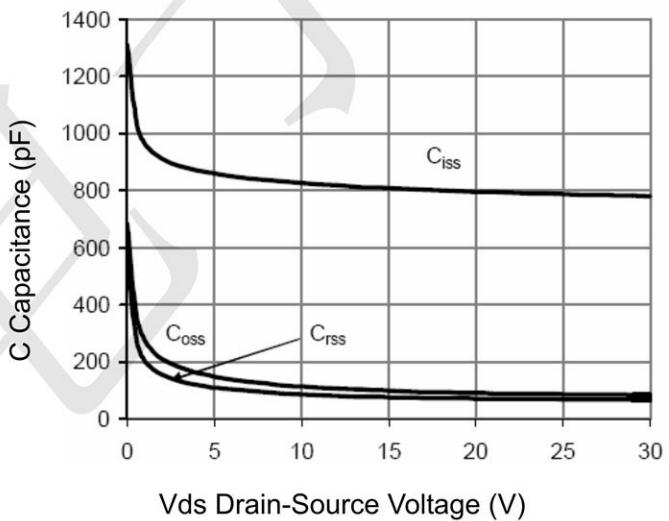


Figure 10 Capacitance

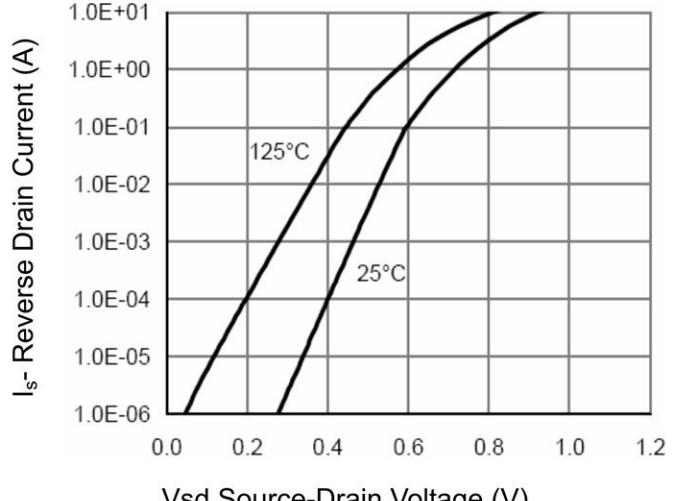


Figure 12 Source-Drain Diode Forward

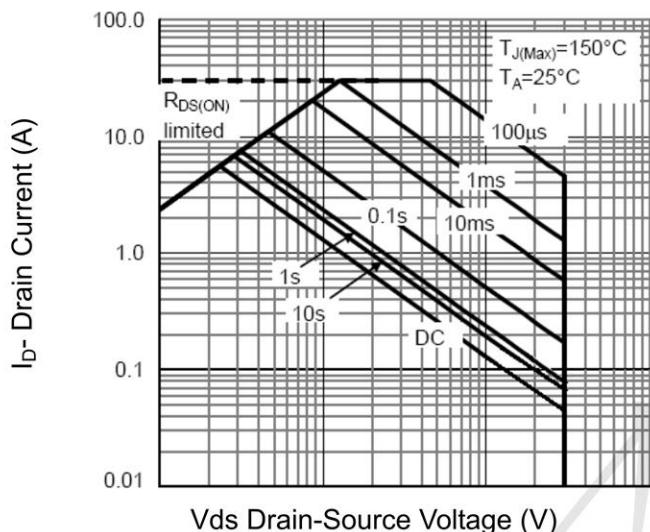


Figure 13 Safe Operation Area

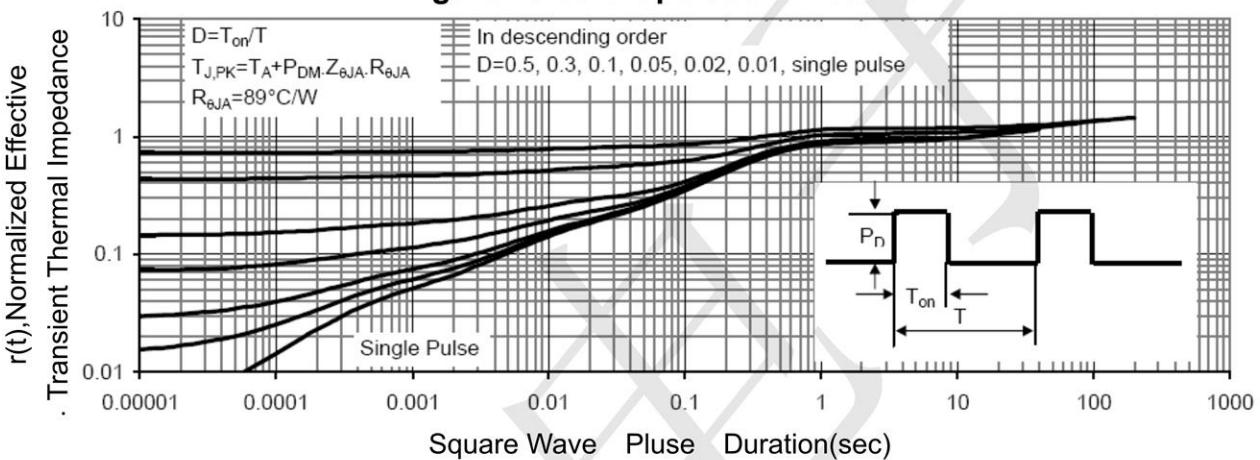
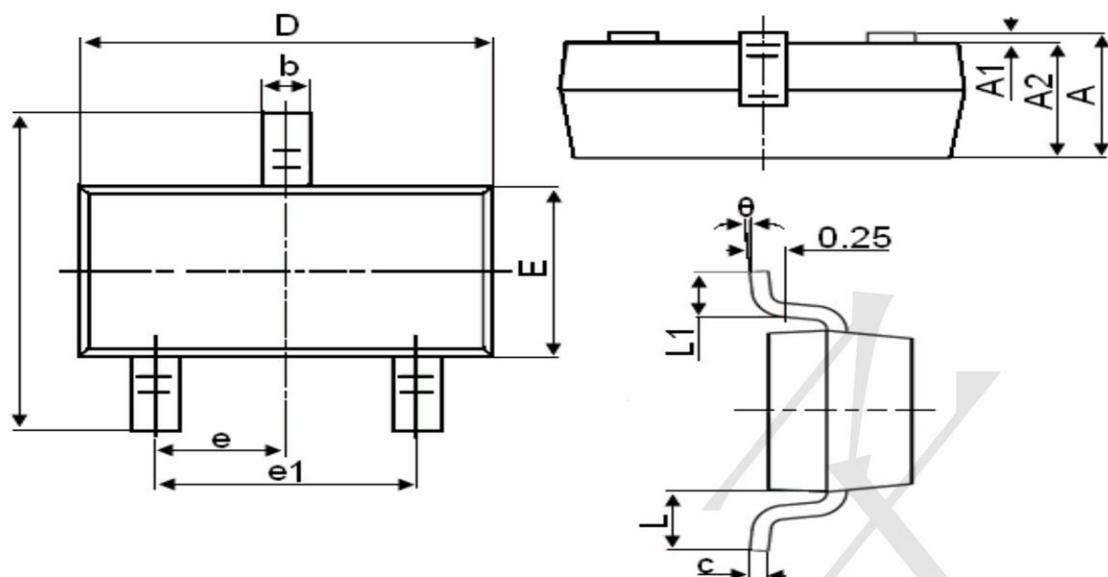


Figure 14 Normalized Maximum Transient Thermal Impedance

Package Outline Dimensions (SOT-23)



| Symbol | Dimensions in Millimeters | |
|--------|---------------------------|-------|
| | MIN. | MAX. |
| A | 0.900 | 1.150 |
| A1 | 0.000 | 0.100 |
| A2 | 0.900 | 1.050 |
| b | 0.300 | 0.500 |
| c | 0.080 | 0.150 |
| D | 2.800 | 3.000 |
| E | 1.200 | 1.400 |
| E1 | 2.250 | 2.550 |
| e | 0.950TYP | |
| e1 | 1.800 | 2.000 |
| L | 0.550REF | |
| L1 | 0.300 | 0.500 |
| θ | 0° | 8° |