



# LG-05PT4D94H-5118Y-T PHOTOTRANSISTOR

**DATA SHEET** 

 SPEC. NO.
 :
 SZ19092701

 DATE
 :
 2019/12/06

REV.  $\underline{A/4}$ 

Approved By: Checked By: Prepared By:

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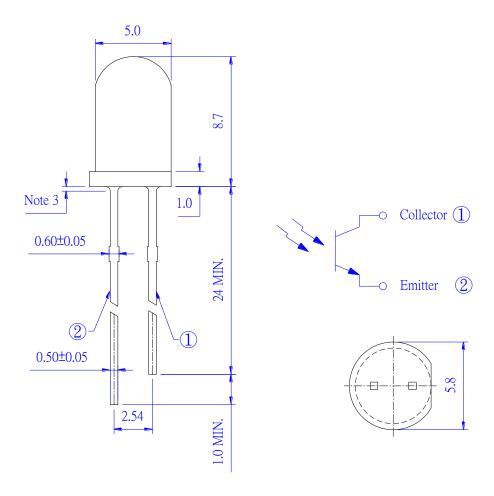




## **Features**

- ♦ Pb free product—RoHS compliant
- ♦ High Photo Sensitivity
- ♦ General purpose leads
- ♦ Reliable and rugged
- ♦ Long life solid state reliability

# Package Dimension



Part NO.	Chip Material	Lens Color
LG-05PT4D94H-5118Y-T	Silicon	Black

#### **Notes:**

- 1. All dimensions are in millimeters.
- 2. Tolerance is  $\pm 0.20$ mm unless otherwise noted.
- 3. Protruded resin under flange is 0.5mm max.
- 4. Lead spacing is measured where the leads emerge from the package.
- 5. Specifications are subject to change without notice.

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#### Absolute Maximum Ratings at Ta=25℃

Parameter	Parameter Maximum Rating		
Power Dissipation	100 mW		
Collector- Emitter Voltage	30	V	
Emitter- Collector Voltage	5	٧	
Operating Temperature	-40°℃~+85°℃		
Storage Temperature Range	-40°C~+100°C		
Lead Soldering Temperature	260°C for 5 seconds		

#### Note:

#### 1. Storage:

The storage ambient for the LEDs should not exceed  $30 \, \text{C}$  temperature or 70% relative humidity. It is recommended that LEDs out of their original packaging are used within three months.

For extended storage out of their original packaging, it is recommended that the LEDs be stored in a sealed container with appropriate desiccant or in desiccators with nitrogen ambient.

#### 2. Precautions in handling:

- When soldering, leave 2mm of minimum clearance from the resin to the soldering point.
- Dipping the resin to solder must be avoided.
- · Correcting the soldered position after soldering must be avoided.
- In soldering, do not apply any stress to the lead frame particularly when heated.
- When forming a lead, make sure not to apply any stress inside the resin.
- Lead forming must be done before soldering.
- It is necessary to cut the lead frame at normal temperature.





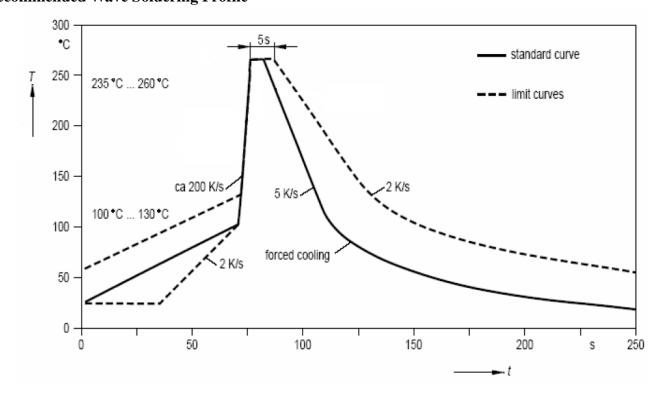
## Electrical Optical Characteristics at Ta=25°C

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Collector- Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	30			V	I <sub>C</sub> =0.1mA Ee=0mW/cm <sup>2</sup>
Emitter-Collector Breakdown Voltage	V <sub>(BR)ECO</sub>	5			<b>\</b>	I <sub>R</sub> =0.1mA Ee=0 mW/cm <sup>2</sup>
Collector- Emitter Saturation Voltage	V <sub>CE(SAT)</sub>			0.4	V	I <sub>C</sub> =2mA Ee=1.0mW/cm <sup>2</sup>
Rise Time	T <sub>r</sub>		15		$\mu$ S	Vcc=5V $R_L$ =1K $\Omega$ $I_C$ =1mA
Fall Time	T <sub>f</sub>		15		$\mu$ S	Vcc=5V R <sub>L</sub> =1K $\Omega$ I <sub>C</sub> =1mA
Viewing Angle	2θ <sub>1/2</sub>		18		Deg.	
Collector Dark Current	I <sub>CEO</sub>			100	nA	V <sub>CE</sub> =10V E <sub>e</sub> =0 mW/cm <sup>2</sup>
On State Collector Current	I <sub>C(ON)</sub>		20.0		mA	V <sub>CE</sub> =5V E <sub>e</sub> =1.0mW/cm <sup>2</sup>

#### Note:

- 1.  $2\theta_{1/2}$  is the off-axis angle at which the  $I_{C(ON)}$  is half the axial  $I_{C(ON)}$ .
- 2. The  $I_{C(ON)}$  guarantee should be added  $\pm 15\%$  tolerance.

## **Recommended Wave Soldering Profile**

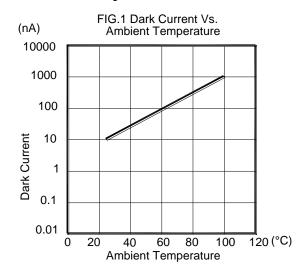






## Typical Electrical / Optical Characteristics Curves

## (25°C Ambient Temperature Unless Otherwise Noted)



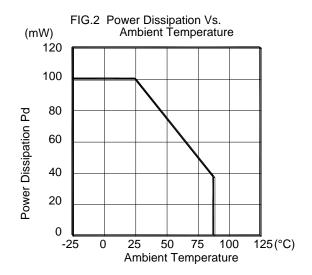


FIG.3 Rise And Fall Time Vs.

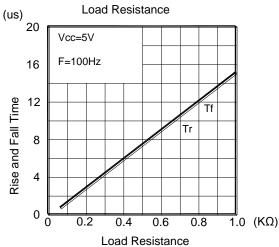


FIG.4 Light Current vs. Angular Displacement

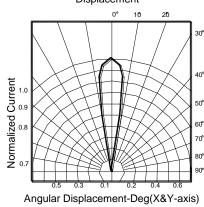
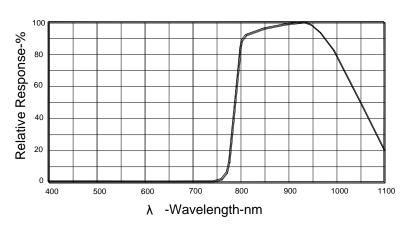


FIG.5 Relative Response vs. Wavelength



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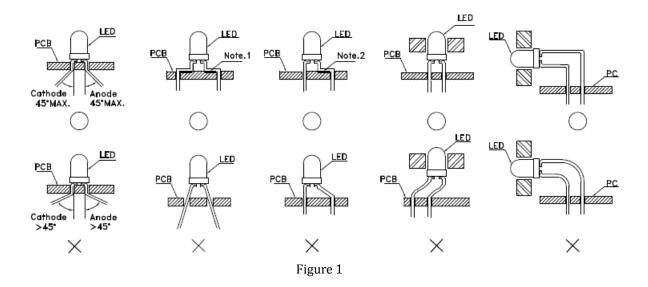






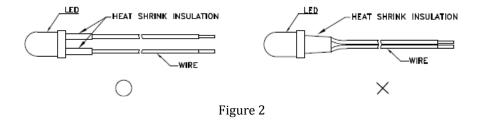
#### LED MOUNTING METHOD

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. (Fig. 1)

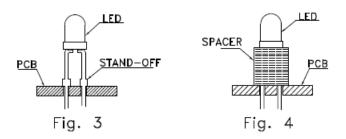


"o" Correct mounting method, "x" Incorrect mounting method, Note 1-2:Do not route PCB Trace in the contact area between the lead frame and the PCB to prevent short-circuit.

2. When soldering wire to the LED, use individual heat-shrink tubing to insulate the exposed leads to prevent accidental contact short-circuit (Fig.2)



3. Use stand-offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.



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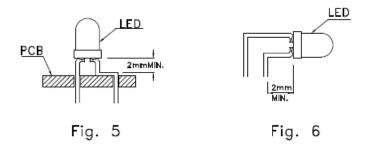




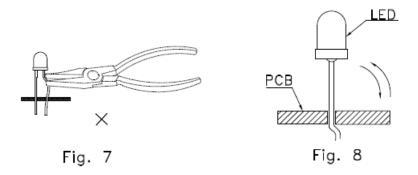


#### LEAD FORMING PROCEDURES

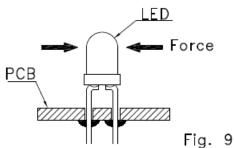
1. Maintain a minimum of 2mm clearance between the base of the LED lens and the first lead bend (Fig.5 and Fig.6).



- 2. Lead forming or bending must be performed before soldering, never during or after soldering.
- 3. Do not stress the LED lens during lead-forming in order to fractures in the lens epoxy and damage the internal structures.
- 4. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB (Fig.7).
- 5. Do not bend the leads more than twice(Fig. 8)



6. After soldering or other high-temperature assembly, allow the LED to cool down to  $50^{\circ}$ C before applying force (Fig.9).In general, avoid placing excess force on the LED to avoid damage. For any questions please consult with LIGHT representative for proper handling procedures.

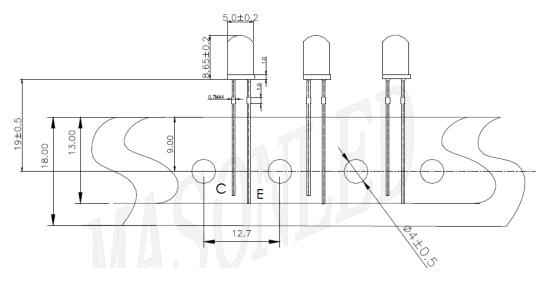


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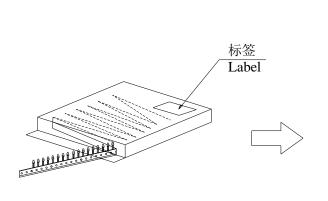




## **PACKAGE**



All dimensions are in millimeters. Tolerance is ±0.20mm unless otherwise noted.



内盒包装 Inner Cardboard Box

外包装箱 Outer Cardboard Box

包装	数量
Packaging Unit	Quantity
内包装盒 Inner Cardboard Box	2000 只/盒 2000 pcs/box
外包装箱 Outer Cardboard Box	20 盒/箱 20 boxes/box



# **Others**

The appearance and specifications of the product may be modified for improvement, without prior notice.

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# **PHOTO TRANSISTOR Diode Specification**

●Commodity: PHOTO TRANSISTOR

## ●lc(on) Bin Limits

BIN CODE	Min. (mA)	Max. (mA)
68	18.0	28.0
69	28.0	33.6

**NOTE**: The Ic(on) guarantee should be added  $\pm 15\%$  tolerance.

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