

● General Description

The AGM01T08LL combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

● Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

● Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM01T08LL	AGM01T08LL	TOLL-8L	----	----	2000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	85	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) (Note 1)	300	A
	Drain Current-Continuous(Tc=100°C)	267	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 2)	1200	A
PD	Maximum Power Dissipation(Tc=25°C)	500	w
	Maximum Power Dissipation(Tc=100°C)	200	w
EAS	Avalanche energy (Note 3)	2800	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

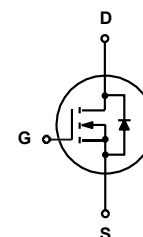
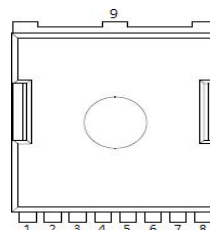
Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹	---	40	°C/W
RθJC	Thermal Resistance Junction-Case ¹	---	0.25	°C/W

Product Summary

BVDSS	RDS(ON)	ID
85V	1.2mΩ	300A

TOLL-8L Pin Configuration



Pin	Description
1	Gate(G)
2,3,4,5,6,7,8	Source(S)
9	Drain(D)

Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	85	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=70V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	2.0	--	4.0	V
gFS	Forward Transconductance	VDS=5V,ID=12A	--	--	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=50A	--	1.2	1.5	mΩ
		VGS=4.5V, ID=25A	--	--	--	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=42V,VGS=0V, F=1MHZ	--	11200	--	pF
Coss	Output Capacitance		--	2022	--	pF
Crss	Reverse Transfer Capacitance		--	472	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	--	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V,VDS=42V, ID=50A,RGEN=4.5Ω	--	40	--	nS
tr	Turn-on Rise Time		--	122	--	nS
td(off)	Turn-Off Delay Time		--	111	--	nS
tf	Turn-Off Fall Time		--	137	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=42V, ID=50A	--	240	--	nC
Qgs	Gate-Source Charge		--	56	--	nC
Qgd	Gate-Drain Charge		--	58	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	300	A
VSD	Forward on Voltage	VGS=0V,IS=50A	--	--	1.3	V
trr	Reverse Recovery Time	IF=50A , dI/dt=100A/μs , TJ=25°C	--	137	--	ns
Qrr	Reverse Recovery Charge		--	347	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

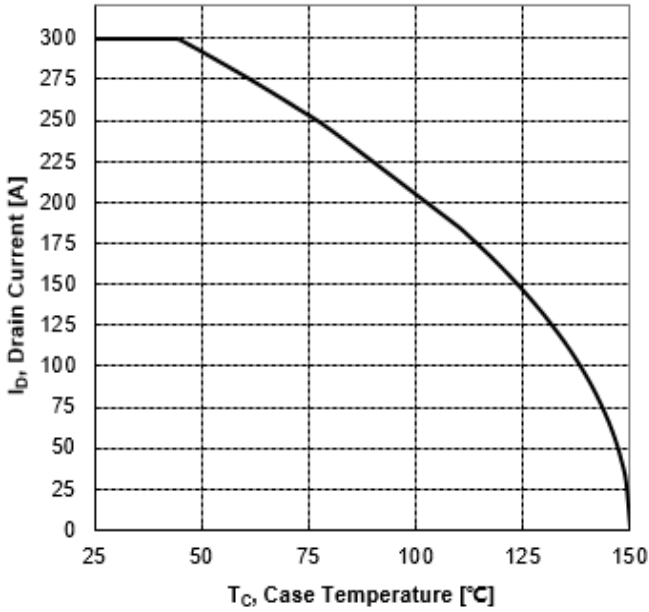
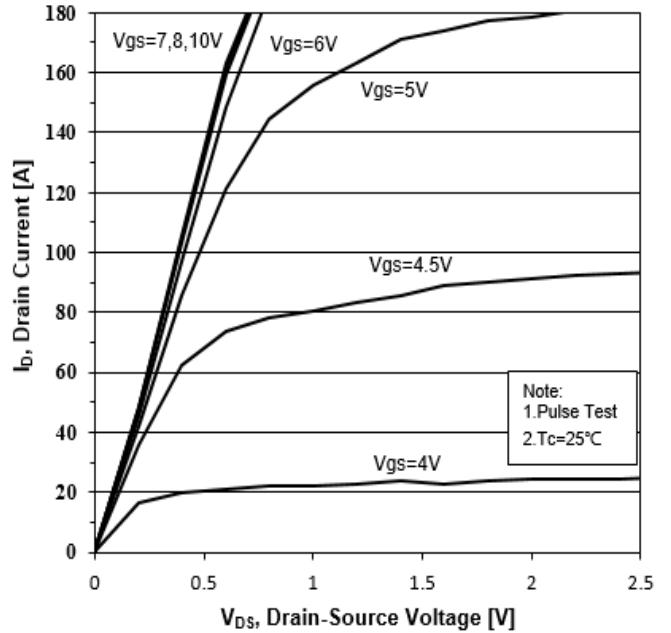
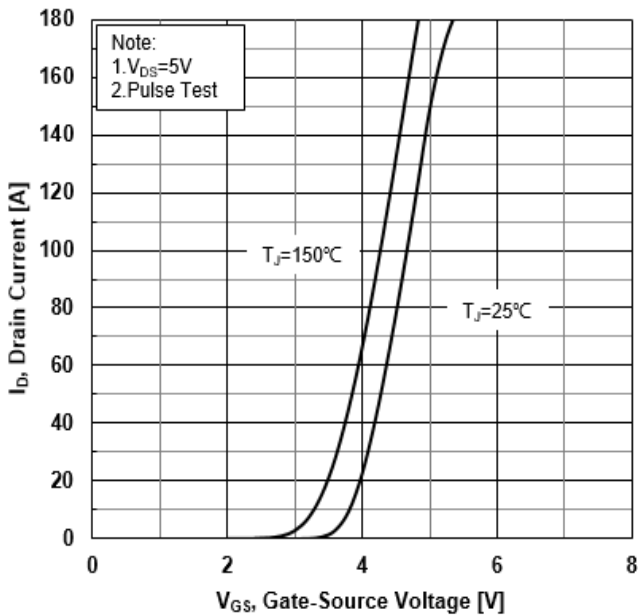
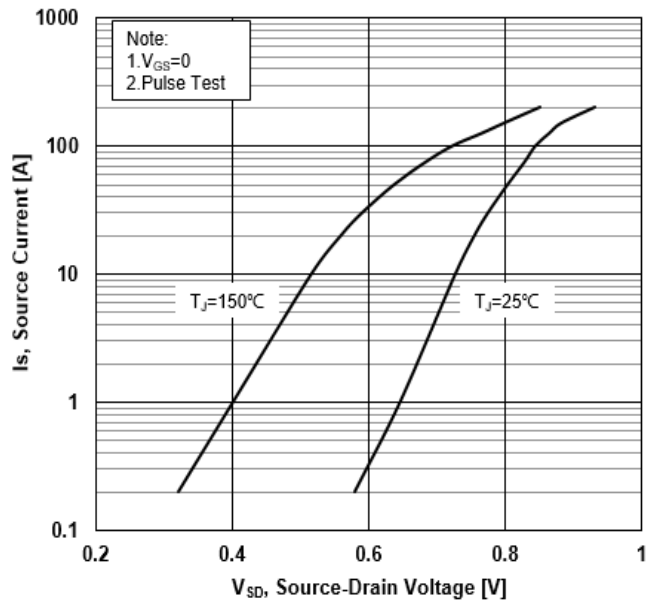
Figure 1. Maximum Continuous Drain Current vs Case Temperature

Figure 2. Typical Output Characteristics

Figure 3. Typical Transfer Characteristics

Figure 4. Source-Drain Diode Forward Characteristics


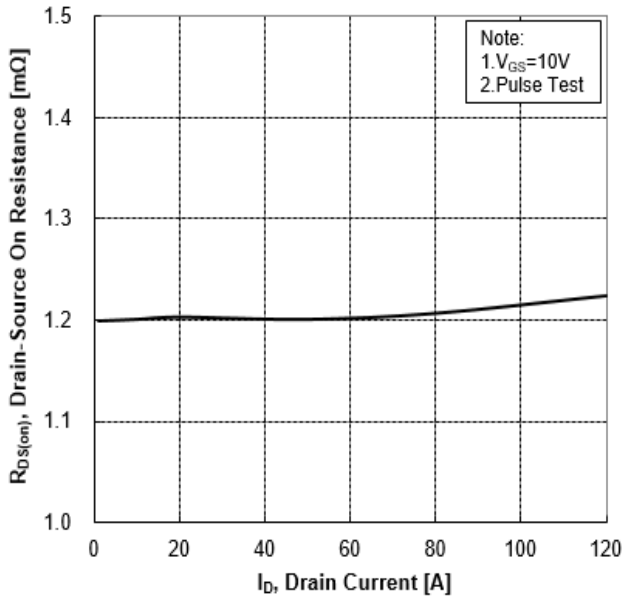
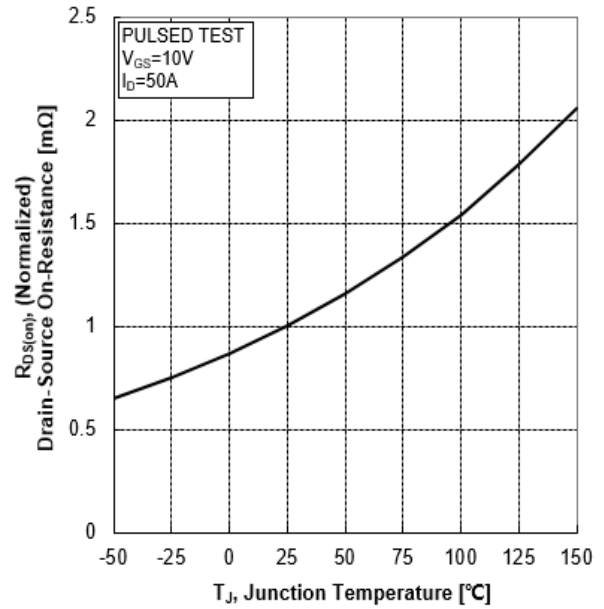
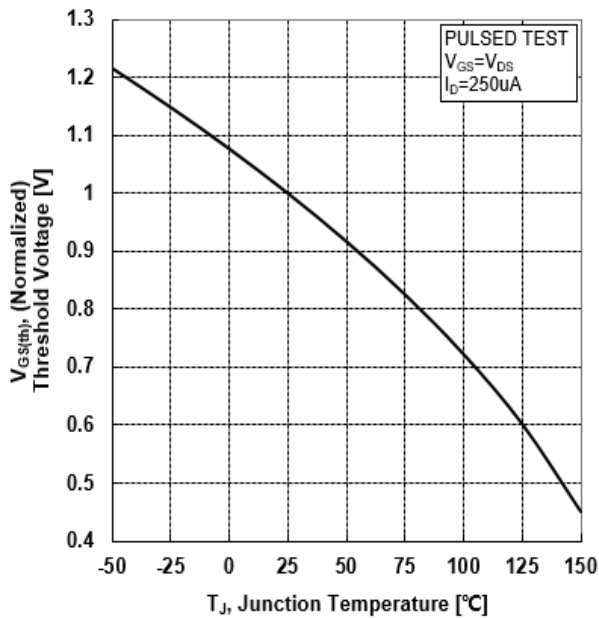
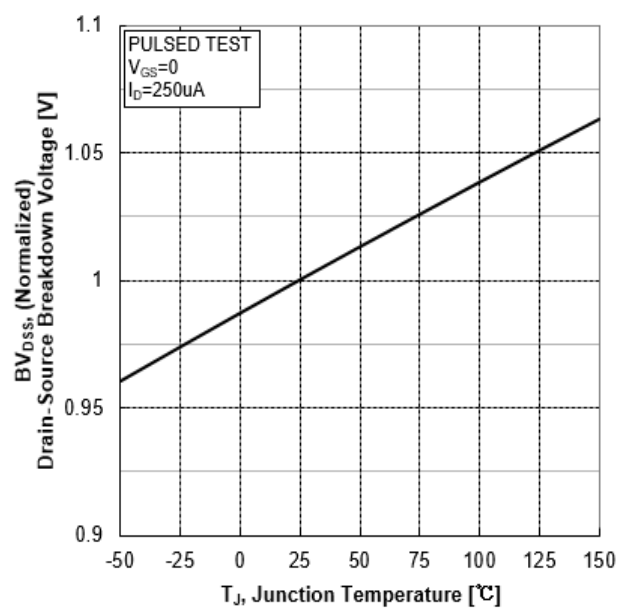
Figure 5. Drain-Source On-Resistance vs Drain Current

Figure 6. Normalized On-Resistance vs Junction Temperature

Figure 7. Normalized Threshold Voltage vs Junction Temperature

Figure 8. Normalized Breakdown Voltage vs Junction Temperature


Figure 9. Capacitance Characteristics

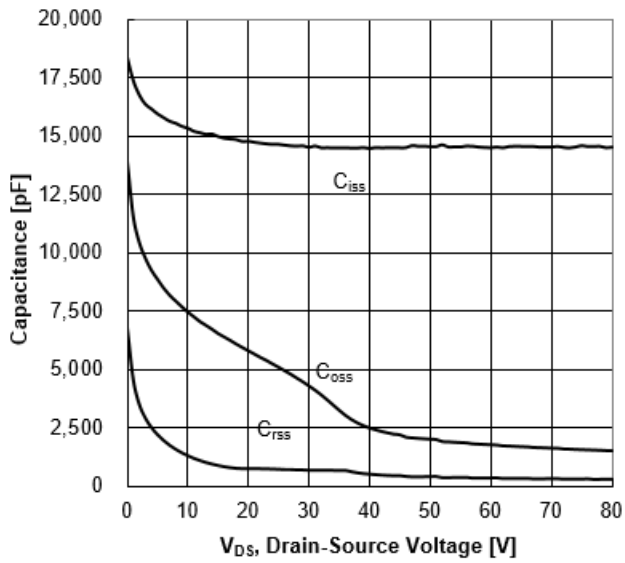


Figure 10. Typical Gate Charge vs Gate-Source Voltage

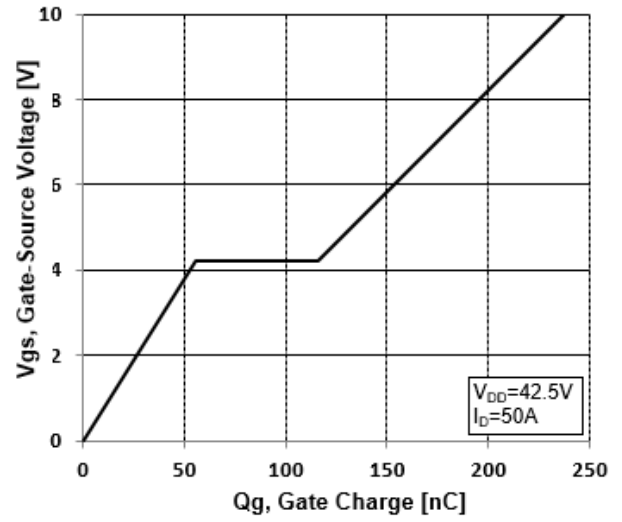


Figure 18. Diode Reverse Recovery Test Circuit

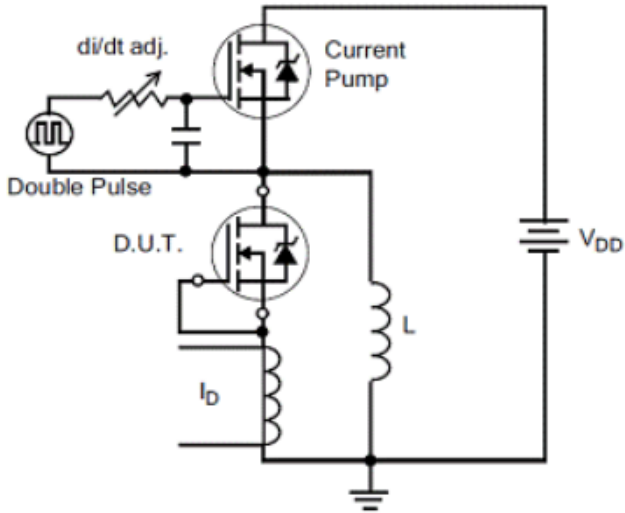


Figure 19. Diode Reverse Recovery Waveform

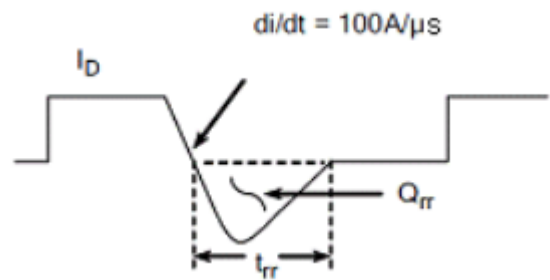


Figure 20. Unclamped Inductive Switching Test Circuit

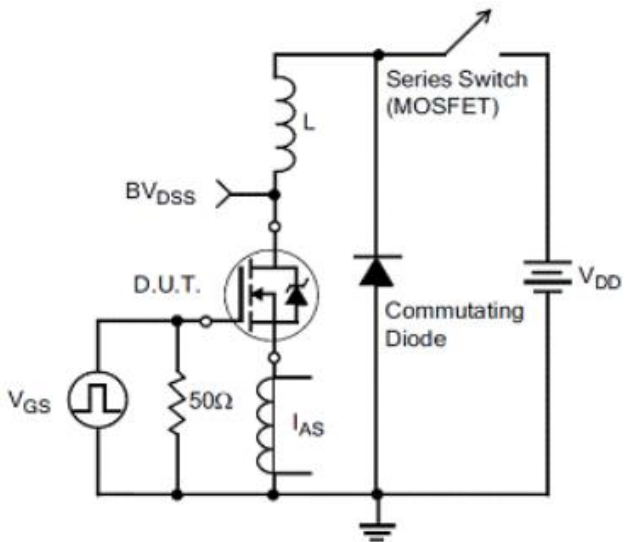
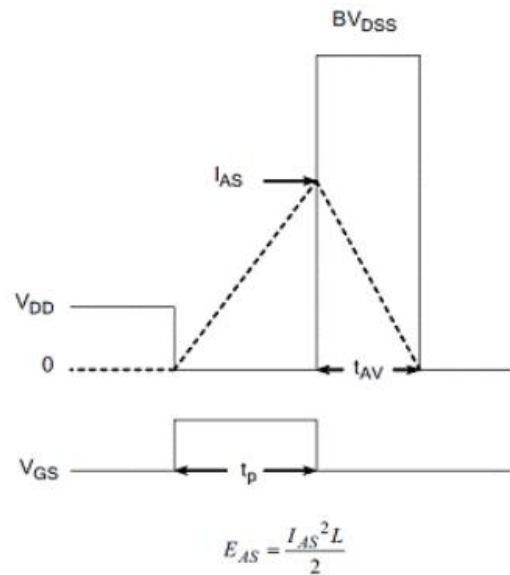
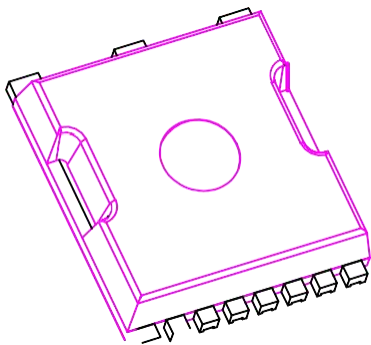
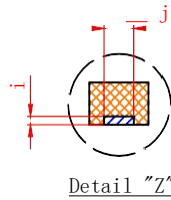
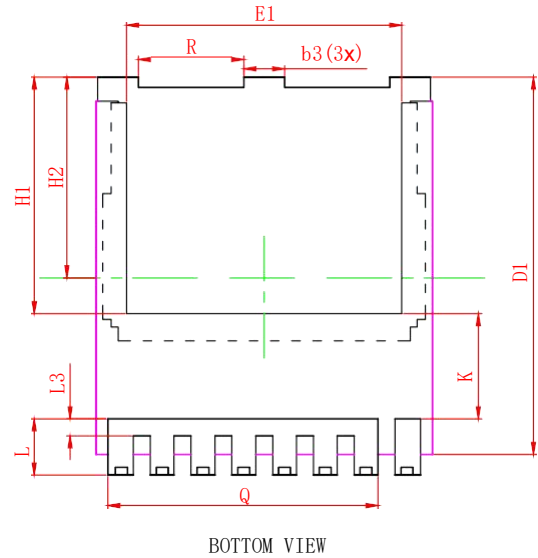
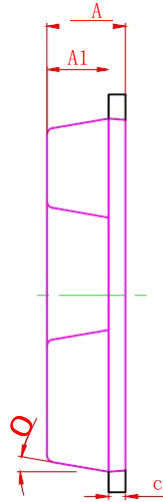
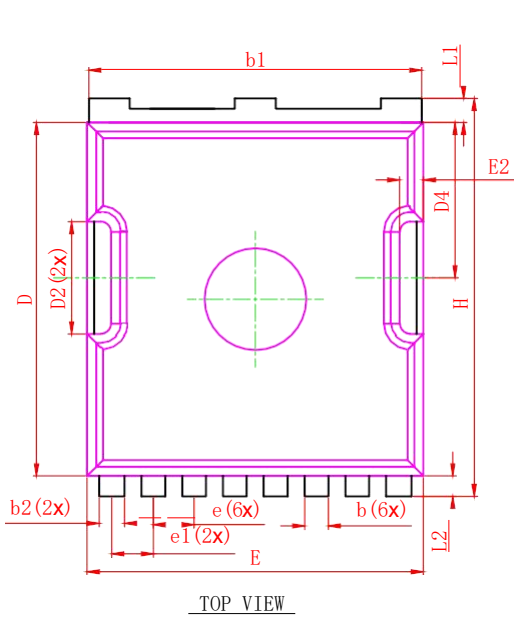


Figure 21. Unclamped Inductive Switching Waveform



Package Dimensions

TOLL-8L Package



SYMBOL	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.200	2.300	2.400
A1	1.700	1.800	1.900
b	0.600	0.700	0.800
b1	9.700	9.800	9.900
b2	0.650	0.750	0.850
b3	1.100	1.200	1.300
c	0.400	0.500	0.600
D	10.300	10.400	10.500
D1	11.000	11.100	11.200
D2	3.200	3.300	3.400
D4	4.470	4.570	4.670
E	9.800	9.900	10.000
E1	8.000	8.100	8.200
E2	0.500	0.600	0.700
e	1.200 BSC		
e1	1.225 BSC		
H	11.600	11.700	11.800
H1	6.950 BSC		
H2	5.900 BSC		
i	0.100 REF.		
j	0.350 REF.		
K	3.100 REF.		
L	1.550	1.650	1.750
L1	0.600	0.700	0.800
L2	0.500	0.600	0.700
L3	0.400	0.500	0.600
Q	7.950 REF.		
R	3.000	3.100	3.200
Ø	10°REE.		


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