

Wire Wound Type Common Mode Filter

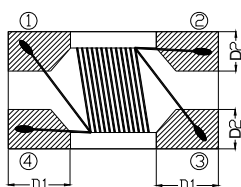
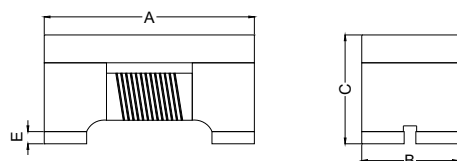
WCM2012F2SF-601T03

1. Features

1. High common mode impedance at high frequency cause excellent noise suppression performance.
2. WCM2012F2SF series realizes small size and low profile. 2.0x1.2x1.2 mm.
3. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
4. Operating temperature -40~+125°C (Including self - temperature rise)



2. Dimension



Series	A(mm)	B(mm)	C(mm)	D1(mm)	D2(mm)	E(mm)
2012F2SF	2.0±0.2	1.2±0.2	1.2±0.2	0.50±0.1	0.51±0.1	0.15±0.1

3. Part Numbering

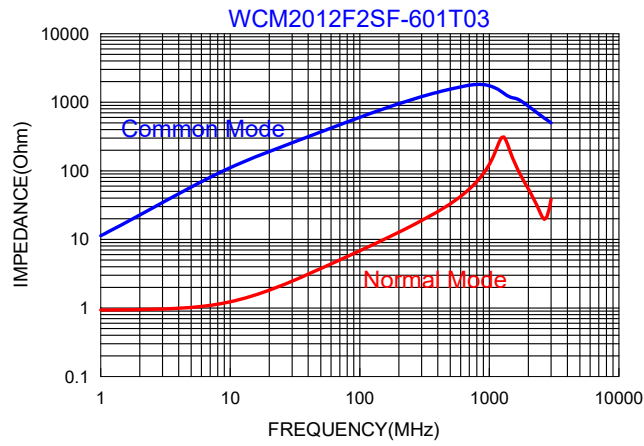
WCM	2012	F	2	S	F	-	601	T	03
A	B	C	D	E	F		G	H	I

- A: Series
 B: Dimension
 C: Material Ferrite Core
 D: Number of Lines 2=2 lines
 E: Type S=Shielded , N=Unshielded
 F: Lead free
 G: Impedance 601=600Ω
 H: Packaging T=Taping and Reel
 I: Rated Current 03=300mA

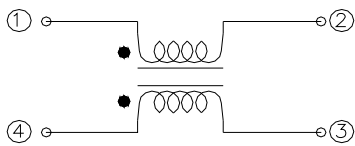
4. Specification

TAI-TECH Part Number	Common mode Impedance (Ω)	Test Frequency (MHz)	DC Resistance (Ω) max.	Rated Current (mA)max.	Rated Volt. (Vdc)max.	Withstand Volt. (Vdc) max.	IR (Ω) min.
WCM2012F2SF-601T03	600±25%	100	0.88	300	50	125	10M

Typical Impedance v.s. Frequency Curve

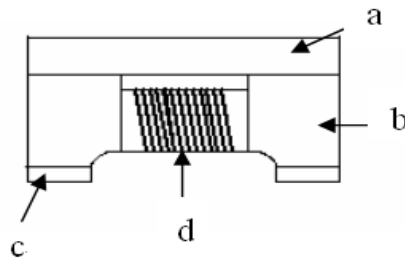


5. Schematic Diagram



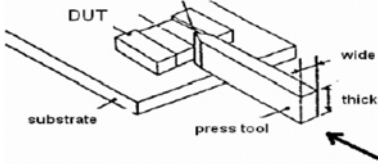
6. Materials

No.	Description	Specification
a.	Upper Plate	Ferrite
b.	Core	Ferrite Core
c.	Termination	Ag/Ni/Sn
d.	Wire	Enameled Copper Wire



7. Reliability and Test Condition

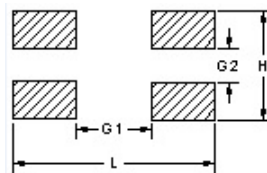
Item	Performance	Test Condition
Operating temperature	-40~+125°C (Including self - temperature rise)	
Storage temperature	-40~+125°C (on board)	
Electrical Performance Test		
Z(common mode)	Refer to standard electrical characteristics list.	Keysight E4991B + Keysight 16197A
DCR		Agilent-34420A Agilent-4338B
I.R.		Chroma 19073
Temperature Rise Test		1.Applied the allowed DC current. 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test	Appearance : No damage. Impedance : within±15% of initial value RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Temperature : 125±2°C Applied current : rated current Duration : 1000±12hrs Measured at room temperature after placing for 24 hrs.
Load Humidity		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Humidity : 85±3% R.H. Temperature : 85°C±2°C Duration : 1000hrs Min. Bead : with 100% rated current · Inductance: with 10% rated current Measured at room temperature after placing for 24 hrs.
Moisture Resistance		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs,keep at 25°C for 2 hrs then keep at -10°C for 3 hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Condition for 1 cycle Step1 : -40±2°C 30±5min Step2 : 125±2°C ≤0.5min Step3 : 125±2°C 30±5min Number of cycles : 500 Measured at room temperature after placing for 24 hrs.
Vibration		Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minute Equipment : Vibration checker Total Amplitude:10g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations).

Item	Performance	Test Condition															
Bending	Appearance : No damage. Impedance : within±15% of initial value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.															
Shock	RDC : within ±15% of initial value and shall not exceed the specification value	<table border="1" data-bbox="979 365 1414 499"> <thead> <tr> <th>Type</th> <th>Peak value (g's)</th> <th>Normal duration (D) (ms)</th> <th>Wave form</th> <th>Velocity change (V)ft/sec</th> </tr> </thead> <tbody> <tr> <td>SMD</td> <td>50</td> <td>11</td> <td>Half-sine</td> <td>11.3</td> </tr> <tr> <td>Lead</td> <td>50</td> <td>11</td> <td>Half-sine</td> <td>11.3</td> </tr> </tbody> </table> <p>3 shocks in each direction along 3 perpendicular axes. (18 shocks).</p>	Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (V)ft/sec	SMD	50	11	Half-sine	11.3	Lead	50	11	Half-sine	11.3
Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (V)ft/sec													
SMD	50	11	Half-sine	11.3													
Lead	50	11	Half-sine	11.3													
Solderability	More than 95% of the terminal electrode should be covered with solder.	a. Method B, 4 hrs @155°C dry heat @235°C±5°C Testing Time :5 +0/-0.5 seconds b. Method D category 3. (8hours ± 15 min)@ 260°C±5°C Testing Time :30 +0/-0.5 seconds															
Resistance to Soldering Heat		Depth: completely cover the termination <table border="1" data-bbox="986 674 1414 790"> <thead> <tr> <th>Temperature(°C)</th> <th>Time(s)</th> <th>Temperature ramp/immersion and emersion rate</th> <th>Number of heat cycles</th> </tr> </thead> <tbody> <tr> <td>260 ±5 (solder temp)</td> <td>10 ±1</td> <td>25mm/s ±6 mm/s</td> <td>1</td> </tr> </tbody> </table>	Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate	Number of heat cycles	260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	1							
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260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	1														
Terminal Strength	Appearance : No damage. Impedance : within±15% of initial value RDC : within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(>0805:1kg , <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. 															

8. Soldering and Mounting

8-1. Recommended PC Board Pattern

	WCM2012F2S
L(mm)	2.60
H(mm)	1.40
G1(mm)	1.25
G2(mm)	0.45



8-2. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

8-2.1 IR Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

8-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Figure 2.)

- Preheat circuit and products to 150°C
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 350°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.

Fig.1 IR Soldering Reflow

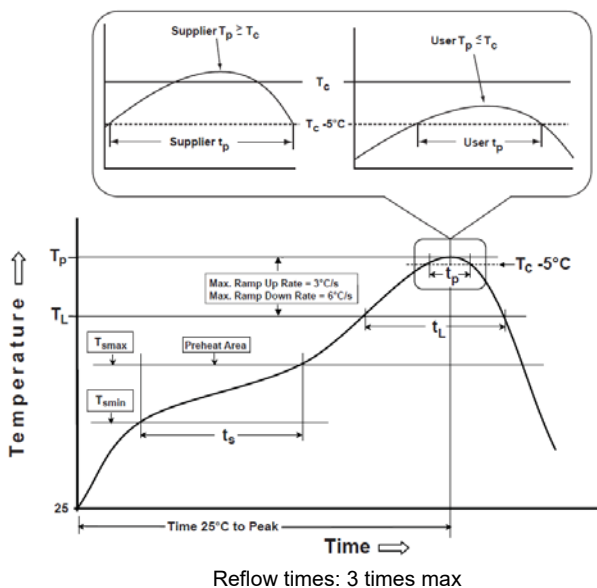


Fig.2 Iron soldering temperature profiles

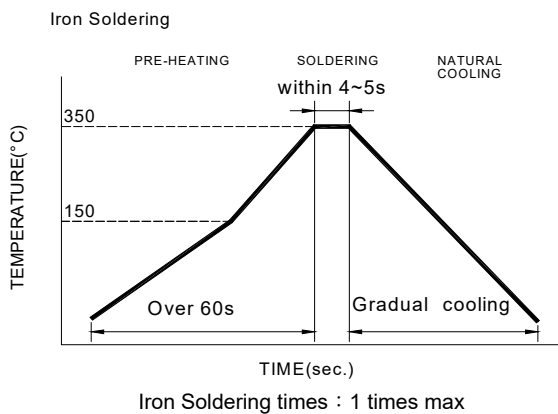


Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat -Temperature Min(T_{smin}) -Temperature Max(T_{smax}) -Time(t_s)from(T_{smin} to T_{smax})	150°C 200°C 60-120seconds
Ramp-up rate(T_L to T_p)	3°C/second max.
Liquidus temperature(T_L) Time(t_L)maintained above T_L	217°C 60-150 seconds
Classification temperature(T_c)	See Table (1.2)
Time(t_p) at $T_c - 5^\circ\text{C}$ (T_p should be equal to or less than T_c .)	< 30 seconds
Ramp-down rate(T_p to T_L)	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

T_p : maximum peak package body temperature, T_c : the classification temperature.

For user (customer) T_p should be equal to or less than T_c .

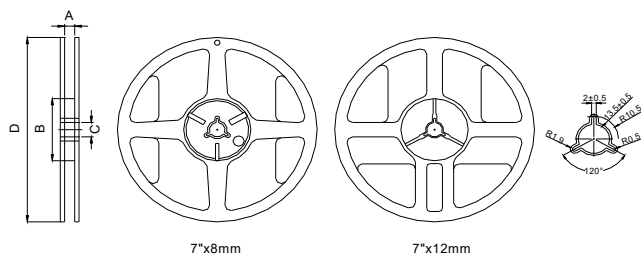
Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E ◦

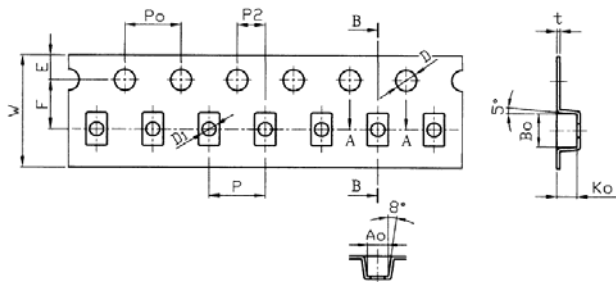
9. Packaging Information

9-1. Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9.0±0.5	60.0±2.0	13.5±0.5	178.0±2.0

9-2. Tape Dimension / 8mm

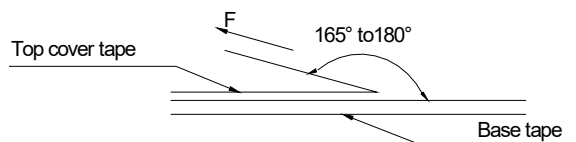


Series	W(mm)	P(mm)	E(mm)	F(mm)	P2(mm)	D(mm)	D1(mm)	P0(mm)	A0(mm)	B0(mm)	K0(mm)	t(mm)
WCM2012F2S	8.00±0.10	4.00±0.10	1.75±0.10	3.50±0.05	2.00±0.05	1.50+0.10/-0.00	1.00±0.10	4.00±0.10	1.50±0.10	2.35±0.10	1.45±0.10	0.28±0.05

9-3. Packaging Quantity

Chip size	Chip/Reel	Inner Box	Middle Box	Carton
WCM2012F2S	2000	10000	50000	100000

9-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

Application Notice

- Storage Conditions(component level)
 - To maintain the solderability of terminal electrodes:
 - 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
 - 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
 - 3. Recommended products should be used within 12 months form the time of delivery.
 - 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.