TAI-TECH KBM01-221100443

# Ferrite Chip Bead(Lead Free)

FCM1005KF-100T05

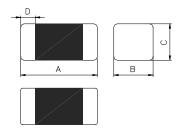
Certificate

Green Partner

## 1.Features

- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. S.M.T. type.
- 4. Suitable for reflow soldering.
- 5. Shapes and dimensions follow E.I.A. spec.
- 6. Available in various sizes.
- 7. Excellent solder ability and heat resistance.
- 8. High reliability.
- 9. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 10. Operating Temperature: -55~+125°C (Including self-temperature rise)

### 2.Dimensions



Chip Size						
A	1.00±0.10					
В	0.50±0.10					
С	0.50±0.10					
D	0.25±0.10					

Units: mm

# 3.Part Numbering



B: Dimension

C: Material

D: Impedance

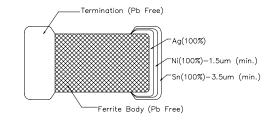
E: Packaging

Lead Free Material

100=10 $\Omega$ 

T=Taping and Reel, B=Bulk(Bags)

F: Rated Current

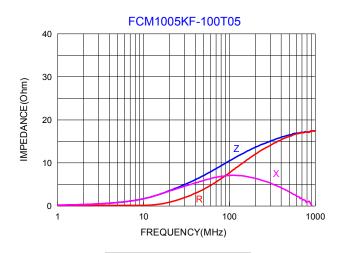


# 4. Specification

Tai-Tech Part Number	Impedance (())		DC Resistance $(\Omega)$ max.	Rated Current (mA) max.
FCM1005KF-100T05	10±25%	60mV/100M	0.20	500

- Rated current: based on temperature rise test
- In compliance with EIA 595

#### Impedance-Frequency Characteristics



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**TAI-TECH KBM01-221100443** P3.

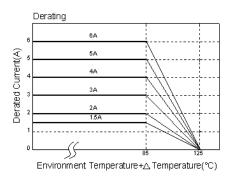
# 5. Reliability and Test Condition

Item	Item Performance							Test Condition				
Series No.	FCB	FCM	НСВ	GHB	FCA	-						
Operating Temperature		(Includ	-55∼+125°C ing self-temperat	ture rise)								
Transportation Storage Temperature			-55~+125°C (on board)			For long storage conditions, please se Application Notice						
Impedance (Z)	Pefer to stope	dard electrical ch	orgatoriation list			Agilent42 Agilent E Agilent42 Agilent16	4991 287					
DC Resistance	_ Refer to stand	iard electrical cri	iaracteristics list			Agilent 4						
Rated Current						DC Power Over Rat some risk	ed Curr		ements, the	re will be		
Temperature Rise Test		Rated Current < 1A							current. by digital su	ırface		
Life test	Appearance:	no damage.				times.( IF Reflow P Temperation: Applied of Duration:	PC/JEDI rofiles) ture: 12 current: : 1000±° d at roo	EC J-STD 5±2°C rated curr l2hrs.	ough IR refl 0-020E Clas ent. erature afte	sification		
Load Humidity		Impedance: within±15%of initial value.  RDC: within ±15% of initial value and shall not exceed the specification value						Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Humidity: 85±2%R.H. Temperature: 85±2°C. Duration:1000hrsMin.Bead:with100%ratedcurr ent Inductance: with 10% rated current Measured at room temperature after placing				
Thermal shock	Appearance: no damage.  Impedance: within±15% of initial value.  RDC: within ±15% of initial value and shall not exceed the specification value.					Number of cycles: 500 Measured at room temperature after placing						
Vibration		within±15% of in		ot exceed the spe	cification value	times.( IF Reflow P Oscillatio for 20 min Equipme Total Amp Testing T	for 24±2 hrs.  Preconditioning: Run through IR reflow for times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz ~ 2KHz ~ 10H for 20 minutes Equipment: Vibration checker Total Amplitude:10g Testing Time: 12 hours(20 minutes, 12 cycle each of 3 orientations) °					
Bending		within±10% of in		ot exceed the spe	ecification value	Shall be mounted on a FR4 substrate of the following dimensions:  >=0805inch(2012mm):40x100x1.2mm  <0805inch(2012mm):40x100x0.8mm  Bending depth:  >=0805inch(2012mm):1.2mm  <0805inch(2012mm):0.8mm  Duration of 10 sec for a min.						
						Test cor	ndition:	Normal		Velocity		
Shock		within±10% of in		ot exceed the spe	ecification value	Type	Value (g's) 50	duration (D) (ms)	Wave form Half-sine	change (Vi)ft/sec 11.3		
						Lead	50	11	Half-sine	11.3		
Solderability	More than 95%	% of the terminal	electrode should	d be covered with	n solder.	@235°C: b. Metho ± 15 min)	±5°C Te d D cat )@ 260°	st time:5 - egory 3. (	@155°C d +0/-0.5 seco (steam agin	onds.		

Item	Performance	Test Condition			
		Number of heat cycles: 1			
Resistance to Soldering	Appearance : No damage.	Temperature (°C) Time (s) Temperature ramp/immersion and emersion rate			
Heat	Impedance: within±15% of initial value RDC: within ±15% of initial value and shall not exceed the specification value	260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s			
		Depth: completely cover the termination			
Terminal strength	Appearance: No damage. Impedance: within±15% of initial value RDC: within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Component mounted on a PCB apply a force >0805inch(2012mm):1kg <=0805inch(2012mm):0.5kg to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shock the component being tested.			

#### \*\*Derating Curve

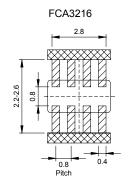
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over  $85^{\circ}\mathrm{C}$ , the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



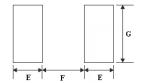
# 6. Soldering and Mounting

#### 6-1. Recommended PC Board Pattern

	Chip Size							Land Patterns For Reflow Soldering		
Series	s Type A(mm) B(mm) C(mm) D(mm)					E(mm)	F(mm)	G(mm)		
FCB	<mark>1005</mark>	1.0±0.10	0.50±0.10	<mark>0.50±0.10</mark>	0.25±0.10	<mark>0.50</mark>	<mark>0.40</mark>	<mark>0.60</mark>		
FCM	1608	1.6±0.15	0.80±0.15	0.80±0.15	0.30±0.20	0.80	0.85	0.95		
HCB	2012	2.0±0.20	1.25±0.20	0.85±0.20	0.50±0.30	1.05	1.00	1.45		
GHB	2012	2.0±0.20	1.25±0.20	1.25±0.20	0.50±0.30	1.05		1.45		
FCI	3216	3.2±0.20	1.60±0.20	1.10±0.20	0.50±0.30	1.05	2.20	1.80		
FHI	3225	3.2±0.20	2.50±0.20	1.30±0.20	0.50±0.30	1.05	2.20	2.70		
FCH	4516	4.5±0.20	1.60±0.20	1.60±0.20	0.50±0.30	1.05	3.30	1.80		
HCI	4532	4.5±0.20	3.20±0.20	1.50±0.20	0.50±0.30	1.05	3.30	3.40		



Land
Solder Resist



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

# 6-2. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

**TAI-TECH KBM01-221100443** P5.

#### 6-2.1 IR Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

#### 6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Figure 2.)

- Preheat circuit and products to 150℃
- Never contact the ceramic with the iron tip1.0mm tip diameter (max)
- · Use a 20 watt soldering iron with tip diameter of 1.0mm
- · Limit soldering time to 4~5sec.

Fig.1 IR Soldering Reflow

• 350°C tip temperature (max)

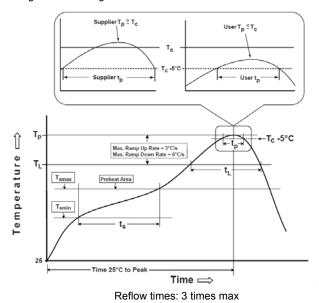
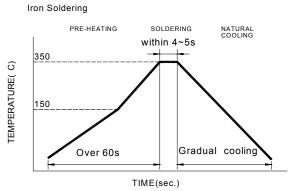


Fig.2 Iron soldering temperature profiles



Iron Soldering times: 1 times max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat -Temperature Min(T <sub>smin</sub> ) -Temperature Max(T <sub>smax</sub> ) -Time(t <sub>s</sub> )from(T <sub>smin</sub> to T <sub>smax</sub> )	150°C 200°C 60-120seconds
Ramp-up rate( $T_L$ to $T_p$ )	3°C/second max.
$\label{eq:Liquidus} \begin{array}{c} \text{Liquidus temperature}(T_L) \\ \text{Time}(t_L) \text{maintained above } T_L \end{array}$	217℃ 60-150 seconds
Classification temperature(T <sub>c</sub> )	See Table (1.2)
$\label{eq:total_final} \mbox{Time}(t_p) \mbox{ at Tc-} \mbox{ 5^{\circ}\!$	< 30 seconds
Ramp-down rate(T <sub>p</sub> to T <sub>L</sub> )	6°C /second max.
Time 25℃ to peak temperature	8 minutes max.

Tp: maximum peak package body temperature, Tc: the classification temperature.

For user (customer)  ${\bf Tp}$  should be equal to or less than  ${\bf Tc.}$ 

Table (1.2) Package Thickness/Volume and Classification Temperature (Tc)

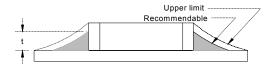
	Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
	<1.6mm	260°C	260°C	260°C
PB-Free Assembly	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E  $\,^{\circ}$ 

#### 6-2.3 Solder Volume:

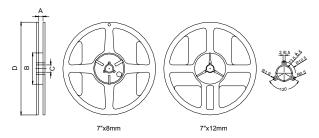
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



# 7.Packaging Information

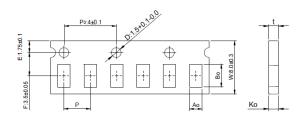
## 7-1. Reel Dimension



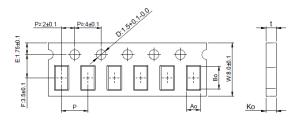
7	Type A(mm)		B(mm)	C(mm)	D(mm)	
7"	'x8mm	<mark>9.0±0.5</mark>	<mark>60±2</mark>	<mark>13.5±0.5</mark>	<mark>178±2</mark>	
7"	x12mm	13.5±0.5	60±2	13.5±0.5	178±2	

#### 7-2.1 Tape Dimension / 8mm

### ■Material of taping is paper

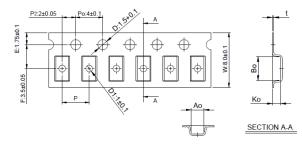


Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
<mark>100505</mark>	<mark>1.12±0.03</mark>	<mark>0.62±0.03</mark>	<mark>0.60±0.03</mark>	<mark>2.0±0.05</mark>	<mark>0.60±0.03</mark>



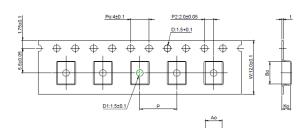
Size Bo(mm)		Ao(mm)	Ko(mm)	P(mm)	t(mm)
160808	1.80±0.05	0.96+0.05/-0.03	0.95±0.05	4.0±0.10	0.95±0.05
201209	2.10±0.05	1.30±0.05	0.95±0.05	4.0±0.10	0.95±0.05

## ■Material of taping is plastic



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
201212	2.10±0.10	1.28±0.10	1.28±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321611	3.35±0.10	1.75±0.10	1.25±0.10	4.0±0.10	0.23±0.05	1.0±0.10
322513	3.42±0.10	2.77±0.10	1.55±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321609	3.40±0.10	1.77±0.10	1.04±0.10	4.0±0.10	0.22±0.05	1.0±0.10

## 7-2.2 Tape Dimension / 12mm



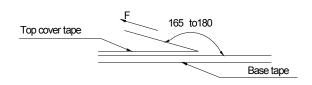
Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
451616	4.70±0.10	1.75±0.10	1.75±0.10	4.0±0.10	0.24±0.05	1.5±0.10
453215	4.70±0.10	3.45±0.10	1.60±0.10	8.0±0.10	0.24±0.05	1.5±0.10

TAI-TECH KBM01-221100443

#### 7-3. Packaging Quantity

Chip Size	453215	451616	322513	321611	321609	201212	201209	160808	<mark>100505</mark>
Chip / Reel	1000	2000	2500	3000	3000	2000	4000	4000	<mark>10000</mark>
Inner box	4000	8000	12500	15000	15000	10000	20000	20000	<mark>50000</mark>
Middle box	20000	40000	62500	75000	75000	50000	100000	100000	<mark>250000</mark>
Carton	40000	80000	125000	150000	150000	100000	200000	200000	<mark>500000</mark>

#### 7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed	
(℃)	(%)	(hPa)	mm/min	
5~35	45~85	860~1060	300	

#### **Application Notice**

Storage Conditions(component level)

To maintain the solder ability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 3. Recommended products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
  - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
  - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
  - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.