

CMOS LDO Regulator Series for Portable Equipments Versatile Package FULL CMOS LDO Regulator



BUxxTD3WG series

General Description

BUxxTD3WG series is high-performance FULL CMOS regulator with 200-mA output, which is mounted on versatile package SSOP5 (2.9 mm \times 2.8 mm \times 1.25 mm). It has excellent noise characteristics and load responsiveness characteristics despite its low circuit current consumption of 35µA. It is most appropriate for various applications such as power supplies for logic IC, RF, and camera modules.ROHM's.

Features

- High accuracy detection
- low current consumption
- Compatible with small ceramic capacitor(Cin=Co=0.47uF)
- With built-in output discharge circuit
- High ripple rejection
- ON/OFF control of output voltage
- With built-in over current protection circuit and thermal shutdown circuit
- Package SSOP5 is similar to SOT-23-5 (JEDEC)
- Low dropout voltage

Typical Application Circuit

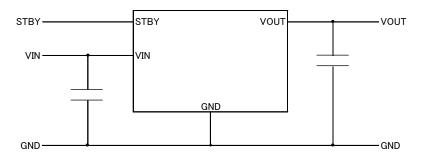


Fig.1 Application Circuit

Key Specifications

Output voltage:	1.0V to 3.4V
Accuracy output voltage:	±1.0% (±25mV)
Low current consumption:	35µA

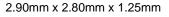
■ Operating temperature range: -40°C to +85°C

Applications

Battery-powered portable equipment, etc.

Package

SSOP5:

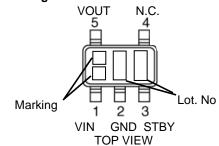




OProduct structure: Silicon monolithic integrated circuit OThis product is not designed protection against radioactive rays.

Connection Diagram

SSOP5



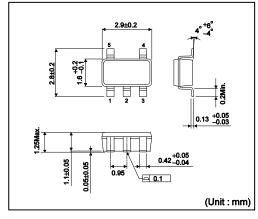
Pin Descriptions

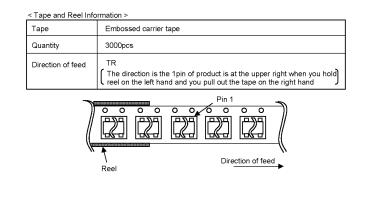
	SSOP5						
PIN No.	Symbol	Function					
1	VIN	Power Supply Voltage					
2	GND	Grouding					
3	STBY	ON/OFF control of output voltage (High: ON, Low: OFF)					
4	N.C.	Unconnected Terminal					
5	VOUT	Output Voltage					

Ordering Information

В	U	Х	Х	Т	D	3	W	G	-	Х	Т	R
Part Number		Output Volt 10 : 1.0V ↓ 34 : 3.4V	tage	Series Maximum 200mA	I Output Cu	rrent	with output discharge	Package G : SSOP5		Halogen Free G : compatible Blank : incompatible	Packageing and forr Embossed tape and TR : The pin numbe	

SSOP5





●Lineup

Marking	F0	L6	F1	M0	L5	F2	F3	F4	F5
Output Voltage	1.0V	1.1V	1.2V	1.25V	1.3V	1.5V	1.8V	1.85V	1.9V
Part Number	BU10	BU11	BU12	BU1C	BU13	BU15	BU18	BU1J	BU19
		n			n	n			
	F6	F7	F8	F9	G0	G1	G2	G3	G4
	2.0V	2.1V	2.5V	2.6V	2.7V	2.8V	2.85V	2.9V	3.0V
	BU20	BU21	BU25	BU26	BU27	BU28	BU2J	BU29	BU30
		I			1				
	G5	G6	G7	G8					
	3.1V	3.2V	3.3V	3.4V					

●Absolute Maximum Ratings (Ta=25°C)

BU31

PARAMETER	Symbol	Limit	Unit
Power Supply Voltage	VMAX	-0.3 ~ +6.5	V
Power Dissipation	Pd	540(*1)	mW
Maximum junction temperature	TjMAX	+125	°C
Operating Temperature Range	Topr	-40 ~ +85	°C
Storage Temperature Range	Tstg	-55 ~ +125	°C

BU34

(*1)Pd deleted at 5.4mW/°C at temperatures above Ta=25°C, mounted on 70x70x1.6 mm glass-epoxy PCB.

• RECOMMENDED OPERATING RANGE (not to exceed Pd)

BU32

BU33

PARAMETER	Symbol	Limit	Unit
Power Supply Voltage	VIN	1.7~6.0	V
Maximum Output Current	IMAX	200	mA

OPERATING CONDITIONS

PARAMETER	Symbol	MIN.	TYP.	MAX.	Unit	CONDITION	
Input Capacitor	Cin	0. 22 (*2)	0. 47	-	μF	Ceramic capacitor recommended	
Output Capacitor	Со	0. 22 (*2)	0. 47	-	μF		

(*2) Make sure that the output capacitor value is not kept lower than this specified level across a variety of temperature, DC bias, changing as time progresses characteristic.

•Electrical Characteristics

(Ta=25°C, VIN=VOUT+1.0V (*3), STBY=VIN, Cin=0.47 μ F, Co=0.47 μ F, unless otherwise noted.)

PARAMETER		0 1 1		Limit			
PARAMETER		Symbol	MIN.	TYP.	MAX.	Unit	Conditions
Overall Device			•				•
		VOUT	VOUT × 0.99	VOUT × 0.99		v	IOUT=10 µ A, VOUT≧2.5V
Output Voltage		VOUT	VOUT-25mV	VOUT	VOUT+25mV	V	IOUT=10 μ A, VOUT < 2.5V
Operating Current		IIN	-	35	60	μA	IOUT=0mA
Operating Current (STBY)	ISTBY	_	-	1.0	μA	STBY=0V
Ripple Rejection Ratio		RR	45	70	-	dB	VRR=-20dBv, fRR=1kHz, IOUT=10mA
			-	800	1100	mV	1.0V≦VOUT<1.2V(IOUT=200mA)
			-	600	900	mV	1.2V≦VOUT<1.5V(IOUT=200mA)
			-	440	700	mV	1.5V≦VOUT<1.8V(IOUT=200mA)
		VSAT	-	380	600	mV	1.8V≦VOUT<2.5V(IOUT=200mA)
Dropout Voltage			-	280	540	mV	2.5V≦VOUT≦2.6V(IOUT=200mA)
			-	260	500	mV	2.7V≦VOUT≦2.85V(IOUT=200mA)
			-	240	460	mV	2.9V≦VOUT≦3.1V(IOUT=200mA)
			-	220	420	mV	3.2V≦VOUT≦3.4V(IOUT=200mA)
Line Regulation		VDL	-	2	20	mV	VIN=VOUT+1.0V to 5.5V (*4), IOUT=10 µ A
Load Regulation		VDLO	-	10	80	mV	IOUT=0.01mA to 100mA
Over Current Protec	tion (OCP)					
Limit Current		ILMAX	220	400	700	mA	Vo=VOUT*0.95
Short Current		ISHORT	20	70	150	mA	Vo=0V
Standby Block							
Discharge Resistor		RDSC	20	50	80	Ω	VIN=4.0V, STBY=0V, VOUT=4.0V
STBY Pin Pull-down Current		ISTB	0.1	0.6	2.0	μA	STBY=1.5V
STBY Control Voltage	ON	VSTBH	1.2	-	6.0	V	
	OFF	VSTBL	-0.3	-	0.3	V	

This product is not designed for protection against radioactive rays.

(*3) VIN=2.5V for VOUT≦1.5V

(*4) VIN=2.5V to 3.6V for VOUT ${\leq}\,1.5V$

Block Diagrams

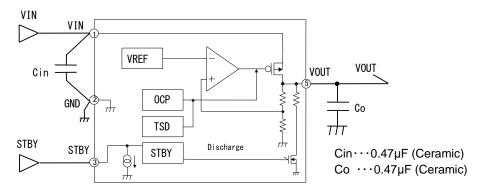


Fig. 2 Block Diagrams

•Reference data BU18TD3WG (Ta=25°C unless otherwise specified.)

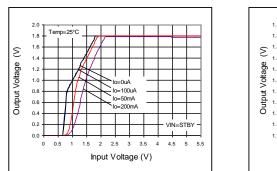


Fig 3. Output Voltage

1.85 VIN=STB Temp=25 1.84 1.83 1.82 1.81 1.80 1.79 1.78 lo=100u/ 1.77 lo=50mA 1.76 lo=200mA 1.75 2.2 2.3 2.4 1.7 1.8 1.9 2.1 2. Input Voltage (V)

Fig 4. Line Regulation

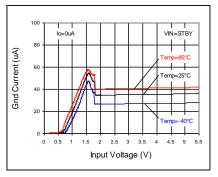


Fig 5. Circuit Current IGND

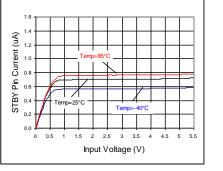


Fig 6. VSTBY - ISTBY

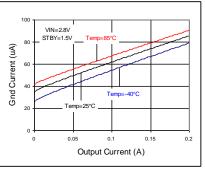


Fig 7. IOUT - IGND

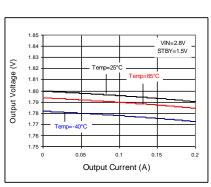


Fig 8. Load Regulation

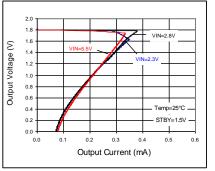


Fig 9. OCP Threshold

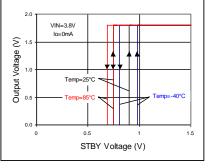


Fig 10. STBY Threshold

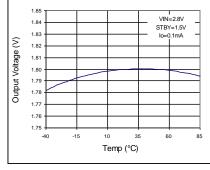
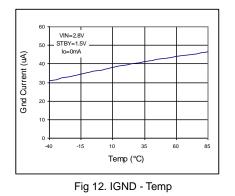


Fig 11. VOUT - Temp



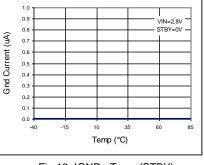
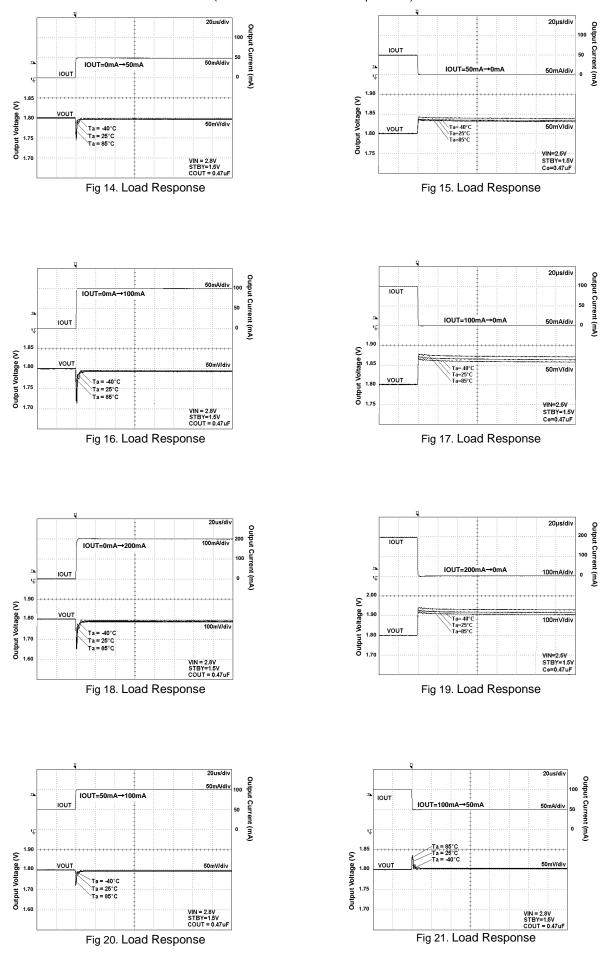
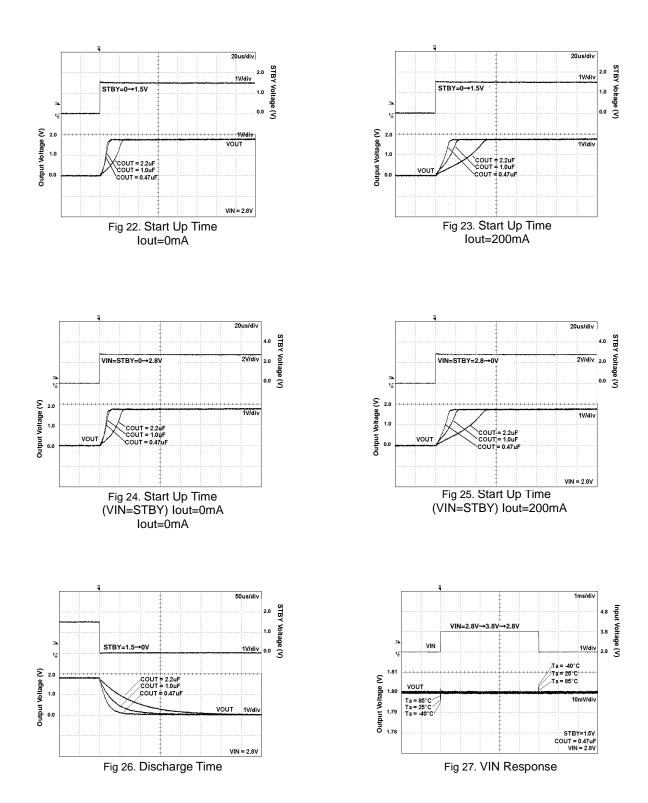


Fig 13. IGND - Temp (STBY)

•Reference data **BU18TD3WG** (Ta=25°C unless otherwise specified.)



•Reference data BU18TD3WG (Ta=25°C unless otherwise specified.)



BUxxTD3WG series

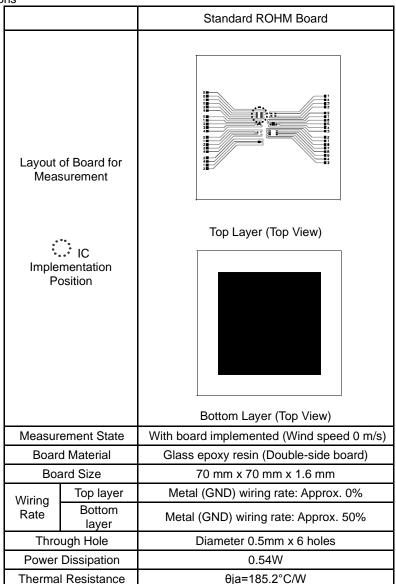
About power dissipation (Pd)

As for power dissipation, an approximate estimate of the heat reduction characteristics and internal power consumption of IC are shown, so please use these for reference. Since power dissipation changes substantially depending on the implementation conditions (board size, board thickness, metal wiring rate, number of layers and through holes, etc.), it is recommended to measure Pd on a set board. Exceeding the power dissipation of IC may lead to deterioration of the original IC performance, such as causing operation of the thermal shutdown circuit or reduction in current capability. Therefore, be sure to prepare sufficient margin within power dissipation for usage.

Calculation of the maximum internal power consumption of IC (PMAX)

PMAX=(VIN-VOUT)×IOUT(MAX.) (VIN: Input voltage VOUT: Output voltage IOUT(MAX): Maximum output current)

○ Measurement conditions



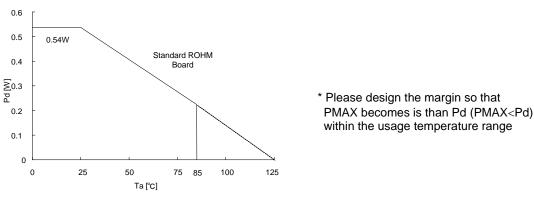


Fig. 28 SSOP5 Power dissipation heat reduction characteristics (Reference)

Operation Notes

1.) Absolute maximum ratings

Use of the IC in excess of absolute maximum ratings (such as the input voltage or operating temperature range) may result in damage to the IC. Assumptions should not be made regarding the state of the IC (e.g., short mode or open mode) when such damage is suffered. If operational values are expected to exceed the maximum ratings for the device, consider adding protective circuitry (such as fuses) to eliminate the risk of damaging the IC.

2.) GND potential

The potential of the GND pin must be the minimum potential in the system in all operating conditions.

Never connect a potential lower than GND to any pin, even if only transiently.

3.) Thermal design

Use a thermal design that allows for a sufficient margin for that package power dissipation rating (Pd) under actual operating conditions

4.) Inter-pin shorts and mounting errors

Use caution when orienting and positioning the IC for mounting on printed circuit boards. Improper mounting or shorts between pins may result in damage to the IC.

5.) Operation in strong electromagnetic fields

Strong electromagnetic fields may cause the IC to malfunction. Caution should be exercised in applications where strong electromagnetic fields may be present.

6.) Common impedance

Wiring traces should be as short and wide as possible to minimize common impedance. Bypass capacitors should be use to keep ripple to a minimum.

7.) Voltage of STBY pin

To enable standby mode for all channels, set the STBY pin to 0.3 V or less, and for normal operation, to 1.2 V or more. Setting STBY to a voltage between 0.3 and 1.2 V may cause malfunction and should be avoided. Keep transition time between high and low (or vice versa) to a minimum.

Additionally, if STBY is shorted to VIN, the IC will switch to standby mode and disable the output discharge circuit, causing a temporary voltage to remain on the output pin. If the IC is switched on again while this voltage is present, overshoot may occur on the output. Therefore, in applications where these pins are shorted, the output should always be completely discharged before turning the IC on.

8.) Over-current protection circuit (OCP)

This IC features an integrated over-current and short-protection circuitry on the output to prevent destruction of the IC when the output is shorted. The OCP circuitry is designed only to protect the IC from irregular conditions (such as motor output shorts) and is not designed to be used as an active security device for the application. Therefore, applications should not be designed under the assumption that this circuitry will engage.

9.) Thermal shutdown circuit (TSD)

This IC also features a thermal shutdown circuit that is designed to turn the output off when the junction temperature of the IC exceeds about 150°C. This feature is intended to protect the IC only in the event of thermal overload and is not designed to guarantee operation or act as an active security device for the application. Therefore, applications should not be designed under the assumption that this circuitry will engage.

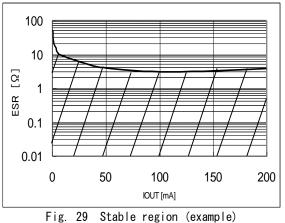
10.) Input/output capacitor

Capacitors must be connected between the input/output pins and GND for stable operation, and should be physically mounted as close to the IC pins as possible. The input capacitor helps to counteract increases in power supply impedance, and increases stability in applications with long or winding power supply traces. The output capacitance value is directly related to the overall stability and transient response of the regulator, and should be set to the largest runstable regionor the application to increase these characteristics. During design, keep in mind that in general, ceramic capacitors have a wide range of tolerances, temperature coefficients and DC bias characteristics, and that their capacitance values tend to decrease over time. Confirm these details before choosing appropriate capacitors for your application. (Please refer the technical note, regarding ceramic capacitor of recommendation)

11.) About the equivalent series resistance (ESR) of a ceramic capacitor

Capacitors generally have ESR (equivalent series resistance) and it operates stably in the ESR-IOUT area shown on the right. Since ceramic capacitors, tantalum capacitors, electrolytic capacitors, etc. generally have different ESR, please check the ESR of the capacitor to be used and use it within the stability area range shown in the right graph for evaluation of the actual application.

Cout=0.47 μ F, Cin=0.47 μ F, Temp=+25°C

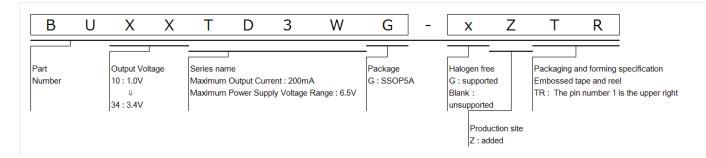


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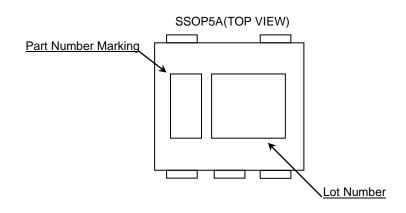
Revision History

Date	Revision	Changes
7.Feb.2013	001	New Release
30.Jul.2013	002	Adding a Revision History. VSBYH is changed.
21.Aug.2013	003	ELECTRICAL CHARACTERISTICS of each Output Voltage is removed. Adding dropout voltage.
04.Dec.2020	004	p.10-2,p.10-3,Updated Packages and Part Numbers

Ordering Information



Marking Diagram



Part Number	Output Voltage [V]	Part Number Marking
BU10TD3WG	1.0	F0
BU11TD3WG	1.1	L6
BU12TD3WG	1.2	F1
BU1CTD3WG	1.25	MO
BU13TD3WG	1.3	L5
BU15TD3WG	1.5	F2
BU18TD3WG	1.8	F3
BU1JTD3WG	1.85	F4
BU19TD3WG	1.9	F5
BU20TD3WG	2.0	F6
BU21TD3WG	2.1	F7
BU25TD3WG	2.5	F8
BU26TD3WG	2.6	F9
BU27TD3WG	2.7	G0
BU28TD3WG	2.8	G1
BU2JTD3WG	2.85	G2
BU29TD3WG	2.9	G3
BU30TD3WG	3.0	G4
BU31TD3WG	3.1	G5
BU32TD3WG	3.2	G6
BU33TD3WG	3.3	G7
BU34TD3WG	3.4	G8

Physical Dimension and Packing Information Package Name SSOP5A 2. 9 ± 0.2 $4^{\circ} \pm 4^{\circ}$ 5 01 8±0. 6±0. 3 5MIN ci 0 1 2 3 0.14 ± 0.06 S 45MAX $15\pm0.$ H H 00 0. 1±0. 0.95 0.4 ± 0.1 (UNIT:mm) PKG:SSOP5A □ 0. 1 S Drawing No. EX001-0084-1 < Tape and Reel Information > Таре Embossed carrier tape 3000pcs Quantity TR Direction of feed The direction is the 1pin of product is at the upper right when you hold ${\sf L}$ reel on the left hand and you pull out the tape on the right hand Ο 0 Ο 0 Ο Ο Ο 0 Ο 0 0 Ο E2 TR E2 TR E2 TR E2 TR E2 TR E2 TR ΤL E1 ΤL ÷ E1 ΤL E1 ΤL E1 ΤL E1 ΤL E1 Direction of feed Pocket Quadrants Reel

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(Note1) Medical Equipment Classification of the Specific Applications

JÁPAN	USA	EU	CHINA	
CLASSⅢ	CLASSⅢ	CLASS II b		
CLASSⅣ	CLASSII	CLASSⅢ	CLASSII	

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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

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ROHM Semiconductor:

BU10TD3WG-TRBU12TD3WG-TRBU15TD3WG-TRBU19TD3WG-TRBU1JTD3WG-TRBU21TD3WG-TRBU25TD3WG-TRBU26TD3WG-TRBU28TD3WG-TRBU29TD3WG-TRBU2JTD3WG-TRBU30TD3WG-TRBU32TD3WG-TRBU33TD3WG-TRBU34TD3WG-TRBU20TD3WG-TRBU31TD3WG-TRBU18TD3WG-TRBU1CTD3WG-TRBU27TD3WG-TRBU13TD3WG-GTRBU11TD3WG-GTRBU11TD3WG-GTR