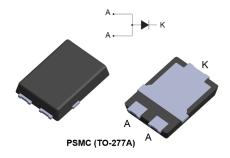


200 V, 10 A power Schottky rectifier



Features

- Low profile design 1.1 mm package typical height
- · Wettable flanks for automatic visual inspection
- · Very low conduction losses
- · High junction temperature capability
- ECOPACK2 compliant

Applications

- AC/DC converter, as for LED lighting applications
- DC/DC converter, especially for server stand by power supply or telecom
- · Secondary rectification
- DC / DC converter
- · Auxiliary Power supply
- Freewheeling function
- · Reverse battery protection

Description

This 10 A, 200 V Schottky diode is suitable for power supply, especially for lighting power, server or telecom.

Packaged in PSMC (TO-277A), STPS10200SF provides a high level of performance in a compact and flat package which can withstand high operating junction temperature.

Product status link	
STPS10200SF	

Product summary			
Symbol Value			
I _{F(AV)}	10 A		
V _{RRM}	200 V		
T _j (max.)	175 °C		
V _F (typ.)	0.660 V		



1 Characteristics

Table 1. Absolute ratings (limiting values at 25 °C, unless otherwise specified, anode terminals short-circuited)

Symbol	Parameter	Value	Unit
V _{RRM}	Repetitive peak reverse voltage	200	V
I _{F(AV)}	Average forward current, δ = 0.5 square wave	10	А
I _{FSM}	Surge non repetitive forward current	210	Α
T _{stg}	Storage temperature range	-65 to +175	°C
T _j	Maximum operating junction temperature ⁽¹⁾	+175	°C

^{1.} $(dP_{tot}/dT_j) < (1/R_{th(j-a)})$ condition to avoid thermal runaway for a diode on its own heatsink.

Table 2. Thermal resistance parameters

Symbol	Parameter	Typ. value	Unit
R _{th(j-c)}	Junction to case	0.84	°C/W

For more information, please refer to the following application note:

AN5088: Rectifiers thermal management, handling and mounting recommendations

Table 3. Static electrical characteristics (anode terminals short-circuited)

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
1 (1)	Davoras lagicare gurrent	T _j = 25 °C	$V_R = V_{RRM}$	-		6	μA
'R'	I _R ⁽¹⁾ Reverse leakage current	T _j = 125 °C		-	1.5	4	mA
		T _j = 25 °C	I _F = 5 A	-		0.830	
V _F ⁽²⁾ Forward voltage drop	Forward valtage drap	T _j = 125 °C		-	0.600	0.665	.,
	Forward voltage drop	T _j = 25 °C	I _E = 10 A	-		0.895	V
	Tj	T _j = 125 °C	IF - IU A	-	0.660	0.730	

- 1. Pulse test: $t_p = 5$ ms, $\delta < 2\%$
- 2. Pulse test: $t_p = 380 \ \mu s, \ \delta < 2\%$

To evaluate the conduction losses, use the following equation:

$$P = 0.60 \times I_{F(AV)} + 0.013 \times I_{F^{2}(RMS)}$$

For more information, please refer to the following application notes related to the power losses:

- AN604: Calculation of conduction losses in a power rectifier
- AN4021: Calculation of reverse losses in a power diode

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1.1 Characteristics (curves)

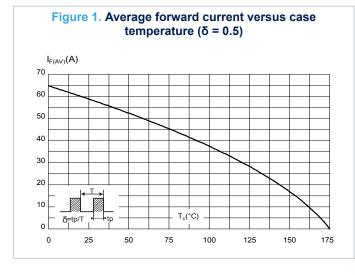


Figure 2. Relative variation of thermal impedance junction to case versus pulse duration $Z_{th(j-c)}/R_{th(j-c)}$ 1.0 0.9 0.8 0.7 0.6 0.5 0.4 0.3 0.2 0.1 0.0 1.E-04 1.E-03 1.E-02 1.E-01 1.E+00

Figure 3. Reverse leakage current versus reverse voltage applied (typical values)

I_R(μA)

1.E+04

1.E+03

1.E+01

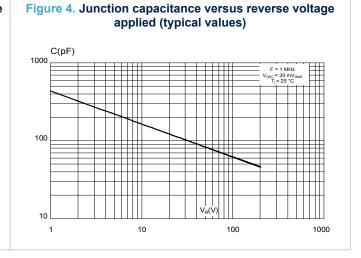
1.E+01

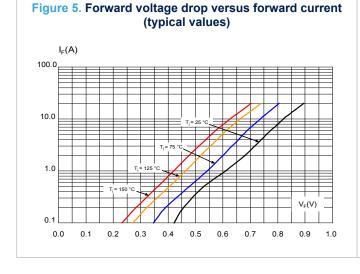
1.E+01

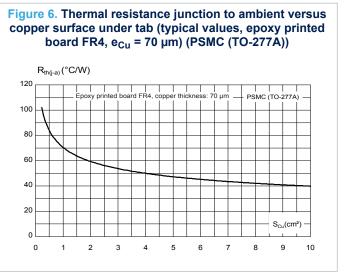
1.E+01

1.E-02

0 20 40 60 80 100 120 140 160 180 200







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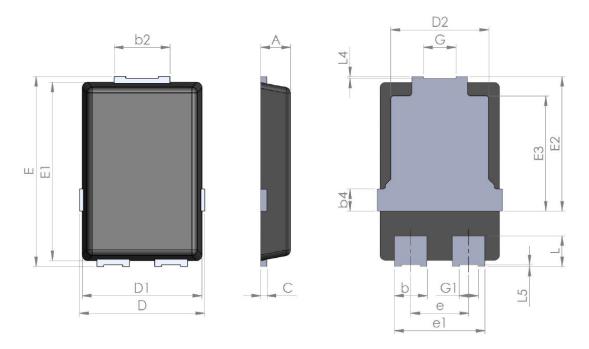
Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

2.1 PSMC (TO-277A) package information

- Epoxy meets UL94,V0
- Cooling method : by conduction (C)

Figure 7. PSMC (TO-277A) package outline



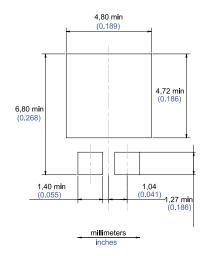
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Table 4. PSMC (TO-277A) package mechanical data

			Dime	nsions		
Ref.		Millimeters		Incl	nes (for reference o	only)
	Min.	Тур.	Max.	Min.	Тур.	Max.
А	1.00	1.10	1.20	0.039	0.043	0.047
b	1.05	1.20	1.35	0.041	0.047	0.053
b2	1.90	2.05	2.20	0.075	0.081	0.087
b4		0.75			0.029	
С	0.15	0.23	0.40	0.006	0.009	0.016
D	4.45	4.60	4.75	0.175	0.181	0.187
D1	4.25	4.40	4.45	0.167	0.173	0.175
D2	3.40	3.60	3.70	0.134	0.142	0.146
E	6.35	6.50	6.65	0.250	0.256	0.262
E1	6.05	6.10	6.15	0.238	0.240	0.242
E2	4.50	4.60	4.70	0.177	0.181	0.185
E3		3.94			1.55	
е		2.13			0.084	
e1		3.33			0.131	
G		1.20			0.047	
G1		0.70			0.027	
L	0.90	1.05	1.24	0.035	0.041	0.049
L4	0.02			0.0008		
L5	0.02			0.0008		

Figure 8. PSMC (TO-277A) package footprint in mm (in inches)



Note: For package and tape orientation, reel and inner box dimensions and tape outline please check TN1173

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3 Ordering information

Table 5. Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
STPS10200SF	PS10200	PSMC (TO-277A)	90 mg	6000	Tape and Reel

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Revision history

Table 6. Document revision history

Date	Version	Changes
01-Dec-2020	1	Initial release.

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