

# P-Channel 12-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$ $I_{D}$				
	0.0050 at $V_{GS} = -4.5 \text{ V}$	- 16			
- 12	0.0065 at V <sub>GS</sub> = - 2.5 V	- 15			
	0.0100 at V <sub>GS</sub> = - 1.8 V	- 13			

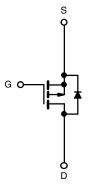
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



#### **APPLICATIONS**

- · Load Switch
- · Battery Switch



P-Channel MOSFET

	SO-8		
S 1 S 2 S 3 G 4		8 7 6 5	D D D
	Top View	J	

ABSOLUTE MAXIMUM RATINGS	T <sub>A</sub> = 25 °C, unles	ss otherwise n	oted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 12		V	
Gate-Source Voltage		V <sub>GS</sub>	± 8			
Outliness Duis Outlines (T. 450.00)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 16	- 10		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 11.5	- 8	<b>A</b>	
Pulsed Drain Current		I <sub>DM</sub>	- 50		Α	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 2.7	- 1.36		
W	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	3.0	1.5	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		1.9	0.95		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manipana lugation to Applicati	t ≤ 10 s	$R_{thJA}$	33	42	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	□thJA	70	84	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	16	21	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



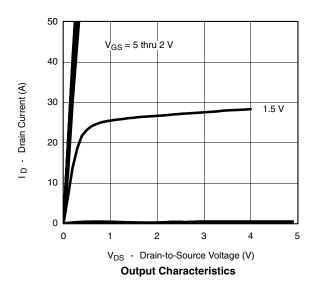
Parameter	Symbol	Test Conditions	Тур.	ур. Мах.			
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -600 \mu\text{A}$ - 0.5 - 1		1.0	٧		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	± 100		± 100	nA	
7. 0.1 1/1. 10. 1	I <sub>DSS</sub>	V <sub>DS</sub> = - 12 V, V <sub>GS</sub> = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current		$V_{DS} = -12 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 4.5 V	- 30			Α	
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 14 A		0.0050			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 13 A		0.0065		Ω	
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 12 A		0.0100			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 6 V, I <sub>D</sub> = - 14 A	80			S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 2.7 A, V <sub>GS</sub> = 0 V		- 0.6	- 1.1	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			110	165		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -6 \text{ V}, V_{GS} = -5 \text{ V}, I_{D} = -14 \text{ A}$		15		nC	
Gate-Drain Charge	$Q_{gd}$			27.5		1	
Turn-On Delay Time	t <sub>d(on)</sub>			110	170		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 6 V, $R_L$ = 6 $\Omega$		235	350		
Turn-Off Delay Time		$\text{I}_\text{D}\cong$ - 1 A, $\text{V}_\text{GEN}$ = - 4.5 V, $\text{R}_\text{g}$ = 6 $\Omega$		410	620	ns	
Fall Time	t <sub>f</sub>			285	430		
Gate Resistance	$R_{g}$			3.6		Ω	
Source-Drain Reverse Recovery Time t <sub>rr</sub>		I <sub>F</sub> = - 2.1 A, dl/dt = 100 A/μs		180	270	ns	

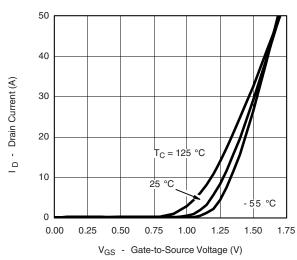
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C unless otherwise noted

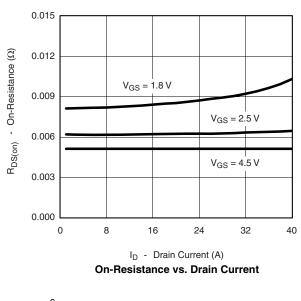


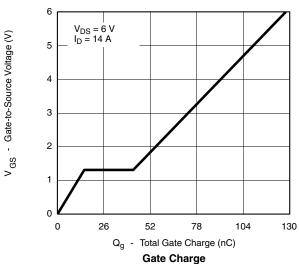


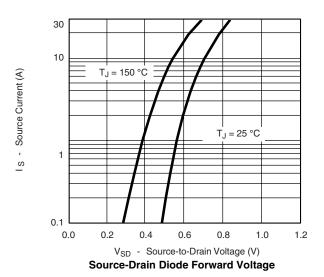
**Transfer Characteristics** 

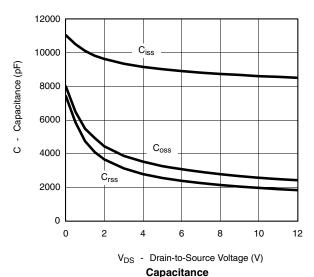


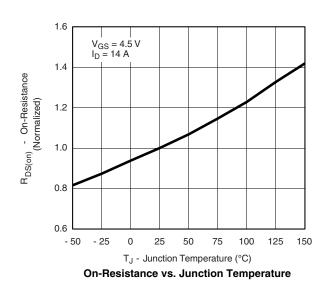
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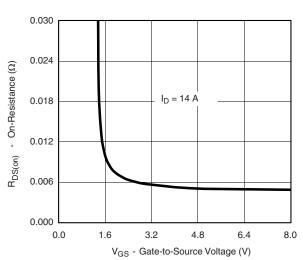






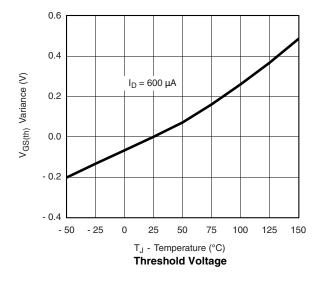


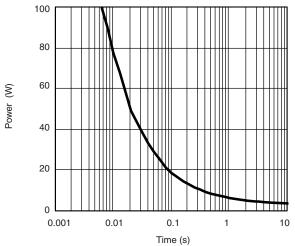




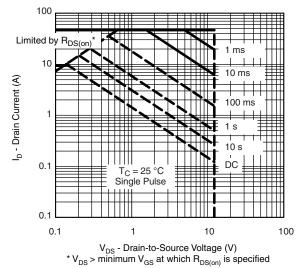


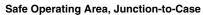
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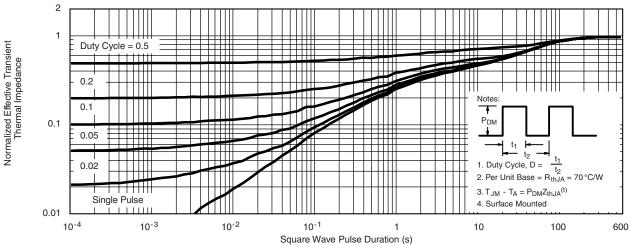




Single Pulse Power, Junction-to-Ambient



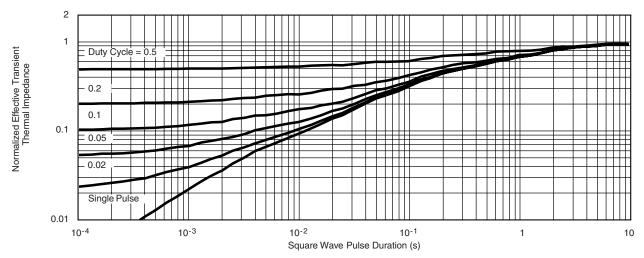




Normalized Thermal Transient Impedance, Junction-to-Ambient



### TYPICAL CHARACTERISTICS 25 °C unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot



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