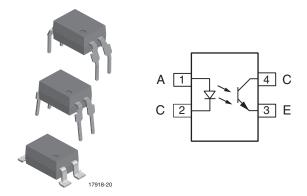
# SFH615A



Vishay Semiconductors

# Optocoupler, Phototransistor Output, High Reliability, 5300 $V_{RMS}$



#### DESCRIPTION

The SFH615A feature a variety of transfer ratios, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 package.

The coupling devices are designed for signal transmission between two electrically separated circuits.

The couplers are end-stackable with 2.54 mm lead spacing. Creepage and clearance distances of > 8 mm are achieved with option 6. This version complies with IEC 60950 (DIN VDE 0805) for reinforced insulation up to an operation voltage of 400  $V_{\rm RMS}$  or DC. Specifications subject to change.

### FEATURES

- Excellent CTR linearity depending on forward current
- Isolation test voltage, 5300 V<sub>RMS</sub>
- Fast switching times
- Low CTR degradation
- · Low coupling capacitance
- Material categorization:
- for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### APPLICATIONS

- Switchmode power supply
- Telecom
- Battery powered equipment

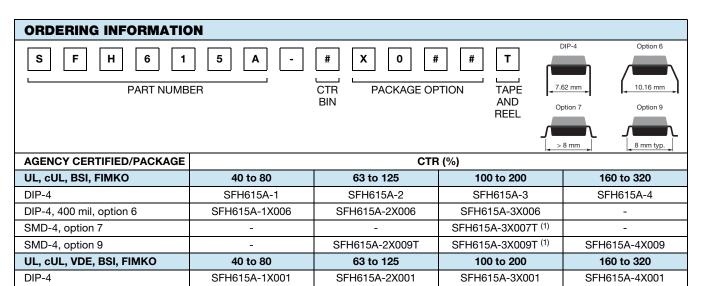
#### AGENCY APPROVALS

- UL file no. E52744
- cUL tested to CSA 22.2 bulletin 5A
- DIN EN 60747-5-5 (VDE 0884-5) available with option 1

SFH615A-3X016

SFH615A-3X017

- BSI EN 60950; EN 60065
- FIMKO
- CQC



### • Add

• Additional options may be possible, please contact sales office.

SFH615A-1X016

SFH615A-1X017T (1)

(1) Also available in tubes; do not add T to end.

<sup>(2)</sup> T3 rotation in tape and reel packaging.

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DIP-4, 400 mil, option 6

SMD-4, option 7

SMD-4, option 9

1

Document Number: 83433

SFH615A-4X016

SFH615A-4X017T (1)

SFH615A-2X016

SFH615A-2X017T (1)

SFH615A-2X019T

SFH615A-2X019T3 (2)

Pb-free



HALOGEN

GREEN

(5-2008)



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## **Vishay Semiconductors**

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Reverse voltage		V <sub>R</sub>	6	V		
DC forward current		I <sub>F</sub>	60	mA		
Forward surge current	t <sub>p</sub> ≤ 10 µs	I <sub>FSM</sub>	2.5	А		
LED power dissipation	at 25 °C	P <sub>diss</sub>	70	mW		
OUTPUT						
Collector emitter voltage		V <sub>CEO</sub>	70	V		
Emitter collector voltage		V <sub>ECO</sub>	7	V		
Collector current		Ι <sub>C</sub>	50	mA		
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA		
Ouput power dissipation	at 25 °C	P <sub>diss</sub>	150	mW		
COUPLER						
Operation temperature		T <sub>amb</sub>	-55 to +100	°C		
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C		
Soldering temperature <sup>(1)</sup>	2 mm from case, $\leq$ 10 s	T <sub>sld</sub>	260	°C		

#### Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT	INPUT							
Forward voltage	I <sub>F</sub> = 60 mA		V <sub>F</sub>		1.35	1.65	V	
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>		0.01	10	μA	
Capacitance	$V_R = 0 V$ , f = 1 MHz		Co		13		pF	
OUTPUT								
Collector emitter capacitance	$V_{CE} = 5 V, f = 1 MHz$		C <sub>CE</sub>		5.2		pF	
	V 10.V	SFH615A-1	I <sub>CEO</sub>		2	50	nA	
		SFH615A-2	I <sub>CEO</sub>		2	50	nA	
Collector emitter leakage current	V <sub>CE</sub> = 10 V	SFH615A-3	I <sub>CEO</sub>		5	100	nA	
		SFH615A-4	I <sub>CEO</sub>		5	100	nA	
COUPLER								
Collector emitter saturation voltage	I <sub>F</sub> = 10 mA, f = 1 MHz		V <sub>CEsat</sub>		0.25	0.4	V	
Coupling capacitance			C <sub>C</sub>		0.4		pF	

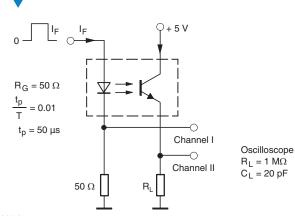
#### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.



<b>CURRENT TRANSFER RATIO</b> ( $T_{amb}$ = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 5 V	SFH615A-1	CTR	40		80	%
		SFH615A-2	CTR	63		125	%
		SFH615A-3	CTR	100		200	%
		SFH615A-4	CTR	160		320	%
	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 5 V	SFH615A-1	CTR	13	30		%
		SFH615A-2	CTR	22	45		%
		SFH615A-3	CTR	34	70		%
		SFH615A-4	CTR	56	90		%

SWITCHING CH	ARACTERISTICS (T <sub>amb</sub> = 25 °C	, unless othe	erwise spec	cified)			
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
NON-SATURATED		•					•
Turn-on time	$I_F$ = 10 mA, $V_{CC}$ = 5 V, $R_L$ = 75 $\Omega$		t <sub>on</sub>		3		μs
Rise time	$I_F$ = 10 mA, $V_{CC}$ = 5 V, $R_L$ = 75 $\Omega$		t <sub>r</sub>		2		μs
Turn-off time	$I_F$ = 10 mA, $V_{CC}$ = 5 V, $R_L$ = 75 $\Omega$		t <sub>off</sub>		2.3		μs
Fall time	$I_F$ = 10 mA, $V_{CC}$ = 5 V, $R_L$ = 75 $\Omega$		t <sub>f</sub>		2		μs
Cut-off frequency	$I_F$ = 10 mA, $V_{CC}$ = 5 V, $R_L$ = 75 $\Omega$		f <sub>CO</sub>		208		kHz
SATURATED		·					
	I <sub>F</sub> = 20 mA	SFH615A-1	t <sub>on</sub>		3		μs
Turn-on time	I <sub>F</sub> = 10 mA	SFH615A-2	t <sub>on</sub>		4.2		μs
Turn-on time		SFH615A-3	t <sub>on</sub>		4.2		μs
	I <sub>F</sub> = 5 mA	SFH615A-4	t <sub>on</sub>		6		μs
	I <sub>F</sub> = 20 mA	SFH615A-1	t <sub>r</sub>		2		μs
D'a a l'aca	I <sub>F</sub> = 10 mA	SFH615A-2	t <sub>r</sub>		3		μs
Rise time		SFH615A-3	t <sub>r</sub>		3		μs
	I <sub>F</sub> = 5 mA	SFH615A-4	t <sub>r</sub>		4		μs
	I <sub>F</sub> = 20 mA	SFH615A-1	t <sub>off</sub>		18		μs
<b>T</b>	1 10 11	SFH615A-2	t <sub>off</sub>		23		μs
Turn-off time	I <sub>F</sub> = 10 mA	SFH615A-3	t <sub>off</sub>		23		μs
F	I <sub>F</sub> = 5 mA	SFH615A-4	t <sub>off</sub>		25		μs
	I <sub>F</sub> = 20 mA	SFH615A-1	t <sub>f</sub>		11		μs
	10	SFH615A-2	t <sub>f</sub>		14		μs
Fall time	I <sub>F</sub> = 10 mA	SFH615A-3	t <sub>f</sub>		14		μs
	I <sub>F</sub> = 5 mA	SFH615A-4	t <sub>f</sub>		15		μs

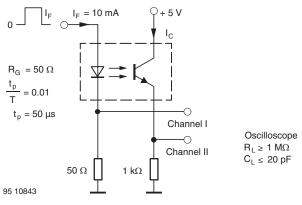


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Fig. 1 - Test Circuit, Non-Saturated Operation



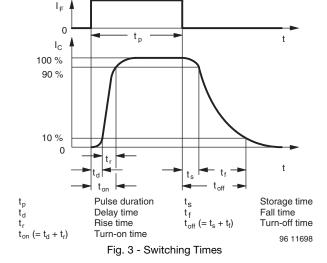


Fig. 2 - <sup>-</sup>	Test Circuit	, Saturated	Operation

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55/115/21				
Pollution degree	According to DIN VDE 0109		2				
Comparative tracking index	Insulation group IIIa	CTI	175				
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V <sub>ISO</sub>	4470	V <sub>RMS</sub>			
Tested withstanding isolation voltage	According to UL1577, t = 1 s	V <sub>ISO</sub>	5300	V <sub>RMS</sub>			
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	V <sub>peak</sub>			
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	VIORM	890	V <sub>peak</sub>			
Isolation resistance	$T_{amb} = 25 \text{ °C}, V_{IO} = 500 \text{ V}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω			
Isolation resistance	$T_{amb} = 100 \ ^{\circ}C, \ V_{IO} = 500 \ V$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω			
Output safety power		P <sub>SO</sub>	700	mW			
Input safety current		I <sub>SI</sub>	400	mA			
Input safety temperature		Ts	175	°C			
Creepage distance	DIP-4		≥7	mm			
Clearance distance	DIP-4		≥7	mm			
Creepage distance	DIP-4, 400 mil, option 6		≥ 8	mm			
Clearance distance	DIP-4, 400 mil, option 6		≥ 8	mm			
Creepage distance	SMD-4, option 7 and option 9		≥ 7	mm			
Clearance distance	SMD-4, option 7 and option 9		≥7	mm			
Insulation thickness		DTI	≥ 0.4	mm			

#### Note

As per DIN EN 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

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### **TYPICAL CHARACTERISTICS** ( $T_{amb} = 25$ °C, unless otherwise specified)

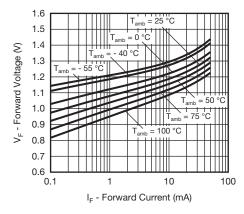


Fig. 4 - Forward Voltage vs. Forward Current

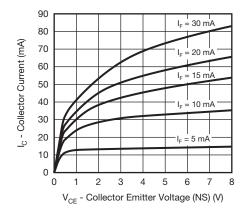


Fig. 5 - Collector Current vs. Collector Emitter Voltage (non-saturated)

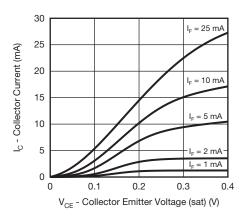


Fig. 6 - Collector Current vs. Collector Emitter Voltage (saturated)

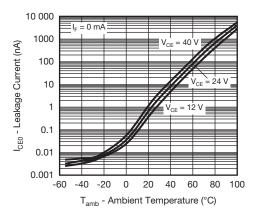


Fig. 7 - Leakage Current vs. Ambient Temperature

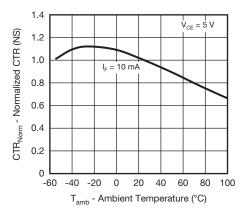


Fig. 8 - Normalized CTR (non-saturated) vs. Ambient Temperature

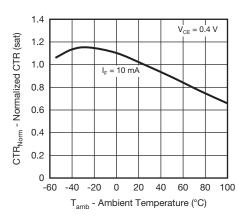
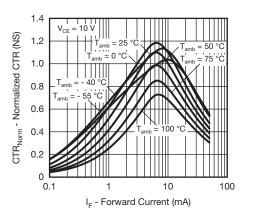


Fig. 9 - Normalized CTR (saturated) vs. Ambient Temperature

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Fig. 10 - Normalized CTR (non-saturated) vs. Forward Current

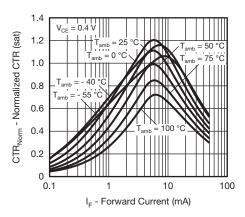


Fig. 11 - Normalized CTR (saturated) vs. Forward Current

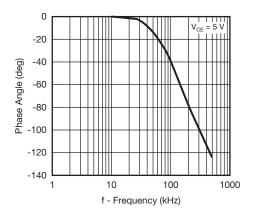


Fig. 12 - Phase Angle vs. Frequency

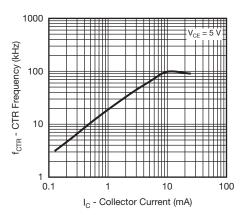


Fig. 13 - Cut-Off Frequency vs. Collector Current

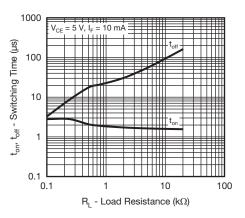
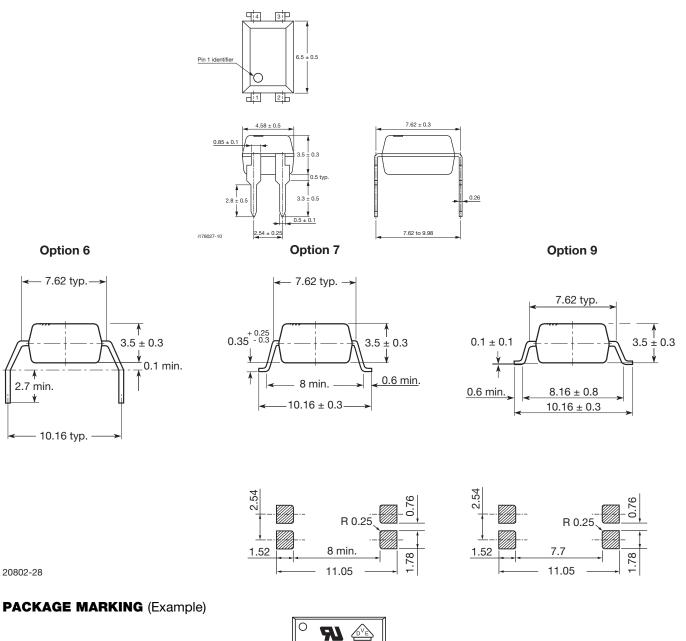


Fig. 14 - Switching Time vs. Load Resistance

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### **PACKAGE DIMENISONS** in millimeters



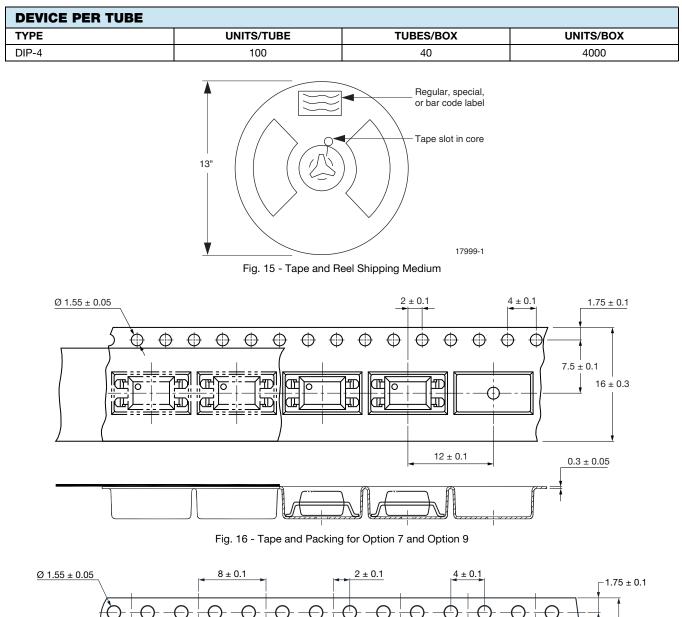
#### Notes

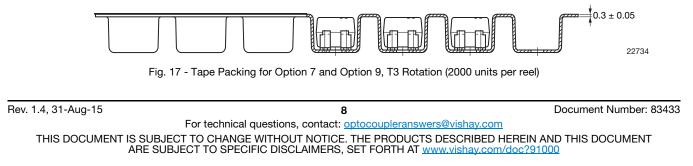
- VDE logo is only marked on option 1 parts. Option information is not marked on the part.
- Tape and reel suffix (T) is not part of the package marking.

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#### **PACKING INFORMATION**





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#### **SOLDER PROFILES**

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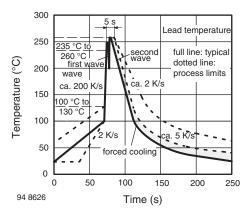


Fig. 18 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP-8 Devices

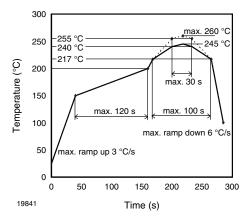


Fig. 19 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD-8 Devices

## **Vishay Semiconductors**

### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions:  $T_{amb} < 30\ ^\circ C,\ RH < 85\ \%$ Moisture sensitivity level 1, according to J-STD-020



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