



3.3V, 2 x 1:5 CMOS Clock Driver

### **Features**

- → Low output skew: <270ps
- → Switching frequency of 133 MHz
- → Fast output rise/fall time <1.5ns
- → Low propagation delay <3.0ns
- → Low input capacitance <6.0pF
- → Balanced CMOS outputs
- → Industrial Temperature: -40°C to +85°C
- $\rightarrow$  3.3V ±10% operation
- → Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- → Halogen and Antimony Free. "Green" Device (Note 3)
- → For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.
  - https://www.diodes.com/quality/product-definitions/
- → Packaging: (Pb-free & Green Available)
  - 20-pin 300-mil wide QSOP (Q)

### **Truth Table**

$\begin{array}{c c} & & & \\ \hline \hline OE_A, & & IN_A, \\ \hline OE_B & & IN_B \\ \hline \end{array}$		Outputs		
		$_{\mathrm{O}}A_{\mathrm{N}}$ , $_{\mathrm{O}}B_{\mathrm{N}}$	MON	
L	L	L	L	
L	Н	Н	Н	
Н	L	Z	L	
Н	Н	Z	Н	

### Note:

- 1. H = High Voltage Level, L = Low Voltage Level
  - Z = High Impedance

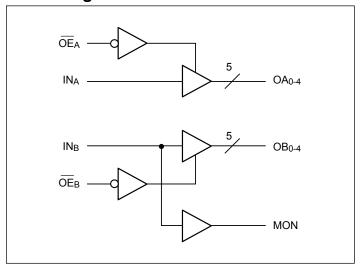
## **Description**

Diodes' PI49FCT32805 is a fast speed, low skew, fast slew rate, and low propagation delay for most computing and communication applications.

The devices are non-inverting drivers. The outputs are configured into 2 groups of 1-in, 5-out with independent output enable. Group B has an extra MON output. Excellent output signals to power and ground ratio minimize power and ground noise, and also improves output performance.

The PI49FCT32805 has built-in series damping resistors on all outputs.

## **Block Diagram**

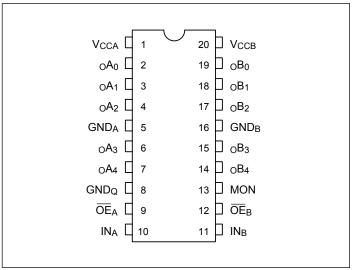


- $1.\ No\ purposely\ added\ lead.\ Fully\ EU\ Directive\ 2002/95/EC\ (RoHS),\ 2011/65/EU\ (RoHS\ 2)\ \&\ 2015/863/EU\ (RoHS\ 3)\ compliant.$
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.





# **Pin Configuration**



# **Pin Description**

Pin Name	Description	
$\overline{OE}_A$ , $\overline{OE}_B$	Hi-Z State Output Enable Inputs (Active LOW)	
IN <sub>A</sub> , IN <sub>B</sub>	Clock Inputs	
OAN, OBN	Clock Outputs	
MON	Monitor Output	
GND	Ground	
$V_{CC}$	Power	





## **Maximum Ratings**

Storage Temperature	–55°C to +150°C
Supply Voltage to Ground Potential (Inputs & V <sub>CC</sub> On	ly)0.5V to +4.6V
Supply Voltage to Ground Potential (Outputs & I/O Or	nly)0.5V to +4.6V
DC Input Voltage	0.5V to +4.6V
DC Output Current	120 mA
Power Dissipation	0.5W
Junction Temperature	125°C max
Latch up	200mA
ESD Protection (Input)	2000V min (HBM)

### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## **DC Electrical Characteristics** ( $T_A = -40$ °C to +85°C, $V_{CC} = 3.3$ V $\pm 10$ %)

Symbol	Parameter	Test Condition <sup>(1)</sup>		Min.	Тур.	Max.	Units
V <sub>OH</sub>	Output High Voltage $V_{CC} = 3.0V$ , $V_{IN} = V_{IL}$ or $V_{IH}$	$I_{OH} = -8mA$		2.4 <sup>(2)</sup>	3.0		
V <sub>OL</sub>	Output Low Voltage V <sub>CC</sub> = 3.0V, V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub>	$I_{OL} = +12mA$	$I_{OL} = +12mA$		0.4	0.5	V
$V_{\mathrm{IH}}$	Input High Voltage			2.0		V <sub>CC</sub> -0.2	
$V_{\mathrm{IL}}$	Input Low Voltage			-0.5		0.8	
$I_{IH}$	Input High Current	$V_{CC} = Max., V_{IN} = V_{CC}$	2			1	
$I_{IL}$	Input Low Current	$V_{CC} = Max., V_{IN} = GN$	D			-1	
I <sub>OZH</sub>	High Impedance Output Current	V <sub>CC</sub> = Max., All outputs Disabled	$V_{OUT} = V_{CC}$			1	μΑ
I <sub>OZL</sub>	High Impedance Output Current	V <sub>CC</sub> = Max., All outputs Disabled	V <sub>OUT</sub> = GND			-1	
V <sub>IK</sub>	Clamp Diode Voltage	$V_{CC}$ = Min., $I_{IN}$ = -18m	nA		-0.9	-1.2	V
I <sub>OH</sub>	Output HIGH Current <sup>(3)</sup>	$V_{OUT} = 1.5V$ , $V_{IN} = V_{IL}$	or $V_{IH}$ , $V_{CC} = 3.3V$	-25	-55	-80	
I <sub>OL</sub>	Output LOW Current <sup>(3)</sup>	$V_{OUT} = 1.5V$ , $V_{IN} = V_{II}$	$_{\rm L}$ or $V_{\rm IH}$ , $V_{\rm CC}$ = 3.3 $V$	25	45	90	mA
Ios	Short Circuit Current <sup>(4)</sup>	V <sub>CC</sub> = Max., V <sub>OUT</sub> = GND		-50	-100	-180	
R <sub>S</sub>	Internal Series Resistor				20		Ω

- 1. For Max or Min conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2.  $V_{OH} = V_{CC} 0.6V$  at rated current.
- 3. This parameter is guaranteed by device characterization.
- 4. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.





## **Power Supply Characteristics**

Parameters	Description	Test Conditions <sup>(1)</sup>	Condition	Min.	Тур	Max.	Units
$I_{CC}$	Quiescent Power Supply Current	V <sub>CC</sub> = Max	$V_{IN} = GND$ or $V_{CC}$		0.1	30	4
$\Delta I_{CC}$	Supply Current per Inputs @ TTL HIGH	V <sub>CC</sub> = Max	$V_{IN} = V_{CC} - 0.6V^{(2)}$		110	300	μΑ
I <sub>CCD</sub>	Supply Current per Output per MHz <sup>(3)</sup>	$V_{CC} = Max$ , Outputs Open $\overline{OE}A$ or $\overline{OE}B = GND$ 50% Duty Cycle	$V_{IN} = V_{CC}$ $V_{IN} = GND$		0.09 Per Output Toggling	0.16	mA/ MHz
		$V_{CC} = Max$ , Outputs Open $f_{O} = 10 \text{ MHz}$	$V_{IN} = V_{CC}$ $V_{IN} = GND$		1.3	9.0 <sup>(4)</sup>	
	50   O   M	50% Duty Cycle  OEA or OEβ = GND  Mon. Outputs Toggling	$V_{IN} = V_{CC} - 0.6V$ $V_{IN} = GND$		1.3	10.0 <sup>(4)</sup>	
Outputs $f_O = 2.5$ $50\%$ Dut $\overline{OE}_A$ or $\overline{O}$		$V_{CC} = Max$ , Outputs Open $f_{O} = 2.5 \text{ MHz}$	$V_{IN} = V_{CC}$ $V_{IN} = GND$		4.4	6.0 <sup>(4)</sup>	mA
	50% Duty Cycle  OEA or OEв = GND  Eleven Outputs Toggling	$V_{\rm IN} = V_{\rm CC} - 0.6V$ $V_{\rm IN} = {\rm GND}$		4.4	7.0 <sup>(4)</sup>		

#### Notes:

- 1. For Max or Min conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Per TTL driven input ( $V_{IN} = V_{CC} 0.6V$ ); all other inputs at  $V_{CC}$  or GND.
- 3. This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- 4. Values for these conditions are examples of the Ic formula. These limits are guaranteed but not tested.

## **Switching Characteristics**

Symbol	Description	Condition	Max.	Units
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay A to $B_N^{(1)}$	15pF	3.0	
t <sub>R</sub> /t <sub>F</sub>	Rise/Fall Time <sup>(2)</sup> 0.5V - 2.0V	15pF	1.5	
t <sub>SK(P)</sub>	Pulse Skew <sup>(2)</sup>	Same Output	0.35	
t <sub>SK(O)</sub>	Output Skew (2)	Same Package, Same Bank	0.27	ns
t <sub>SK(I)</sub>	Package Skew <sup>(2)</sup>	Same Package, Same Bank	0.55	
$t_{ZL}$ , $t_{ZH}$ $t_{LZ}$ , $t_{HZ}$	Enable/Disable Time	15pF 500Ω	5.2	
F <sub>MAX</sub>	Input Frequency	15pF	133	MHz

- 1. Minimum propagation delay of 1.5ns is guaranteed but not tested.
- 2. These parameters are guaranteed by design





## **Capacitance** ( $T_A = 25$ °C, f = 1 MHz)

Parameters <sup>(1)</sup>	Description	<b>Test Conditions</b>	Тур	Max.	Units
$C_{IN}$	Input Capacitance	$V_{IN} = 0V$		4	F
C <sub>OUT</sub>	Output Capacitance	$V_{OUT} = 0V$		6	pF

### Note:

### **Switch Position**

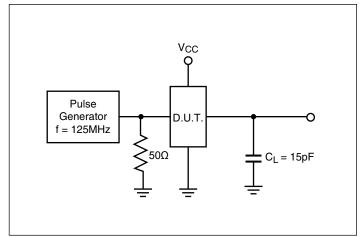
Test	Switch
Disable Low Enable Low	6V
Disable High Enable High	GND
All Other Inputs	Open

<sup>1.</sup> This parameter is determined by device characterization.

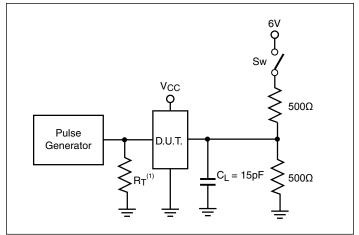




# Tests Circuits for F<sub>IN</sub> >100 MHz



# **Enable/Disable Time Test Set-Up**



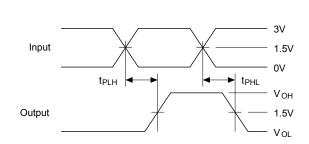
#### Note:

1.  $R_T$  = Termination resistance should be equal to  $Z_{OUT}$  of the Pulse Generator.

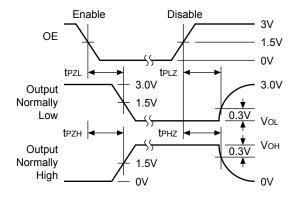




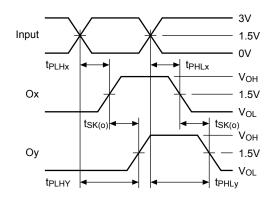
# **Switching Waveforms**



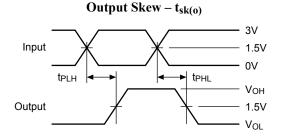
### **Propagation Delay**



**Enable and Disable Times** 

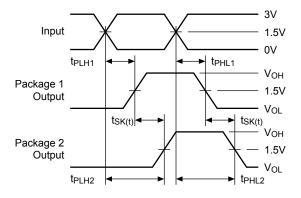


 $t_{SK(0)} = |t_{PLHy} - t_{PLHx}| \text{ or } |t_{PHLy} - t_{PHLx}|$ 



 $t_{SK(p)} = |t_{PHL} - t_{PLH}|$ 





 $t_{SK(t)}$  = |  $t_{PLH2}$  -  $t_{PLH1}$  | or |  $t_{PHL2}$  -  $t_{PHL1}$  |

Package Skew - t<sub>sk(t)</sub>





# **Part Marking**



YY: Year

WW: Workweek

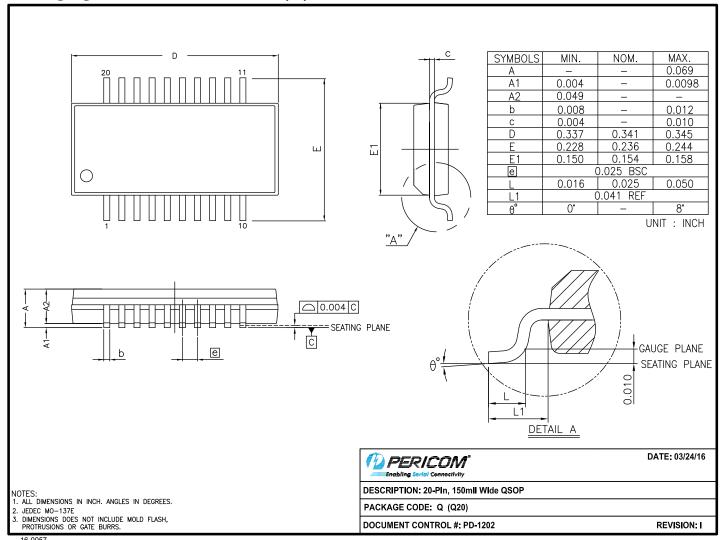
1st X: Assembly Code 2nd X: Fab Code

www.diodes.com





## Packaging Mechanical: 20-QSOP (Q)



### For latest package info.

 $please\ check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/packaging-mech$ 

## **Ordering Information**

Ordering Code	Package Code	Package Description
PI49FCT32805QEX	Q	20-pin 150mil wide (QSOP)

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. E = Pb-free and Green
- 5. X suffix = Tape/Reel





#### IMPORTANT NOTICE

- 1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- 5. Diodes products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2021 Diodes Incorporated

www.diodes.com