



#### 600V HALF BRIDGE GATE DRIVER IN SO-8

#### **Description**

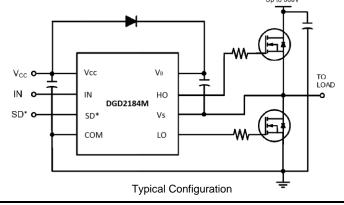
The DIODES™ DGD2184M is a high voltage / high speed gate driver capable of driving N-Channel MOSFETs and IGBTs in a half bridge configuration. High voltage processing techniques enable the DGD2184M's high-side to switch to 600V in a bootstrap operation.

The DGD2184M logic inputs are compatible with standard TTL and CMOS levels (down to 3.3V) for easy interfacing with controlling devices. The driver outputs feature high pulse current buffers designed for minimum driver cross conduction. The DGD2184M has a fixed internal deadtime of 395ns (typ).

The DGD2184M is offered in SO-8 (Type TH) package, the operating temperature extends from -40°C to +125°C.

#### **Applications**

- DC-DC converters
- DC-AC inverters
- AC-DC power supplies
- Motor controls
- Class D power amplifiers



#### **Features**

- Floating High-Side Driver in Bootstrap Operation to 600V
- Drives Two N-Channel MOSFETs or IGBTs in Half Bridge Configuration
- 1.4A Source / 1.8A Sink Output Current Capability
- Outputs Tolerant to Negative Transients
- Internal Dead Time of 395ns to Protect MOSFETs
- Wide Low-Side Gate Driver and Logic Supply: 10V to 20V
- Logic Input (IN and SD\*) 3.3V Capability
- Schmitt Triggered Logic Inputs with Internal Pull Down
- Undervoltage Lockout for High and Low Side Drivers
- Extended Temperature Range: -40°C to +125°C
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

- Package: SO-8
- Package Material: Molded Plastic. "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (§3)
- Weight: 0.075 grams (Approximate)

SO-8 (Type TH)



Top View

#### **Ordering Information** (Note 4)

Part Number	Package	Marking	Reel Size (inch)	Tape Width (mm)	Packing		
Part Number	Fackage	iviai Killy	Reel Size (Ilicii)	rape widin (ililii)	Qty.	Carrier	
DGD2184MS8-13	SO-8 (Type TH)	DGD2184M	13	12	2,500	Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

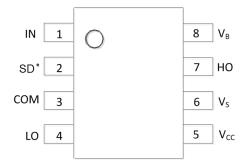
#### **Marking Information**



);; = Manufacturer's Marking DGD2184M = Product Type Marking Code YY = Year (ex: 22 = 2022) WW = Week (01 to 53)



## **Pin Diagrams**

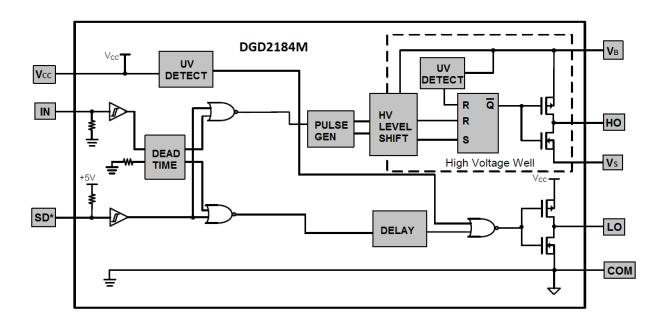


Top View SO-8 (Type TH)

## **Pin Descriptions**

Pin Number	Pin Name	Function
1	IN	Logic Input for High-Side and Low-Side Gate Driver Outputs (HO and LO), in Phase with HO
2	SD*	Logic Input for Shutdown, Enabled Low
3	COM	Low-Side and Logic Return
4	LO	Low-Side Gate Drive Output
5	Vcc	Low-Side and Logic Fixed Supply
6	Vs	High-Side Floating Supply Return
7	НО	High-Side Gate Drive Output
8	$V_{B}$	High-Side Floating Supply

# **Functional Block Diagram**





## **Absolute Maximum Ratings** (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
High-Side Floating Supply Voltage	V <sub>B</sub>	-0.3 to +624	V
High-Side Floating Supply Offset Voltage	Vs	$V_B - 24 \text{ to } V_B + 0.3$	V
High-Side Floating Output Voltage	VHO	Vs – 0.3 to V <sub>B</sub> + 0.3	V
Offset Supply Voltage Transient	dVs / dt	50	V/ns
Low-Side Fixed Supply Voltage	Vcc	-0.3 to +24	V
Low-Side Output Voltage	VLO	-0.3 to Vcc + 0.3	V
Logic Input Voltage (IN and SD*)	VIN	-0.3 to V <sub>CC</sub> + 0.3	V

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation Linear Derating Factor (Note 5)	P <sub>D</sub>	0.625	W
Thermal Resistance, Junction to Ambient (Note 5)	Reja	200	°C/W
Operating Temperature	TJ	+150	
Lead Temperature (Soldering, 10s)	T∟	+300	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	

Note: 5. When mounted on a standard JEDEC 2-layer FR-4 board.

# **Recommended Operating Conditions**

Parameter	Symbol	Min	Max	Unit
High-Side Floating Supply Absolute Voltage	Vв	Vs + 10	Vs + 20	V
High-Side Floating Supply Offset Voltage	Vs	(Note 6)	600	V
High-Side Floating Output Voltage	V <sub>HO</sub>	Vs	V <sub>B</sub>	V
Low-Side Fixed Supply Voltage	Vcc	10	20	V
Low-Side Output Voltage	VLO	0	Vcc	V
Logic Input Voltage (IN and SD*)	Vin	0	Vcc	V
Ambient Temperature	TA	-40	+125	°C

Note: 6. Logic operation for  $V_S$  of -5V to +600V.



## DC Electrical Characteristics (V<sub>BIAS</sub> (V<sub>CC</sub>, V<sub>BS</sub>) = 15V, @T<sub>A</sub> = +25°C, unless otherwise specified.) (Notes 7, 8)

Parameter	Symbol	Min	Тур	Max	Unit	Conditions
Logic "1" Input Voltage	V <sub>IH</sub>	2.5	_	_	V	V <sub>CC</sub> = 10V to 20V
Logic "0" Input Voltage	VIL	_	_	0.8	V	Vcc = 10V to 20V
SD* Input Positive Going Threshold	V <sub>SDTH+</sub>	2.5	_	_	V	V <sub>CC</sub> = 10V to 20V
SD* Input Negative Going Threshold	VsDTH-	_	_	0.8	V	Vcc = 10V to 20V
High Level Output Voltage, VBIAS - VO	Vон	_	_	1.2	V	$I_0 = 0mA$
Low Level Output Voltage, Vo	$V_{OL}$	_	_	0.1	V	$I_O = 20mA$
Offset Supply Leakage Current	ILK	_	_	50	μΑ	$V_B = V_S = 600V$
Quiescent V <sub>BS</sub> Supply Current	IBSQ	20	60	150	μΑ	VIN = 0V or 5V
Quiescent Vcc Supply Current	Iccq	0.4	1.0	1.8	mA	VIN = 0V or 5V
Logic "1" Input Bias Current	I <sub>IN+</sub>	_	25	60	μΑ	IN = 5V, SD* = 0V
Logic "0" Input Bias Current	I <sub>IN-</sub>	_	_	1.0	μΑ	IN = 0V, SD* = 5V
V <sub>BS</sub> Supply Under-Voltage Positive Going Threshold	V <sub>BSUV+</sub>	8.0	8.9	9.8	V	_
V <sub>BS</sub> Supply Under-Voltage Negative Going Threshold	V <sub>BSUV</sub> -	7.4	8.2	9.0	V	_
V <sub>CC</sub> Supply Under-Voltage Positive Going Threshold	V <sub>CCUV+</sub>	8.0	8.9	9.8	V	_
Vcc Supply Under-Voltage Negative Going Threshold	Vccuv-	7.4	8.2	9.0	V	
Output High Short Circuit Pulsed Current	I <sub>O+</sub>	1.4	1.9	_	Α	V <sub>O</sub> = 0V, PW ≤ 10μs
Output Low Short Circuit Pulsed Current	lo-	1.7	2.3	_	А	V <sub>O</sub> = 15V, PW ≤ 10μs

#### AC Electrical Characteristics (VBIAS (VCC, VBS) = 15V, CL = 1000pF, @TA = +25°C, unless otherwise specified.)

Parameter	Symbol	Min	Тур	Max	Unit	Conditions
Turn-On Propagation Delay	ton	_	680	900	ns	Vs = 0V
Turn-Off Propagation Delay	toff	_	270	400	ns	Vs = 0V or 600V
Shut-Down Propagation Delay	tsp	_	180	270	ns	_
Delay Matching, HO & LO Turn-On	t <sub>DMON</sub>	_	_	90	ns	_
Delay Matching, HO & LO Turn-Off	tomoff	_	_	40	ns	Io = 0A
Turn-On Rise Time	t <sub>R</sub>	_	40	60	ns	Vs = 0V
Turn-Off Fall Time	tF	_	20	35	ns	Vs = 0V
Deadtime: tot lo-ho & tot ho-lo	tрт	345	395	445	ns	_

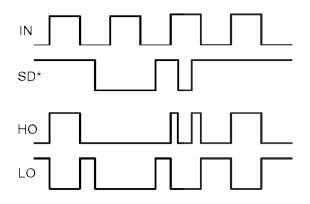
<sup>7.</sup> The  $V_{IN}$  and  $I_{IN}$  parameters are applicable to the two logic input pins: IN and SD\*. The  $V_{O}$  and  $I_{O}$  parameters are applicable to the respective output

pins: HO and LO.

8. For optimal operation, it is recommended that the input pulses (IN and SD\*) should have a minimum amplitude of 2.5V with a minimum pulse width of 800ns.



## **Timing Waveforms**



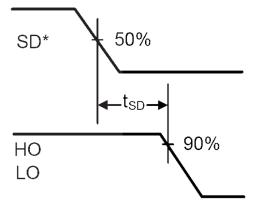
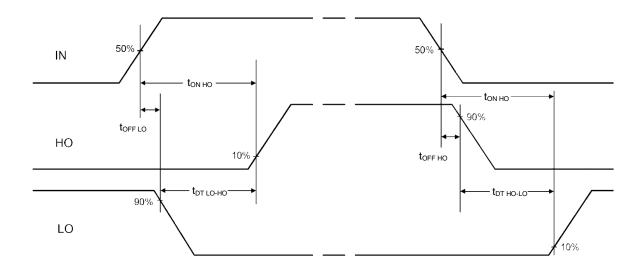


Figure 1. Input / Output Timing Diagram

Figure 2. Shutdown Waveform Definitions



Deadtime tot LO-HO = ton HO - toff LO

tot HO-LO = ton LO - toff HO

Deadtime matching

tmot = tot LO-HO - tot HO-LO

Delay matching
tom off = toff to - tofft ho

Figure 3. Switching Time Waveform Definitions



# Typical Performance Characteristics (@TA = +25°C, Vcc = 15V, unless otherwise specified.)

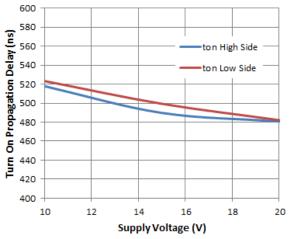


Figure 4. Turn-on Propagation Delay vs. Supply Voltage

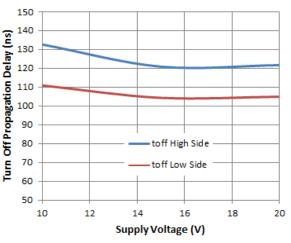


Figure 6. Turn-off Propagation Delay vs. Supply Voltage

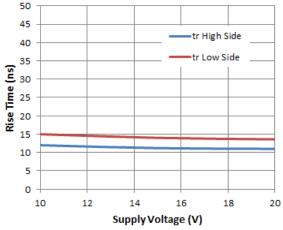


Figure 8. Rise Time vs. Supply Voltage

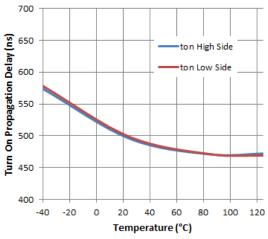


Figure 5. Turn-on Propagation Delay vs. Temperature

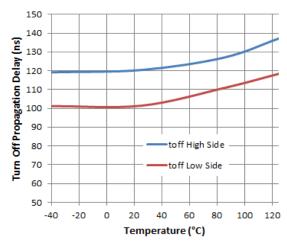


Figure 7. Turn-off Propagation Delay vs. Temperature

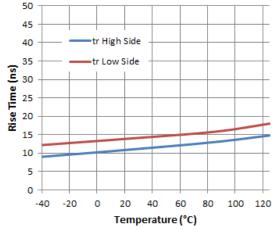


Figure 9. Rise Time vs. Temperature



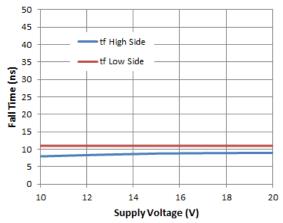
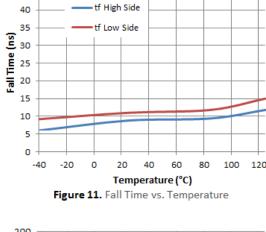


Figure 10. Fall Time vs. Supply Voltage



50

45

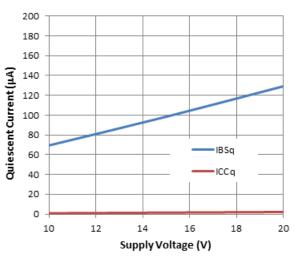


Figure 12. Quiescent Current vs. Supply Voltage

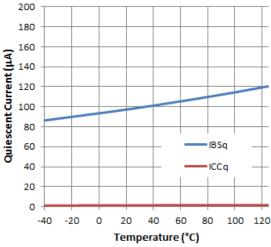


Figure 13. Quiescent Current vs. Temperature

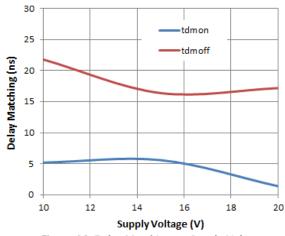


Figure 14. Delay Matching vs. Supply Voltage

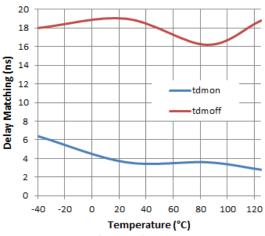


Figure 15. Delay Matching vs. Temperature



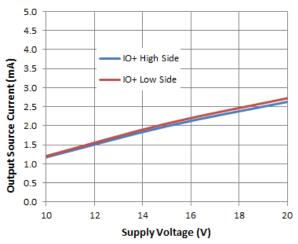


Figure 16. Output Source Current vs. Supply Voltage

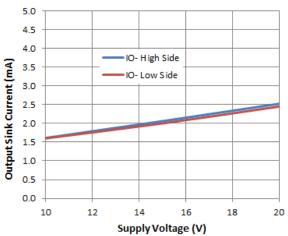


Figure 18. Output Sink Current vs. Supply Voltage

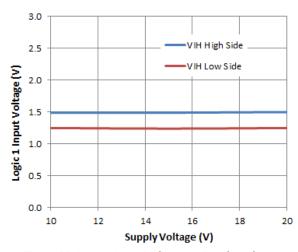


Figure 20. Logic 1 Input Voltage vs. Supply Voltage

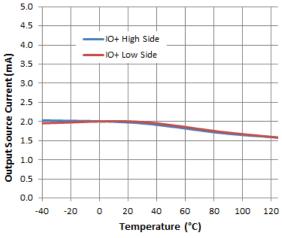


Figure 17. Output Source Current vs. Temperature

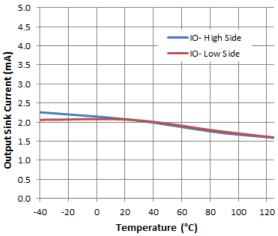


Figure 19. Output Sink Current vs. Temperature

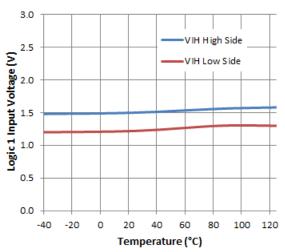


Figure 21. Logic 1 Input Voltage vs. Temperature



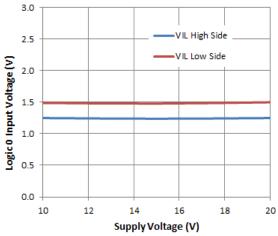


Figure 22. Logic O Input Voltage vs. Supply Voltage

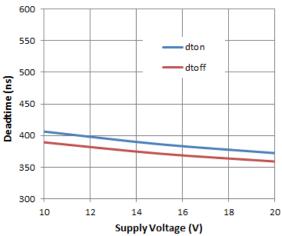


Figure 24. Deadtime vs. Supply Voltage

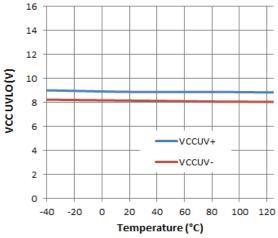


Figure 26. VCC UVLO vs. Temperature

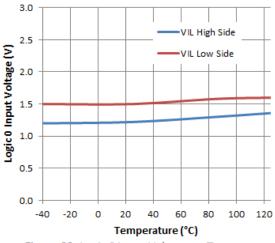


Figure 23. Logic 0 Input Voltage vs. Temperature

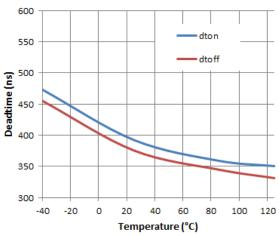


Figure 25. Deadtime vs. Temperature

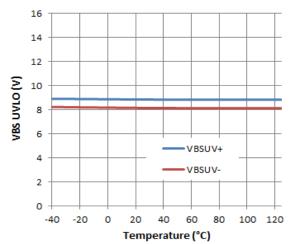


Figure 27. VBS UVLO vs. Temperature



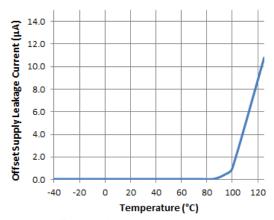


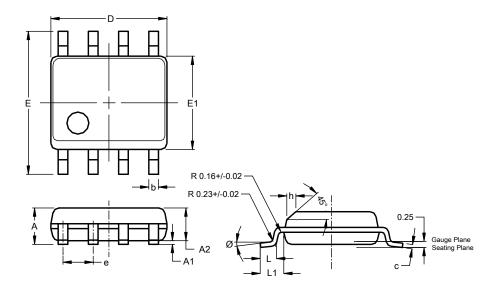
Figure 28. Offset Supply Leakage Current vs. Temperature



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SO-8 (Type TH)

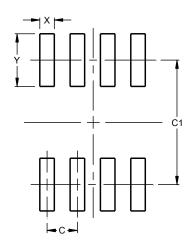


SO-8 (Type TH)					
Dim	Min	Max	Тур		
Α	1.35	1.75			
A1	0.10	0.25			
A2			1.45		
b	0.35	0.51			
C	0.190	0.248			
D	4.80	5.00	4.90		
Е	5.80	6.20	6.00		
E1	3.80	4.00	3.90		
е			1.27		
h	0.25	0.50			
٦	0.41	1.27			
L1			1.04		
Ø	0°	8°			
All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8 (Type TH)



Dimensions	Value (in mm)
С	1.27
C1	5.20
Х	0.60
V	2.20

August 2022



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