

Silicon Carbide (SiC) MOSFET - 22 mohm, 1200 V, M3, TO-247-3L NTHL022N120M3S

Features

- Typ. $R_{DS(on)} = 22 \text{ m}\Omega$ @ $V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge $(Q_{G(tot)} = 139 \text{ nC})$
- Low Effective Output Capacitance (Coss = 141 pF)
- 100% Avalanche Tested
- This Device is Halide Free and RoHS Compliant with Exemption 7a, Pb–Free 2LI (on second level interconnection)

Typical Applications

- Solar Inverters
- Electric Vehicle Charging Stations
- UPS (Uninterruptible Power Supplies)
- Energy Storage Systems
- SMPS (Switch Mode Power Supplies)

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	1200	V
Gate-to-Source Voltage	1		V_{GS}	-10/+22	٧
Recommended Operation Values of Gate-to-Source Voltage		V_{GSop}	-3/+18	V	
Continuous Drain Current (Note 1)	Steady State T _C = 25°C		I _D	68	Α
Power Dissipation (Note 1)			P _D	352	W
Continuous Drain Current (Note 1)	Steady State T _C = 100°C		I _D	48	Α
Power Dissipation (Note 1)			P _D	176	W
Pulsed Drain Current (Note 2)	T _C = 25°C		I _{DM}	252	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	ç
Source Current (Body Diode) T _C = 25°C V _{GS} = -3 V			I _S	72	Α
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 23.1 A, L = 1 mH) (Note 3)			E _{AS}	267	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)			TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

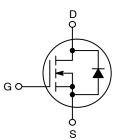
 The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

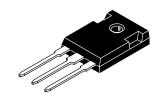
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- 2. Repetitive rating, limited by max junction temperature.
- 3. E_{AS} of 267 mJ is based on starting $T_J = 25^{\circ}C$; L = 1 mH, $I_{AS} = 23.1$ A, $V_{DD} = 100$ V, $V_{GS} = 18$ V.

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX	
1200 V	30 mΩ @ 18 V	68 A	

N-CHANNEL MOSFET





TO-247-3LD CASE 340CX

MARKING DIAGRAM



HL022N120M3S = Specific Device Code

A = Assembly Location

Y = Year

WW = Work Week
ZZ = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping
NTHL022N120M3S	TO247-3L	30 Units / Tube

THERMAL CHARACTERISTICS

Parameter		Max	Unit
Junction-to-Case - Steady State (Note 1)		0.43	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	40	

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF-STATE CHARACTERISTICS	<u>I</u>						
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}$		1200	-	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 1 mA, referenced to 25°C		-	0.3	-	V/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 1200 V	T _J = 25°C	-	-	100	μΑ
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} = +22/-10 V, V _{DS} = 0 V		-	-	±1	μΑ
ON-STATE CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = 2$	0 mA	2.04	2.72	4.4	V
Recommended Gate Voltage	V_{GOP}			-3	-	+18	V
Drain-to-Source On Resistance	R _{DS(on)}	$V_{GS} = 18 \text{ V}, I_D = 40 \text{ A},$	T _J = 25°C	-	22	30	mΩ
		V _{GS} = 18 V, I _D = 40 A,	T _J = 175°C	-	47	-	
Forward Transconductance	9FS	V _{DS} = 10 V, I _D =	40 A	-	34		S
CHARGES, CAPACITANCES & GATE RES	ISTANCE						_
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V	_{DS} = 800 V	-	3130	-	pF
Output Capacitance	C _{OSS}			-	141	-	
Reverse Transfer Capacitance	C _{RSS}			-	12	-	
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -3/18 \text{ V}, V_{DS} = 800 \text{ V},$ $I_{D} = 40 \text{ A}$		-	139	-	nC
Threshold Gate Charge	Q _{G(TH)}			-	20	-	-
Gate-to-Source Charge	Q_{GS}			-	24	-	
Gate-to-Drain Charge	Q_{GD}			-	39	-	
Gate-Resistance	R_{G}	f = 1 MHz		-	1.5	-	Ω
SWITCHING CHARACTERISTICS							
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -3/18$		-	19	-	ns
Rise Time	t _r	V _{DS} = 800 V, I _D = 40 A,	,	-	50	-	
Turn-Off Delay Time	t _{d(OFF)}	$ m R_G^{}=4.5~\Omega$ Inductive Load (Note 4)		-	44	-	
Fall Time	t _f			-	14	-	
Turn-On Switching Loss	E _{ON}			-	1212	-	μJ
Turn-Off Switching Loss	E _{OFF}			-	307	-	
Total Switching Loss	E _{tot}			-	1519	-	
SOURCE-DRAIN DIODE CHARACTERIST	ics					-	-
Continuous Source-Drain Diode Forward Current	I _{SD}	$V_{GS} = -3 \text{ V, } T_C =$	25°C	-	-	72	Α
Pulsed Source-Drain Diode Forward Current (Note 2)	I _{SDM}			-	-	252	
Forward Diode Voltage	V_{SD}	V _{GS} = -3 V, I _{SD} = 40 A,	, T _J = 25°C	-	4.5	-	V

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified) (continued)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit		
SOURCE-DRAIN DIODE CHARACTERISTICS								
Reverse Recovery Time	t _{RR}	$V_{GS} = -3/18 \text{ V}, I_{SD} = 40 \text{ A},$ $dI_S/dt = 1000 \text{ A}/\mu\text{s}, V_{DS} = 800 \text{ V}$	-	24	-	ns		
Reverse Recovery Charge	Q _{RR}	di _S /dt = 1000 A/μs, V _{DS} = 800 V	-	150	-	nC		
Reverse Recovery Energy	E _{REC}		-	14	-	μJ		
Peak Reverse Recovery Current	I _{RRM}		-	12	-	Α		
Charge time	t _A		-	14	-	ns		
Discharge time	t _B	1	-	11	-	ns		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. E_{ON}/E_{OFF} result is with body diode

TYPICAL CHARACTERISTICS

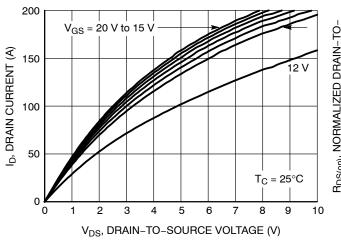


Figure 1. On-Region Characteristics

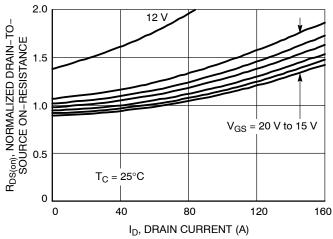


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

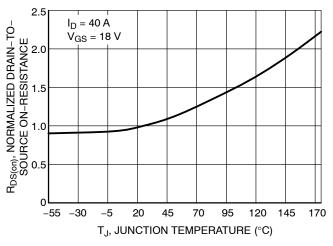


Figure 3. On–Resistance Variation with Temperature

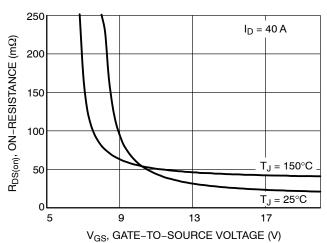


Figure 4. On-Resistance vs. Gate-to-Source Voltage

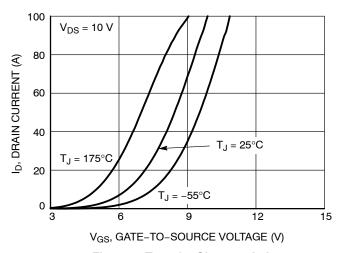


Figure 5. Transfer Characteristics

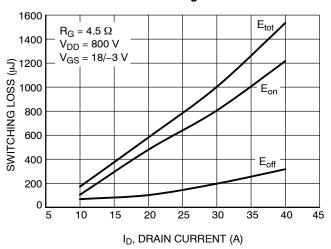


Figure 6. Switching Loss vs. Drain Current

TYPICAL CHARACTERISTICS

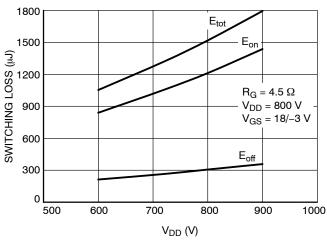


Figure 7. Switching Loss vs. Drain Voltage

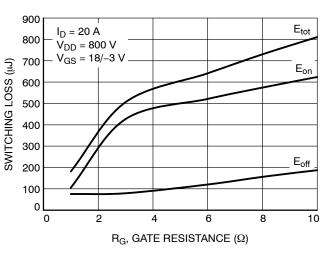


Figure 8. Switching Loss vs. Gate Resistance

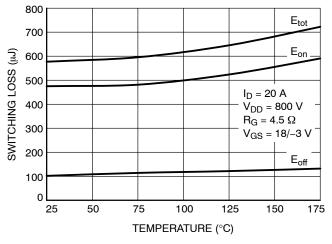


Figure 9. Switching Loss vs. Temperature

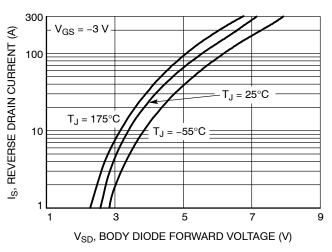


Figure 10. Diode Forward Voltage vs. Current

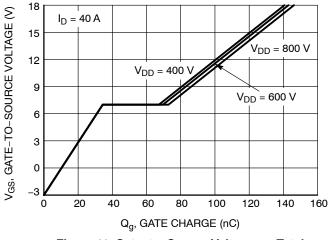


Figure 11. Gate-to-Source Voltage vs. Total Charge

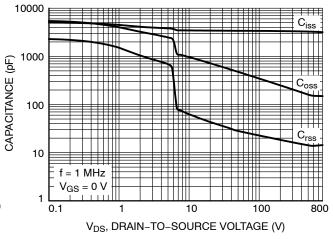
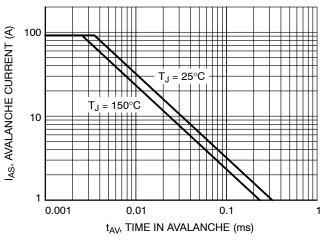


Figure 12. Capacitance vs. Drain-to-Source Voltage

TYPICAL CHARACTERISTICS

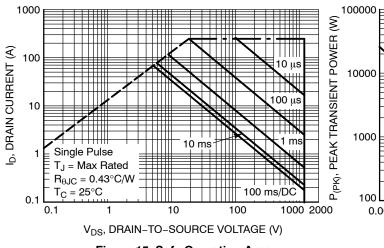
80



(θ) 60 V_{GS} = 18 V V_{GS} = 18 V 20 R_θJ_C = 0.43°C/W 25 50 75 100 125 150 175 T_C, CASE TEMPERATURE (°C)

Figure 13. Unclamped Inductive Switching Capability

Figure 14. Maximum Continuous Drain Current vs. Case Temperature



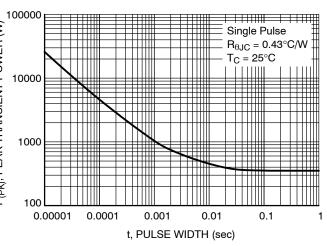


Figure 15. Safe Operating Area

Figure 16. Single Pulse Maximum Power Dissipation

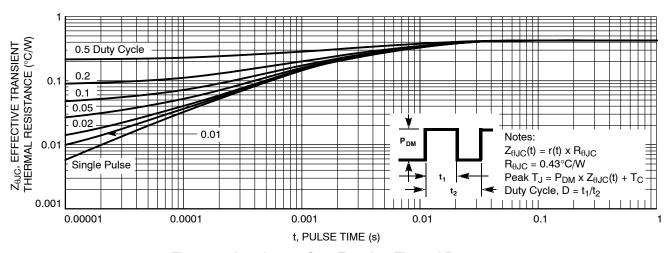
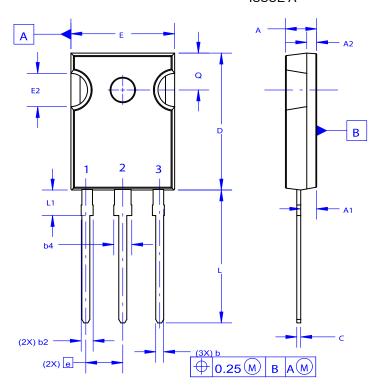


Figure 17. Junction-to-Case Transient Thermal Response

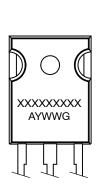
PACKAGE DIMENSIONS

TO-247-3LD CASE 340CX **ISSUE A**



NOTES: UNLESS OTHERWISE SPECIFIED.

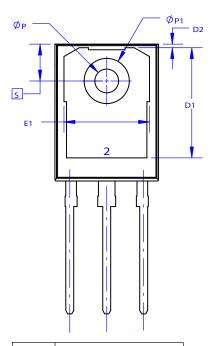
- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
 D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.





XXXXX = Specific Device Code Α = Assembly Location = Year

WW = Work Week G = Pb-Free Package



DIM	MIL	LIMETER	S
DIM	MIN	NOM	MAX
Α	4.58	4.70	4.82
A 1	2.20	2.40	2.60
A2	1.40	1.50	1.60
D	20.32	20.57	20.82
Е	15.37	15.62	15.87
E2	4.96	5.08	5.20
е	~	5.56	~
L	19.75	20.00	20.25
L1	3.69	3.81	3.93
ØΡ	3.51	3.58	3.65
Q	5.34	5.46	5.58
S	5.34	5.46	5.58
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
b4	2.42	2.54	2.66
С	0.51	0.61	0.71
D1	13.08	~	~
D2	0.51	0.93	1.35
E1	12.81	~	~
ØP1	6.60	6.80	7.00

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