

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
100V	62mΩ @ V _{GS} = 10V	4A
	80mΩ @ V _{GS} = 6V	3.5A

Description

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

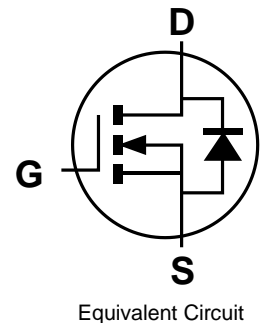
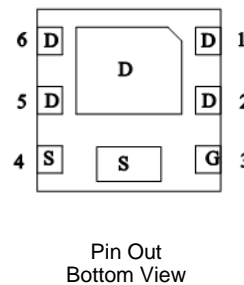
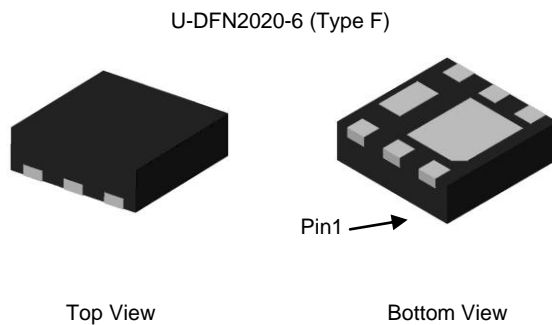
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

Features and Benefits

- 0.6mm Profile – Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low On-Resistance
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **The DMT10H072LFDFQ is suitable for automotive applications requiring specific change control and is AEC-Q101 qualified, is PPAP capable, and is manufactured in IATF16949:2016 certified facilities.**

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.0065 grams (Approximate)



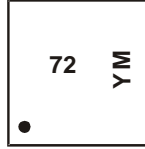
Ordering Information (Note 4)

Part Number	Case	Quantity per Reel
DMT10H072LFDFQ-7	U-DFN2020-6 (Type F)	3,000
DMT10H072LFDFQ-13	U-DFN2020-6 (Type F)	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information

U-DFN2020-6 (Type F)

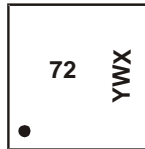


72 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: G = 2019)
 M = Month (ex: 9 = September)

Date Code Key

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027			
Code	G	H	I	J	K	L	M	N	O			
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

U-DFN2020-6 (Type F)



72 = Product Type Marking Code
 YWX = Date Code Marking
 Y = Year (ex: 9 = 2019)
 W = Week (ex: a = week27; z represents week 52 and 53)
 X = Internal Code (ex: U = Monday)

Date Code Key

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	9	0	1	2	3	4	5	6	7

Week	1-26	27-52	53
Code	A-Z	a-z	z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	T	U	V	W	X	Y	Z

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	100	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 7)	T _A = +25°C	4	A
	T _A = +70°C	3.2	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	22	A
Maximum Body Diode Continuous Current	I _S	1.6	A
Avalanche Current, L=0.1mH (Note 5)	I _{AS}	6	A
Avalanche Energy, L=0.1mH (Note 5)	E _{AS}	1.8	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	T _A = +25°C	0.8	W
	T _A = +70°C	0.5	
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	149	°C/W
Total Power Dissipation (Note 7)	T _A = +25°C	1.8	W
	T _A = +70°C	1.1	
Thermal Resistance, Junction to Ambient (Note 7)	R _{θJA}	71	°C/W
Thermal Resistance, Junction to Case (Note 7)	R _{θJC}	13	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

- Notes:
- I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	100	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 80V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	1	—	3	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	47	62	mΩ	V _{GS} = 10V, I _D = 4.5A
		—	54	80		V _{GS} = 6V, I _D = 4A
		—	64	110	mΩ	V _{GS} = 4.5V, I _D = 2.6A
Diode Forward Voltage	V _{SD}	—	0.7	1.0	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{ISS}	—	228	—	pF	V _{DS} = 50V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{OSS}	—	89.3	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	2.5	—	pF	
Gate Resistance	R _g	—	8.2	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	2.5	—	nC	V _{DS} = 50V, I _D = 4.5A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	4.5	—	nC	
Gate-Source Charge	Q _{gs}	—	0.6	—	nC	
Gate-Drain Charge	Q _{gd}	—	1.3	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	3.0	—	ns	V _{DS} = 50V, R _L = 11Ω V _{GS} = 10V, R _{GEN} = 3Ω
Turn-On Rise Time	t _r	—	3.1	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	12.3	—	ns	
Turn-Off Fall Time	t _f	—	4.3	—	ns	
Reverse Recovery Time	t _{RR}	—	22.9	—	ns	I _F = 4.5A, di/dt = 300A/μs
Reverse Recovery Charge	Q _{RR}	—	45.2	—	nC	

Notes: 8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

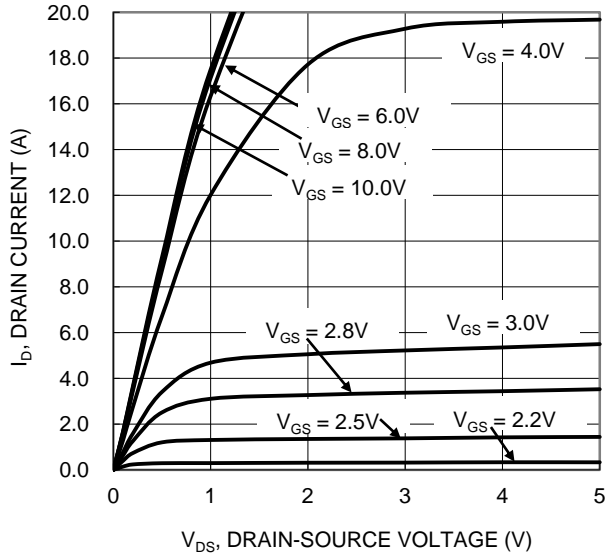


Figure 1. Typical Output Characteristic

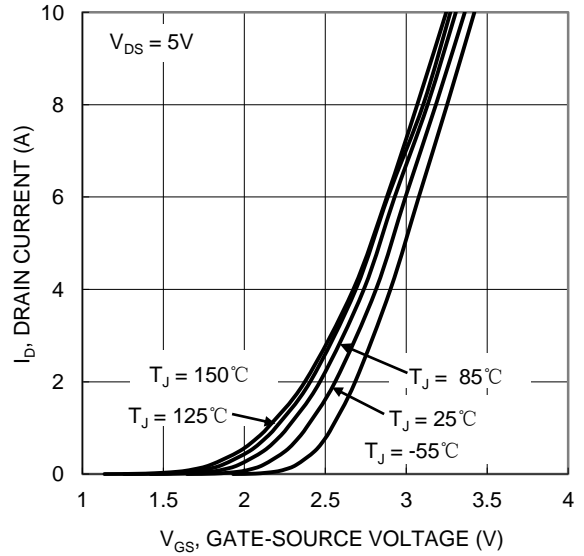


Figure 2. Typical Transfer Characteristic

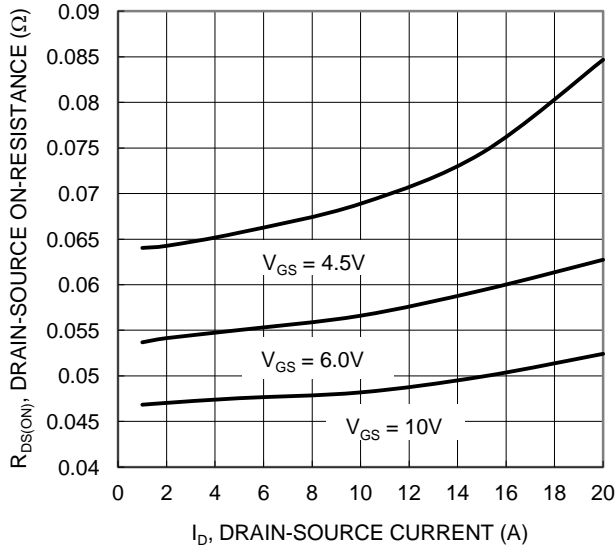


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

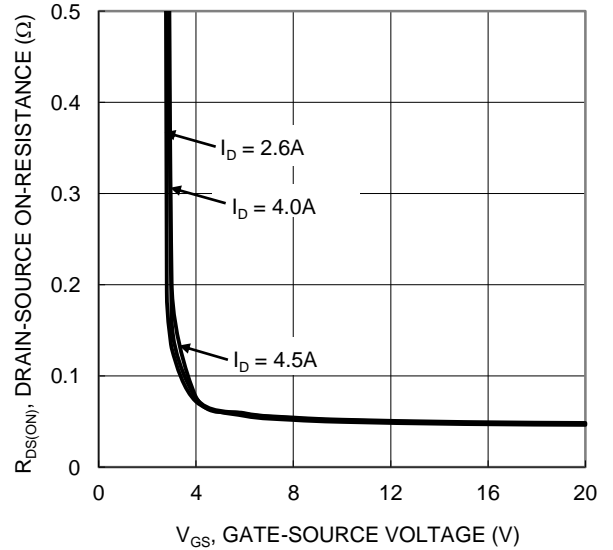


Figure 4. Typical Transfer Characteristic

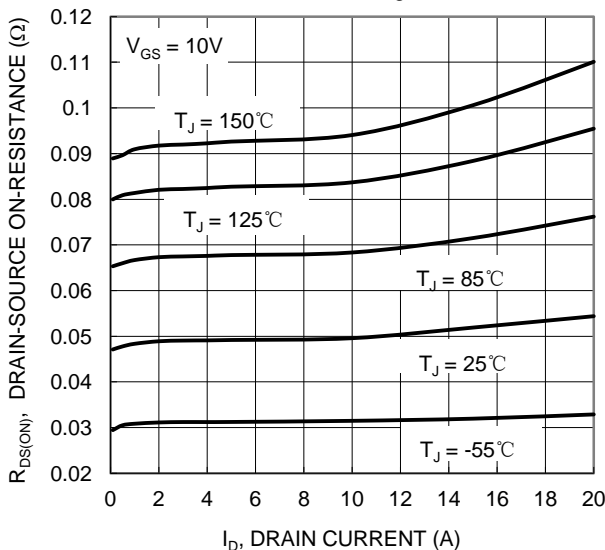


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

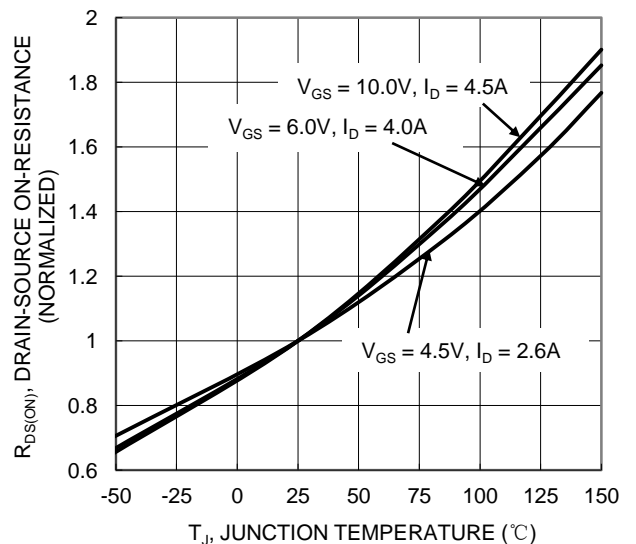


Figure 6. On-Resistance Variation with Junction Temperature

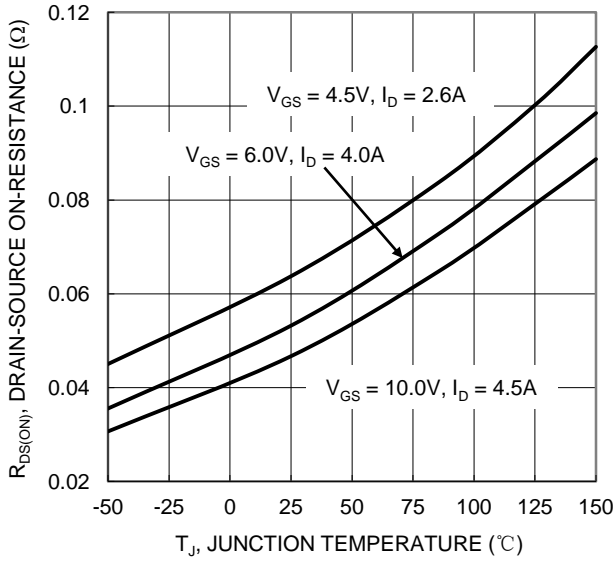


Figure 7. On-Resistance Variation with Junction Temperature

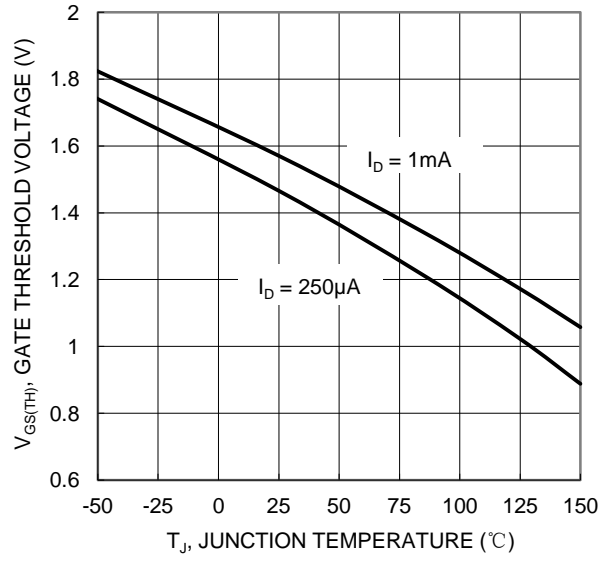


Figure 8. Gate Threshold Variation vs. Junction Temperature

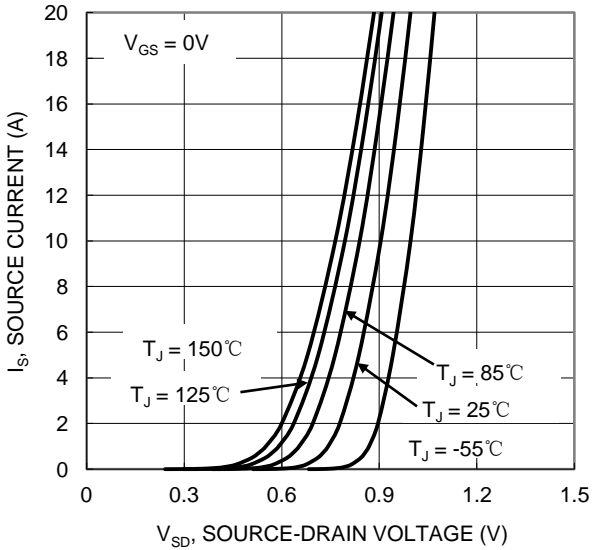


Figure 9. Diode Forward Voltage vs. Current

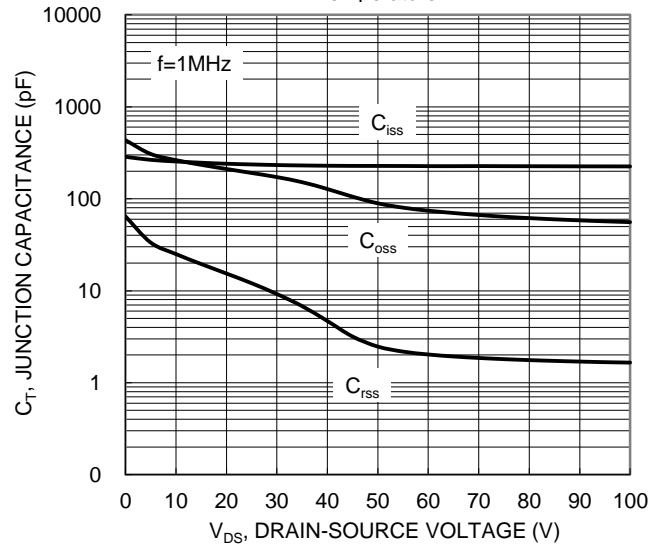


Figure 10. Typical Junction Capacitance

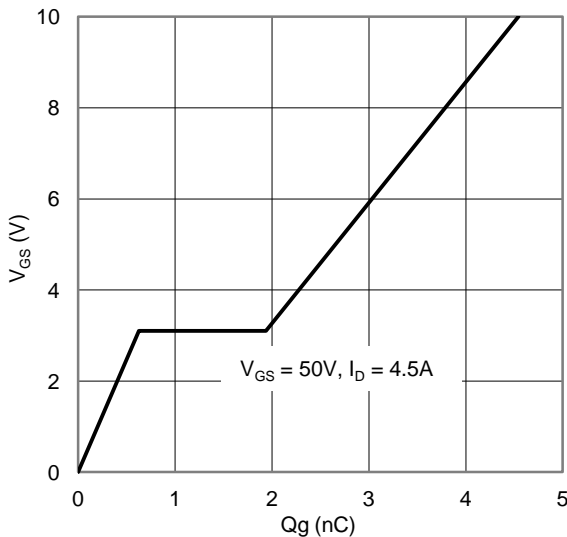


Figure 11. Gate Charge

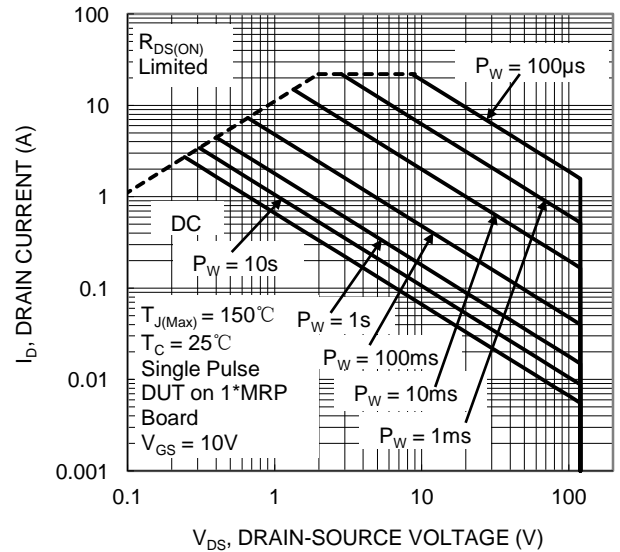


Figure 12. SOA, Safe Operation Area

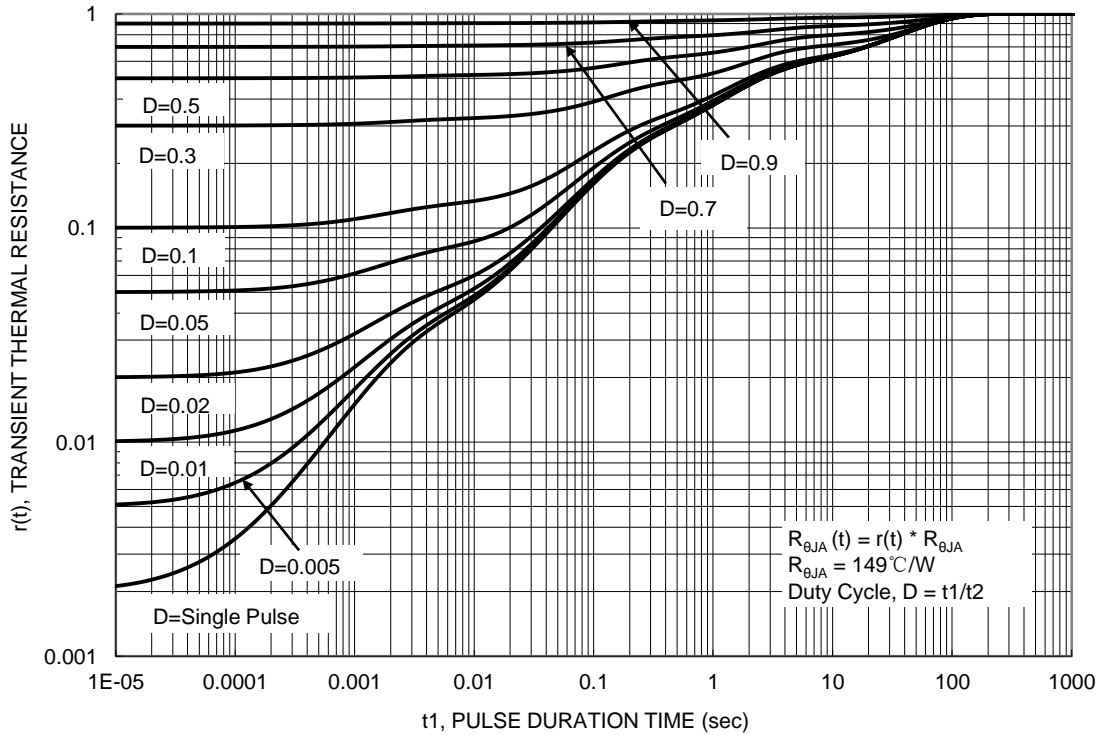
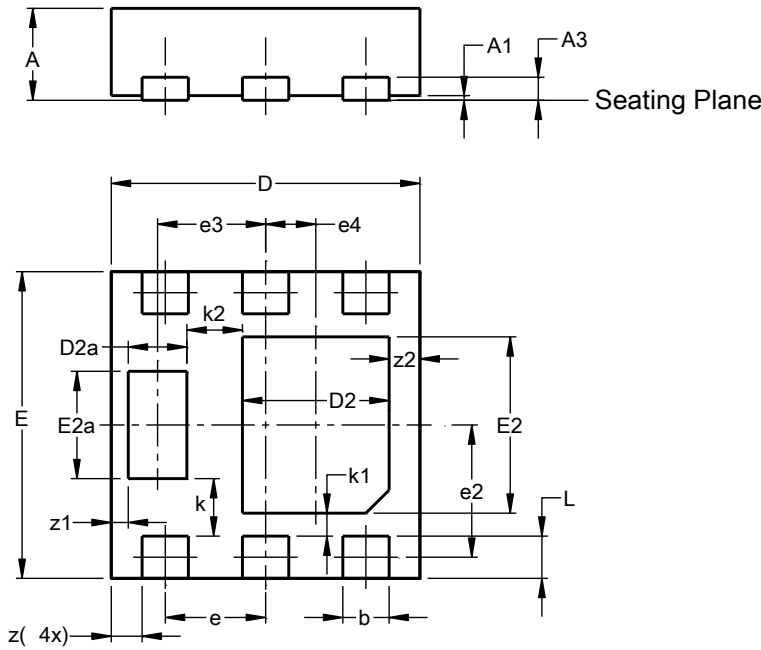


Figure 13. Transient Thermal Resistance

Package Outline Dimension

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (Type F)

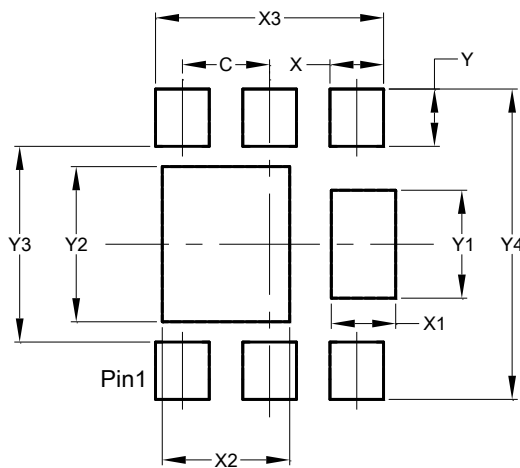


U-DFN2020-6 (Type F)			
Dim	Min	Max	Typ
A	0.57	0.63	0.60
A1	0.00	0.05	0.03
A3	-	-	0.15
b	0.25	0.35	0.30
D	1.95	2.05	2.00
D2	0.85	1.05	0.95
D2a	0.33	0.43	0.38
E	1.95	2.05	2.00
E2	1.05	1.25	1.15
E2a	0.65	0.75	0.70
e	0.65 BSC		
e2	0.863 BSC		
e3	0.70 BSC		
e4	0.325 BSC		
k	0.37 BSC		
k1	0.15 BSC		
k2	0.36 BSC		
L	0.225	0.325	0.275
z	0.20 BSC		
z1	0.110 BSC		
z2	0.20 BSC		
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
C	0.650
X	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300

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