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2014年2月

FCP260N60E / FCPF260N60E

N 沟道 SuperFET[®] II Easy-Drive MOSFET 600 V, 15 A, 260 m Ω

特性

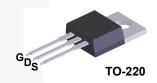
- 650 V @ T_J = 150°C
- 典型值 R_{DS(on)} = 220 mΩ
- 超低栅极电荷 (典型值 Q_q = 48 nC)
- 低有效输出电容 (典型值 Coss(eff.)= 129 pF)
- 100% 经过雪崩测试
- 集成栅极电阻
- 符合 RoHS 标准

应用

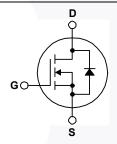
- LCD / LED / PDP 电视灯光
- 光伏逆变器
- AC-DC 电源

说明

SuperFET® II MOSFET 是飞兆半导体新一代利用电荷平衡技术实现出色低导通电阻和更低栅极电荷性能的高压超级结 (SJ) MOSFET 系列产品。这项技术专用于最小化导通损耗并提供卓越的开关性能、dv/dt额定值和更高雪崩能量。类似地,与SuperFET II MOSFET 系列相比,SuperFET II MOSFET Easy-Drive 系列具有略为缓慢的上升和下降时间。该系列产品型号以 "E" 作为前缀,有助于解决 EMI 问题,设计部署更为简单。如需获得更快的开关速度并用于开关损耗必须尽可能低的应用中,请考虑使用SuperFET II MOSFET 系列。







MOSFET 最大额定值 T_C = 25℃ 除非另有说明。

符号		参数		FCP260N60E	FCPF260N60E	单位
V _{DSS}	漏极一源极电压			600		V
V		- DC			20	V
V_{GSS}	栅极一源极电压	- AC	- AC (f > 1 Hz)		:30	V
	P和本本	- 连续 (T _C = 25°C)		15	15*	
I _D	漏极电流	- 连续 (T _C = 100°C)		9.5	9.5*	Α
I _{DM}	漏极电流	- 脉冲	(说明 1)	45	45*	Α
E _{AS}	单脉冲雪崩能量		(说明 2)	29	2.5	mJ
I _{AR}	雪崩电流		(说明 1)	3.0		Α
E _{AR}	重复雪崩能量		(说明 1)	1	.56	mJ
dv/dt	MOSFET dv/dt	MOSFET dv/dt		100		V/ns
av/at	二极管恢复 dv/dt 峰值		(说明3)	2	20	V/IIS
D	T-L - ±1	(T _C = 25°C)		156	36	W
P_{D}	功耗 - 降低至 25°C 以上		1.5	1.25	0.29	W/°C
T _J , T _{STG}	工作和存储温度范围			-55 至 +150		°C
TL	用于焊接的最大引线温度,	距离外壳 1/8",持续 5 秒		3	00	°C
*漏极电流受	限于最大结温					

热性能

符号	参数	FCP260N60E	FCPF260N60E	单位
$R_{\theta JC}$	结至外壳热阻最大值	0.8	3.5	°C/W
$R_{\theta JA}$	结至环境热阻最大值	62.5	62.5	5

封装标识与定购信息

器件编号	顶标	封装	包装方法	卷尺寸	带宽	数量
FCP260N60E	FCP260N60E	TO-220	塑料管	不适用	不适用	50 个
FCPF260N60E	FCPF260N60E	TO-220F	塑料管	不适用	不适用	50 个

测试条件

最小值 典型值 最大值

单位

电气特性 T_C = 25°C 除非另有说明。

关断特性						
D\/		$V_{GS} = 0 \text{ V}, I_D = 10 \text{ mA}, T_J = 25^{\circ}\text{C}$	600	-	-	V
BV _{DSS}	√ _{DSS} 漏极一源极击穿电压	$V_{GS} = 0 \text{ V}, I_D = 10 \text{ mA}, T_J = 150^{\circ}\text{C}$	650	-	-	V
ΔBV _{DSS} / ΔT _J	击穿电压温度系数	I _D =10 mA,参考条件为 25°C	-	0.67	-	V/°C
BV _{DS}	漏极 - 源极雪崩击穿电压	$V_{GS} = 0 \text{ V}, I_D = 15 \text{ A}$	-	700	-	V
1	重柳拱中区温林中 茨	V _{DS} = 480 V, V _{GS} = 0 V		-	1	
I _{DSS}	零栅极电压漏极电流	V _{DS} = 480 V, T _C = 125°C	-	-	10	μΑ
I _{GSS}	栅极 - 体漏电流	V _{GS} = ±20 V, V _{DS} = 0 V	-	-	±100	nA

导通特性

符号

V _{GS(th)}	栅极阈值电压	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	2.5	-	3.5	V
R _{DS(on)}	漏极至源极静态导通电阻	$V_{GS} = 10 \text{ V}, I_D = 7.5 \text{ A}$	-	0.22	0.26	Ω
9 _{FS}	正向跨导	$V_{DS} = 20 \text{ V}, I_{D} = 7.5 \text{ A}$	-	15.5	-	S

动态特性

C _{iss}	输入电容	V - 25 V V - 0 V	-	1880	2500	pF
C _{oss}	输出电容	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	-	1330	1770	pF
C _{rss}	反向传输电容	1 171112	-	85	130	pF
C _{oss}	输出电容	$V_{DS} = 380 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	32	-	pF
C _{oss(eff.)}	有效输出电容	V _{DS} = 0 V 至 480 V, V _{GS} = 0 V	-	129	-	pF
Q _{g(tot)}	10 V 的栅极电荷总量	V _{DS} = 380 V, I _D = 7.5 A,	-	48	62	nC
Q_{gs}	栅极 - 源极栅极电荷	V _{GS} = 10 V	-	7.4	-	nC
Q_{gd}	栅极 - 漏极 " 米勒 " 电荷	(说明 4)	-	17	-	nC
ESR	等效串联电阻	f = 1 MHz	- /	5.8	-	Ω

开关特性

t _{d(on)}	导通延迟时间		-	20	50	ns
t _r	开通上升时间	$V_{DD} = 380 \text{ V}, I_D = 7.5 \text{ A},$	-	11	32	ns
t _{d(off)}	关断延迟时间	$V_{GS} = 10 \text{ V}, R_G = 4.7 \Omega$	-	89	188	ns
t _f	关断下降时间	(说明 4)	-	13	36	ns

漏极 - 源极二极管特性

I _S	漏极 - 源极二极管最大正向连续电	·流	-	-	15	Α
I _{SM}	漏极 - 源极二极管最大正向脉冲电	流	-	-	45	Α
V_{SD}	漏极 - 源极二极管正向电压	$V_{GS} = 0 \text{ V}, I_{SD} = 7.5 \text{ A}$	-	-	1.2	V
t _{rr}	反向恢复时间	V _{GS} = 0 V, I _{SD} = 7.5 A,	-	270	-	ns
Q_{rr}	反向恢复电荷	$dI_F/dt = 100 A/\mu s$	-	3.6	-	μС

注意:

- 1. 重复额定值: 脉冲宽度受限于最大结温。
- 2. I_{AS} = 3 A, V_{DD} = 50 V, R_G = 25 Ω ,启动 T_J = 25 $^{\circ}$ C。
- 1.00
- 4. 本质上独立于工作温度的典型特性。

典型性能特征

图 1. 导通区域特性

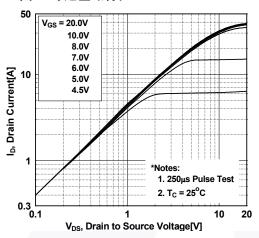


图 3. 导通电阻变化与漏极电流和栅极电压

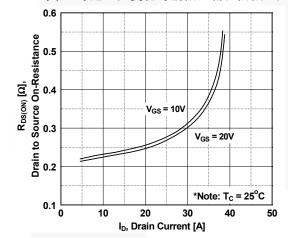


图 5. 电容特性

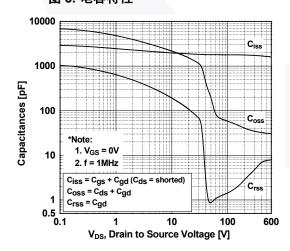


图 2. 传输特性

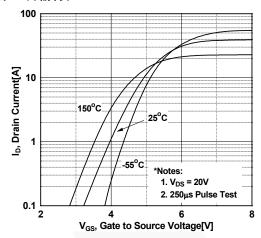


图 4. 体二极管正向电压变化与源极电流和温度

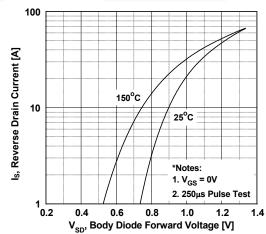
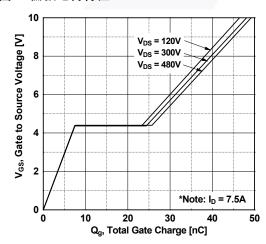


图 6. 栅极电荷特性



典型性能特征 (接上页)

图 7. 击穿电压变化与温度

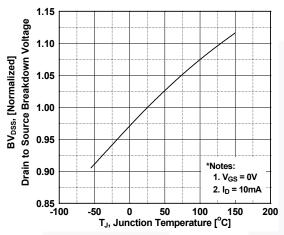


图 9. 最大安全工作区(FCP260N60E)

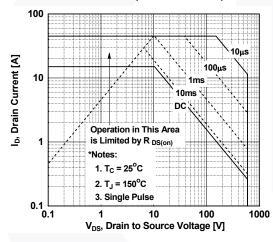


图 11. 最大漏极电流与壳体温度的关系

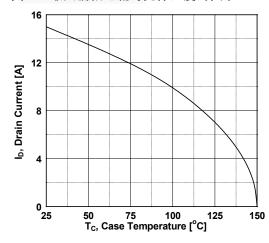


图 8. 导通电阻变化 vs. 温度

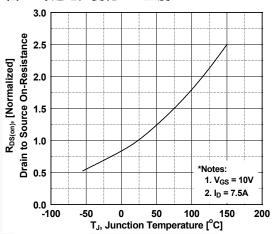


图 10. 最大安全操作区 (FCPF260N60E)

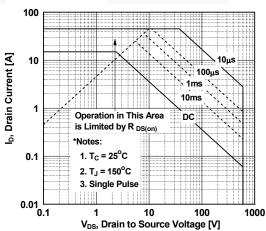
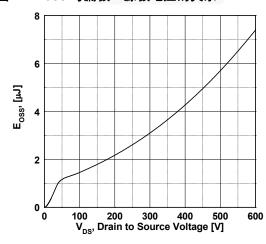


图 12. Eoss 与漏极一源极电压的关系



典型性能特征 (接上页)

图 13. 瞬态热响应曲线 (FCP260N60E)

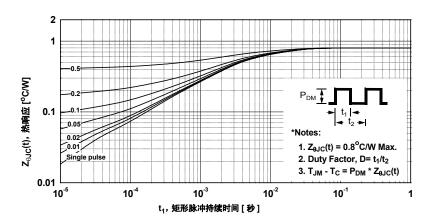
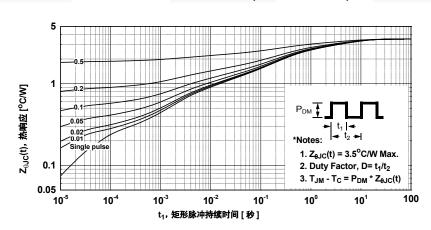


图 14. 瞬态热响应曲线 (FCPF260N60E)



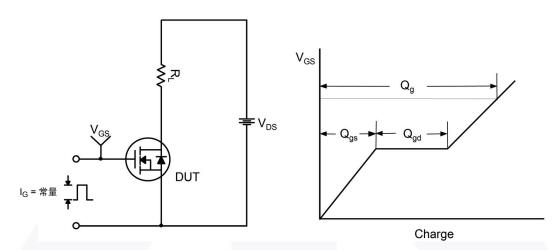


图 15. 栅极电荷测试电路与波形

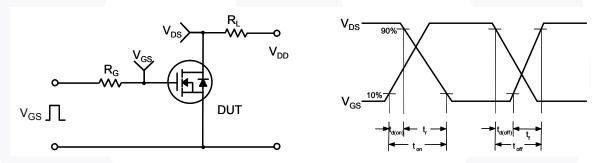


图 16. 阻性开关测试电路与波形

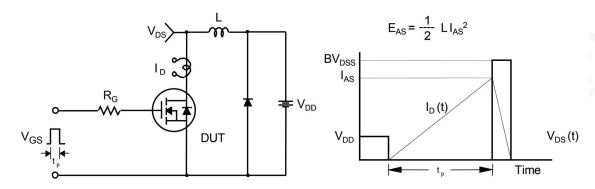
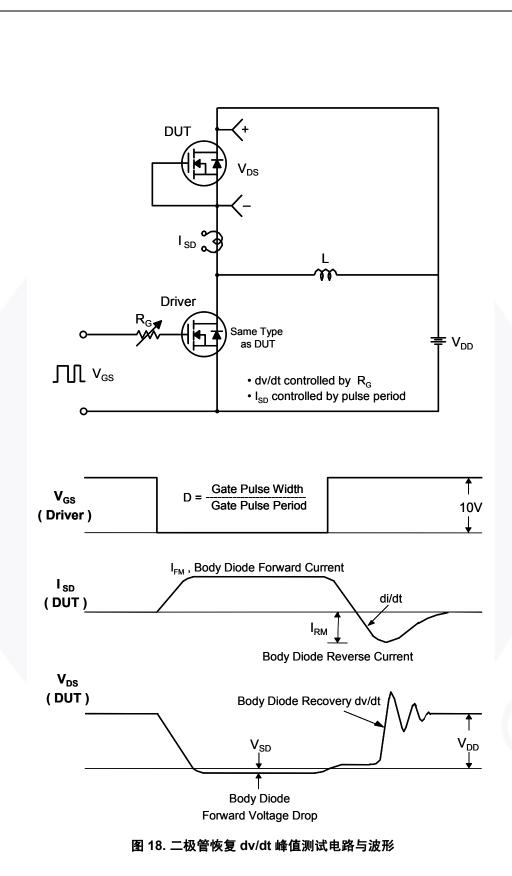
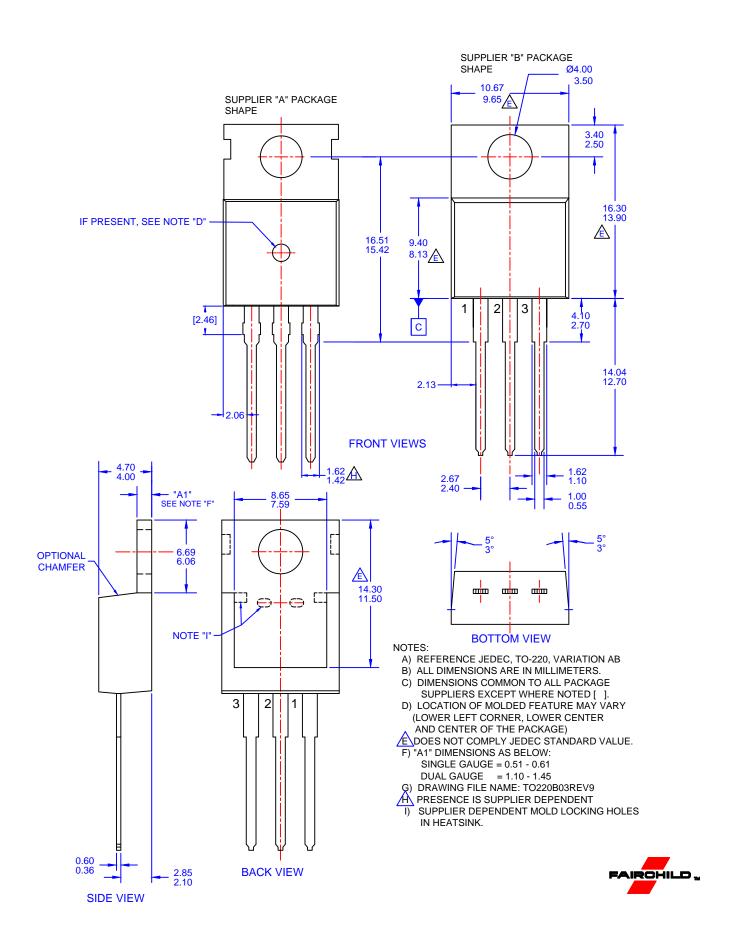
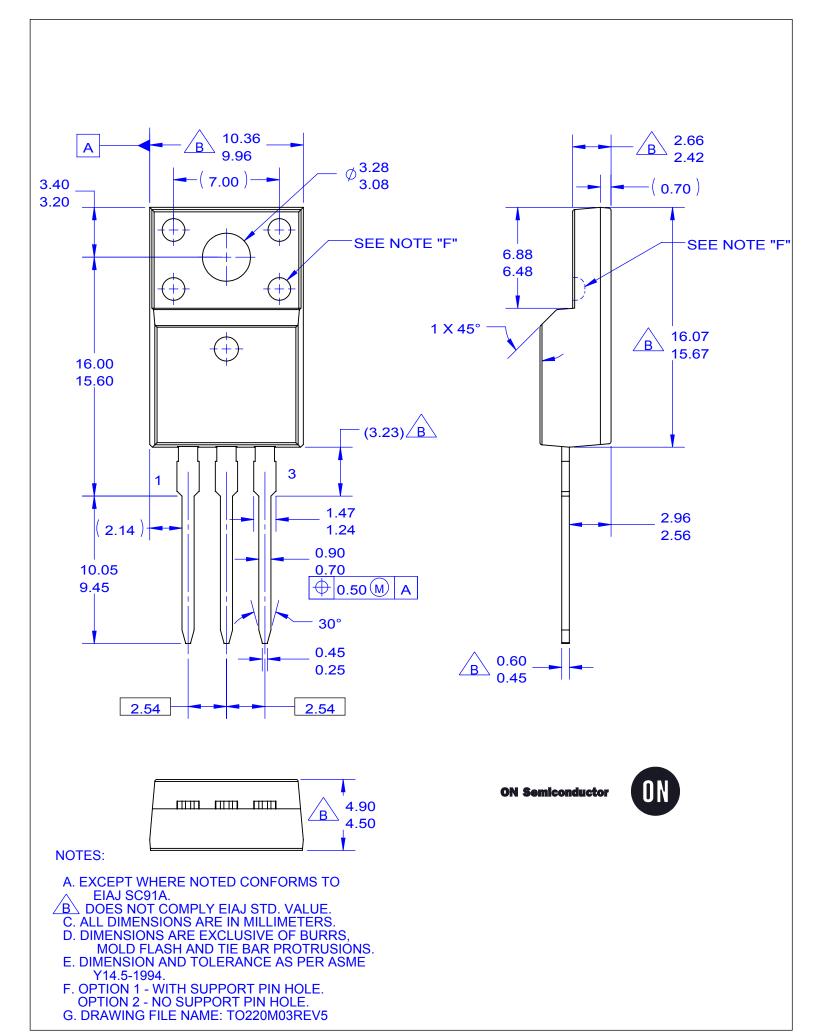


图 17. 非箝位感性开关测试电路与波形







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