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FDPF085N10A N 沟道 PowerTrench[®] MOSFET 100 V, 40 A, 8.5 mΩ

特性

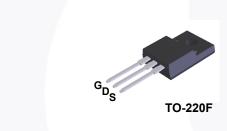
- ・ R_{DS(on)} = 6.5 mΩ (典型值) @ V_{GS} = 10 V, I_D = 40 A
- 快速开关速度
- 低栅极电荷, Q_G = 31 nC (典型值)
- 高性能沟道技术可实现极低的 R_{DS(on)}
- 高功率和高电流处理能力
- ・ 符合 RoHS 标准

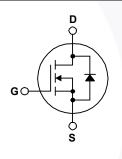
说明

此 N 沟道 MOSFET 采用飞兆半导体先进的 PowerTrench[®] 工艺 生产,这一先进工艺是专为最大限度地降低导通电阻并保持卓越 开关性能而定制的。

应用

- 消费电子设备
- LED TV
- 用于 ATX/ 服务器 / 电信 PSU 的同步整流
- 电机驱动和不间断电源
- 微型太阳能逆变器





最大绝对额定值 T_C=25°C 除非另有说明。

符号		参数	FDPF085N10A	单位	
V _{DSS}	漏极一源极电压	100	V		
V _{GSS}	栅极一源极电压		±20	V	
I _D 漏极电流	记忆中达	- 连续 (T _C = 25°C)	40	Α	
	漏极 电流	- 连续 (T _C = 100°C)	28		
I _{DM}	漏极电流	- 脉冲 (说明 1)	160	А	
E _{AS}	单脉冲雪崩能量	(说明 2)	269	mJ	
dv/dt	二极管恢复 dv/dt 峰值	(说明3)	6.0	V/ns	
P _D 功耗	-1 +7	(T _C = 25°C)	33.3	W	
	切耗	- 降低至 25°C 以上	0.22	W/°C	
T _J , T _{STG}	工作和存储温度范围		-55 至 +175	°C	
TL	用于焊接的最大引线温度,距离外壳 1/8",持续 5 秒		300	°C	

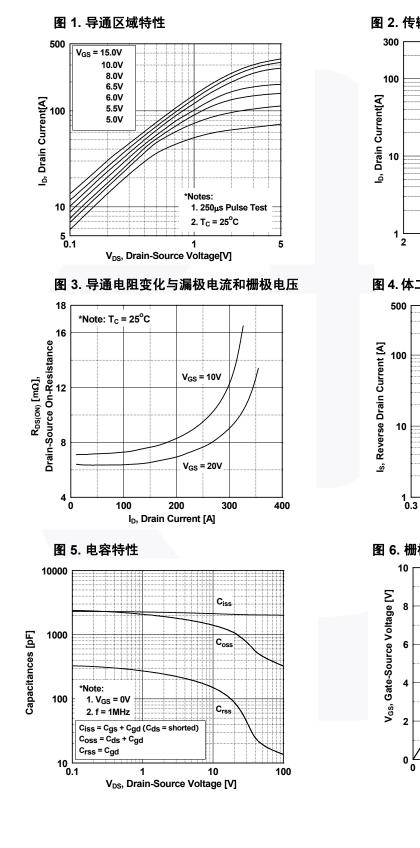
热性能

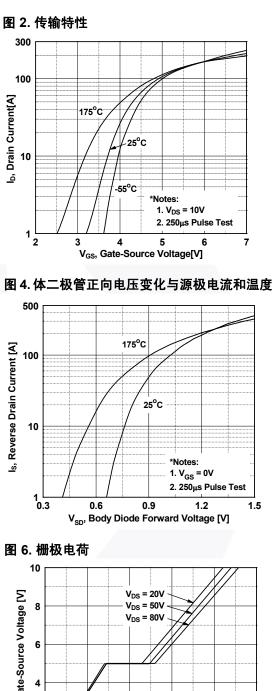
符号	参数	FDPF085N10A	单位
$R_{ ext{ heta}JC}$	结至外壳热阻最大值	4.5	°C/W
$R_{ hetaJA}$	结至环境热阻最大值	62.5	C/W

2014年1月

		封装	包装方法	卷尺寸		带宽	ž	数量	
		TO-220F							
					1 2010		1 ~2/13	_	<u> </u>
电气特性	T _C = 25°C	除非另有说明。				1		1	
符号		参数		测试条件		最小值	典型值	最大值	单位
关断特性									
BV _{DSS}	漏极一源	极击穿电压	I	I _D = 250 μA, V _{GS} = 0 V		100	-	-	V
ΔBV _{DSS} / ΔΤ.	击穿电压			I _D = 250 μA, 参考温度为 25°C		-	0.07	-	V/°C
				/ _{DS} = 80 V, V _{GS} = 0 V		-	-	1	
DSS	零栅极电	压漏极电流		$V_{DS} = 80 \text{ V}, \text{ T}_{C} = 150^{\circ}\text{C}$			-	500	μA
I _{GSS}	栅极 - 体法	漏电流	V	/ _{GS} = ±20 V, V _{DS} = 0 V		-	-	±100	nA
导通特性									
V _{GS(th)}	栅极阈值	电压	١	/ _{GS} = V _{DS} , I _D = 250 μA		2.0	-	4.0	V
R _{DS(on)}	漏极至源	极静态导通电阻	١	/ _{GS} = 10 V, I _D = 96 A		-	6.5	8.5	mΩ
9 _{FS}	正向跨导		١	/ _{DS} = 10 V, I _D = 96 A		-	76	-	S
动态特性									
C _{iss}	输入电容				-	2025	2695	pF	
C _{oss}	输出电容			V _{DS} = 50 V, V _{GS} = 0 V, f = 1 MHz		-	468	620	pF
C _{rss}	反向传输	电容	1			-	20	-	pF
C _{oss} (er)	能量相关输出电容		١	V_{DS} = 50 V, V_{GS} = 0 V		-	752	-	pF
Q _{g(tot)}	10 V 的栅极电荷总量 栅极 - 源极栅极电荷			V _{GS} = 10 V, V _{DS} = 50 V, I _D = 96 A		-	31	40	nC
Q _{gs}						-	9.7	-	nC
Q _{gs2}	栅极平台	栅极平台电荷阈值				-	5.0	-	nC
Q _{gd}		极 " 米勒 " 电荷		(说明4		-	7.5	-	nC
ESR	等效串联	电阻 (G-S)	f	f = 1 MHz		-	0.97	-	Ω
开关特性									
t _{d(on)}	导通延迟	时间				-	18	46	ns
t _r	开通上升	时间		$I_{\rm DD} = 50 \text{ V}, I_{\rm D} = 96 \text{ A},$		-	22	54	ns
t _{d(off)}	关断延迟	时间	1	/ _{GS} = 10 V, R _G = 4.7 Ω		1	29	68	ns
t _f	关断下降时间		(说明4)	-	8	26	ns		
漏极 - 源极	}二极管特	性							
I _S	漏极 - 源极二极管最大正向连续电流					-	-	40	Α
I _{SM}	漏极 - 源极二极管最大正向脉冲电流					-	-	160	Α
V _{SD}	漏极 - 源极二极管正向电压		V	V _{GS} = 0 V, I _{SD} = 96 A		-	-	1.3	V
t _{rr}	反向恢复	时间	V	/ _{DD} = 50 V,V _{GS} = 0 V, I _{SD}	= 96 A,	-	59	-	ns
Q _{rr}	反向恢复	电荷	d	II _F /dt = 100 A/μs		-	80	-	nC

典型性能特征





FDPF085N10AN 沟道 PowerTrench[®] MOSFET

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*Note: I_D = 96A

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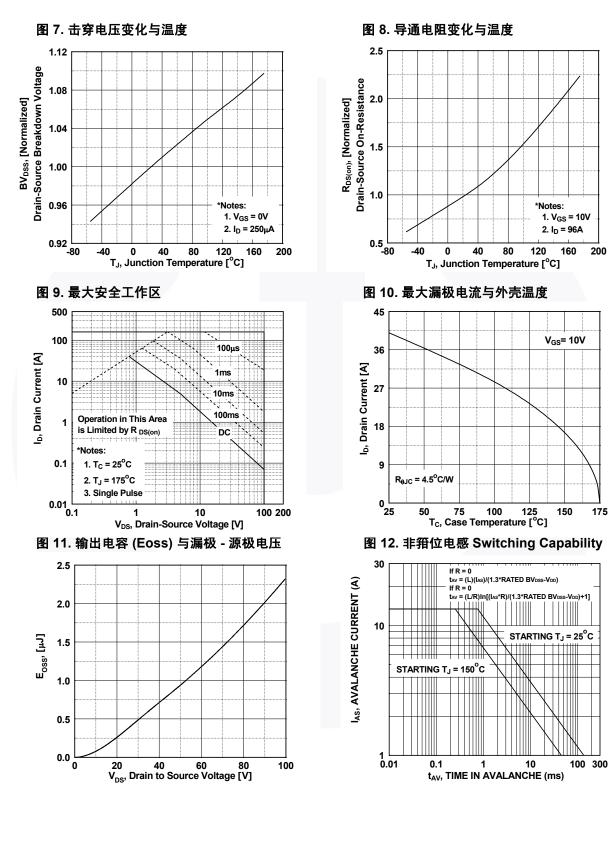
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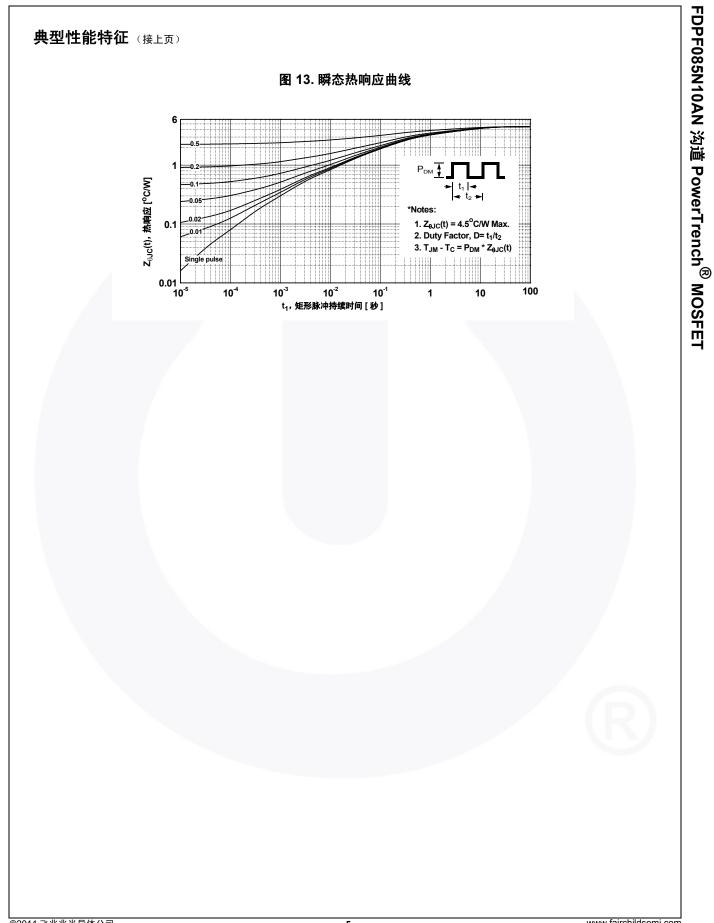
Q_g, Total Gate Charge [nC]

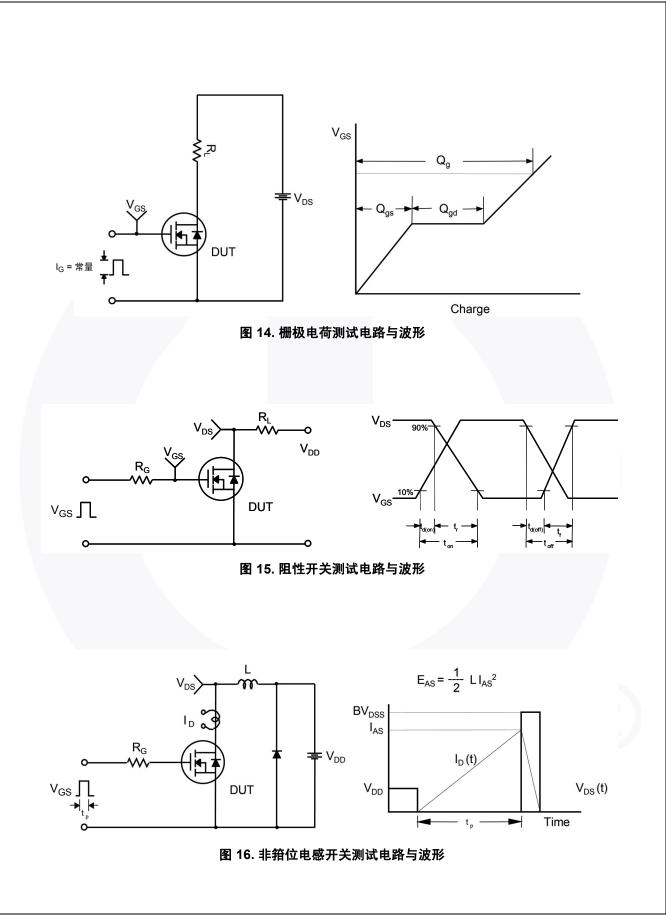
典型性能特征 (接上页)



FDPF085N10AN 沟道 PowerTrench[®] MOSFET

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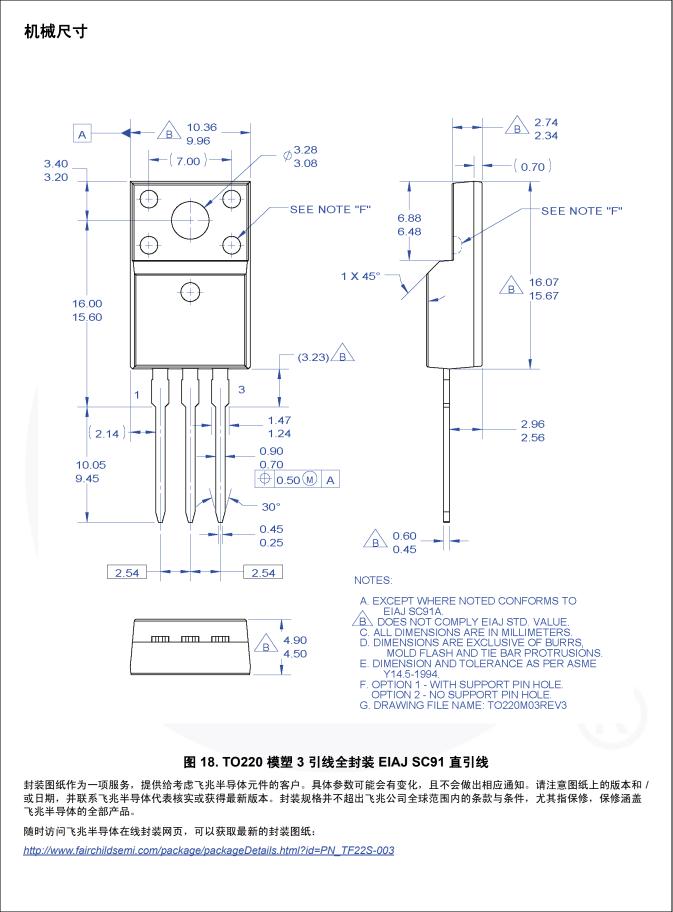




FDPF085N10AN 沟道 PowerTrench[®] MOSFET

DUT + v_{DS} ۱_{SD} م a L Driver R_G Same Type as DUT L F ∨_{DD} ∏∏ v_{gs} • dv/dt controlled by R_{G} • I_{SD} controlled by pulse period ſ Gate Pulse Width V_{GS} D = Gate Pulse Period 10V (Driver) \mathbf{I}_{FM} , Body Diode Forward Current I _{SD} di/dt (DUT) I_{RM} Body Diode Reverse Current V_{DS} (DUT) Body Diode Recovery dv/dt V_{SD} V_{DD} Body Diode Forward Voltage Drop 图 17. 二极管恢复 dv/dt 峰值测试电路与波形

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