

# MOSFET - N-Channel, POWERTRENCH®, GreenBridge™ Series of High-Efficiency Bridge Rectifiers

100 V, 6 A, 110 m $\Omega$ 

## **FDMQ8403**

## **General Description**

This quad MOSFET solution provides ten-fold improvement in power dissipation over diode bridge.

#### **Features**

- Max  $r_{DS(on)} = 110 \text{ m}\Omega$  at  $V_{GS} = 10 \text{ V}$ ,  $I_D = 3 \text{ A}$
- Max  $r_{DS(on)} = 175 \text{ m}\Omega$  at  $V_{GS} = 6 \text{ V}$ ,  $I_D = 2.4 \text{ A}$
- Substantial Efficiency Benefit in PD Solutions
- This Device is Pb-Free, Halid Free and is RoHS Compliant

## **Applications**

• High-Efficiency Bridge Rectifiers

## MOSFET MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise noted)

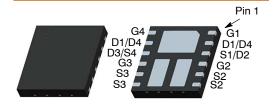
Symbol	Rating		Value	Unit
V <sub>DS</sub>	Drain to Source Voltage	100	V	
V <sub>GS</sub>	Gate to Source Voltage	±20	V	
I <sub>D</sub>	Drain Current - Continuous (Package Limited) - Continuous (Silicon Limited) - Continuous (Note 1a.) - Pulsed	T <sub>C</sub> = 25°C T <sub>C</sub> = 25°C T <sub>A</sub> = 25°C	6 9 3.1 12	A
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25°C	17	W
	Power Dissipation (Note 1a.)	T <sub>A</sub> = 25°C	1.9	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Range	-55 to +150	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## THERMAL CHARACTERISTICS

Symbol	Rating	Value	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1a.)	65	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient (Note 1b.)	135	

V <sub>DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
100 V	110 Ω @ 10 V	6 A



WDFN12 5 x 4.5, 0.8P (MLP 4.5 x 5) CASE 511CR

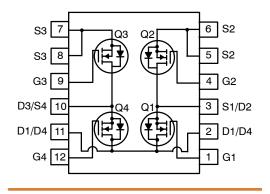
#### **MARKING DIAGRAM**

\$Y&Z&2&K FDMQ 8403

FDMQ8403 = Specific Device Code \$Y = **onsemi** Logo &Z = Assembly plant code

&2 = Date Code format (Year and Week)&K = Lot Run Traceability Code

#### **PIN CONNECTION**



#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 5 of this data sheet.

## **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise noted)

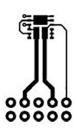
Symbol	Parameter	Test Co	ondition	Min	Тур	Max	Unit
FF CHAR	ACTERISTICS	•		•	•	•	
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		100	_	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, Referenced to 25°C		-	72	-	mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 8	-	-	1	nA	
I <sub>GSS</sub>	Gate to Source Leakage Current	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V		-	-	±100	μΑ
OFF CHAR	ACTERISTICS			•		•	
V <sub>GS(th)</sub>	Drain to Source Breakdown Voltage	$V_{GS} = V_{DS}$ , $I_D = 25$	60 μΑ	2	2.8	4	V
$\frac{\Delta V_{GS(th)}}{\Delta T_{I}}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, Referenced to 25°C		-	-8	_	mV/°C
r <sub>DS(on)</sub>			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A		85	110	mΩ
, ,		V <sub>GS</sub> = 6 V, I <sub>D</sub> = 2.4 A		-	115	175	-
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A, T <sub>J</sub> = 125°C		-	147	191	
9FS	Forward Transconductance	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 3 A		-	6	-	S
OYNAMIC (	CHARACTERISTICS				•	•	•
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		-	162	215	pF
C <sub>oss</sub>	Output Capacitance			_	43	60	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			-	2.6	5	pF
OYNAMIC (	CHARACTERISTICS						
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 50 V, I <sub>D</sub> = 3		-	4.1	10	ns
t <sub>r</sub>	Rise Time	$V_{GS}$ = 10 V, $R_{GEN}$	= 6 Ω	-	1.2	10	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			-	7.2	15	ns
t <sub>f</sub>	Fall Time			-	1.8	10	ns
Qg	Total Gate Charge	V <sub>GS</sub> = 0 V to 10 V		-	3	5	nC
Qg	Total Gate Charge	V <sub>GS</sub> = 0 V to 5 V	I <sub>D</sub> = 3 A	-	1.7	3	nC
Q <sub>gs</sub>	Gate to Source Charge	V <sub>DD</sub> = 50 V, I <sub>D</sub> = 3 A		-	0.9	-	nC
$Q_{gd}$	Gate to Drain "Miller" Charge			-	0.8	-	nC
DRAIN-SO	URCE DIODE CHARACTERISTICS						
$V_{SD}$	Source to Drain Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 3 A (Note 2)		-	0.86	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 3 A, di/dt = 100 A/μs		-	33	53	ns
Q <sub>rr</sub>	Reverse Recovery Charge			-	23	37	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

<sup>1.</sup>  $R_{\theta JA}$  is determined with the device mounted on a 1in<sup>2</sup> pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material.  $R_{\theta JC}$  is guaranteed by design while  $R_{\theta CA}$  is determined by the user's board design.



a. 65°C/W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper, the board designed Q1 + Q3 or Q2 + Q4.



b.  $135^{\circ}$ C/W when mounted on a minimum pad of 2 oz copper, the board designed Q1 + Q3 or Q2 + Q4.

2. Pulse Test: Pulse Width < 300  $\mu$ s, Duty cycle < 2.0%.

## TYPICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted.

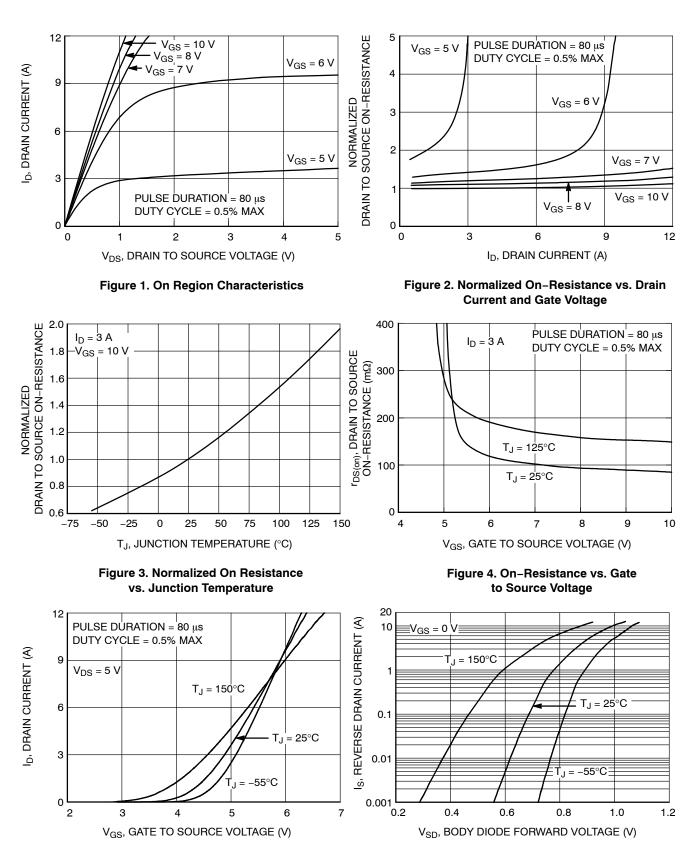


Figure 5. Transfer Characteristics

Figure 6. Source to Drain Diode Forward Voltage vs. Source Current

## TYPICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted) (continued)

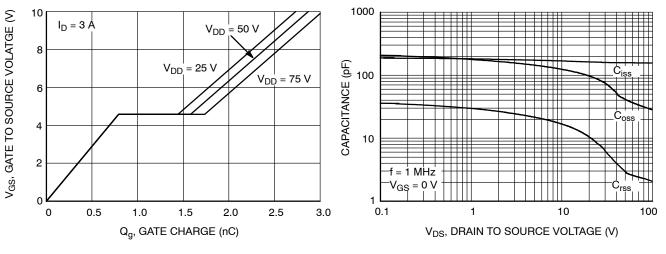


Figure 7. Gate Charge Characteristics

Figure 8. Capacitance vs. Drain to Source Voltage

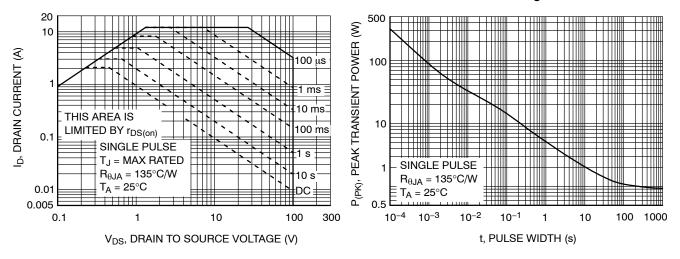


Figure 9. Forward Bias Safe Operating Area

Figure 10. Single Pulse Maximum Power Dissipation

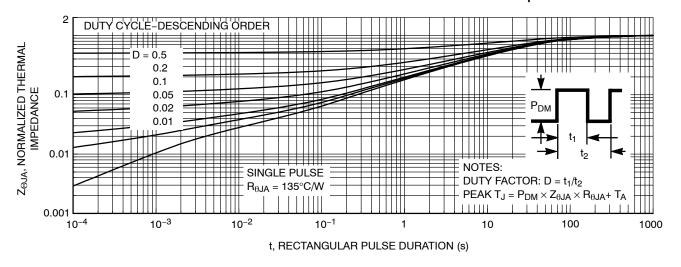


Figure 11. Junction-to-Ambient Transient Thermal Response Curve

## **ORDERING INFORMATION**

Device Marking	Device	Package	Reel Size	Tape Width	Shipping <sup>†</sup>
FDMQ8403	FDMQ8403	WDFN12 (Pb-Free)	13"	12 mm	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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PIN#1 IDENT AREA

> PIN#1 IDENT

4.60

4.40

2.25

(0.50)2X

## WDFN12 5x4.5, 0.8P CASE 511CR

В

4.50

2X

-(0.35)

1.05 0.95(2X)

(0.50)

0.55

Φ

0.10(M) C A B

0.05**M** 

**ISSUE A** 

**DATE 21 MAR 2017** 4.45 2.22 1.78 (0.40)2 17 (0.50)2X1.00(2X) 2.50 (0.50)4.80 2.40 (0.05)(0.65)0.10 C 0.40 (12X)0.80

0.80 MAX // 0.10 C (0.20)0.08 C ᆫ 0.05 0.00 SIDE VIEW SEATING PLANE 5.10 4.90 4.35 4.25 1.75 2.15

1.65

(0.45)

- 2.50

12

0.80

**TOP VIEW** 

2.05

(0.05)

6

5.00

RECOMMENDED LAND PATTERN

## NOTES:

- A) THIS MKT. DWG. DOES NOT FULLY **CONFORM TO JEDEC MO-229** REGISTRATION
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.

**BOTTOM VIEW** 

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0.35 0.25(12X)

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