

# TinyLogic HS Inverter NC7S04

#### **Description**

The NC7S04 is a single high performance CMOS Inverter. Advanced Silicon Gate CMOS fabrication assures high speed and low power circuit operation over a broad  $V_{\rm CC}$  range. ESD protection diodes inherently guard both input and output with respect to the  $V_{\rm CC}$  and GND rails. Three stages of gain between input and output assures high noise immunity and reduced sensitivity to input edge rate.

#### **Features**

- Space-Saving SOT23-5, SC-74A and SC-88A 5-Lead Packages
- Ultra−Small MicroPak™ Leadless Package
- High Speed:  $t_{PD} = 3$  ns Typ
- Low Quiescent Power: I<sub>CC</sub> < 1 μA
- Balanced Output Drive: 2 mA I<sub>OL</sub>, -2 mA I<sub>OH</sub>
- Broad V<sub>CC</sub> Operating Range: 2 V 6 V
- Balanced Propagation Delays
- Specified for 3 V Operation
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

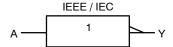
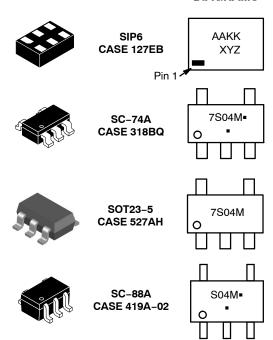


Figure 1. Logic Symbol

#### MARKING DIAGRAMS



AA, 7S04, S04 = Specific Device Code

KK = 2-Digit Lot Run Traceability Code XY = 2-Digit Date Code Format

Z = Assembly Plant Code

M = Date Code\*

\*Date Code orientation and/or position may vary depending upon manufacturing location.

#### **ORDERING INFORMATION**

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

1

## NC7S04

## **Pin Configurations**

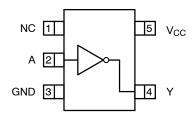


Figure 2. SOT23-5, SC-88A and SC-74A (Top View)

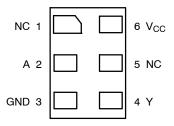


Figure 3. MicroPak (Top Through View)

## **PIN DESCRIPTIONS**

Name	Description
A	Input
Y	Output
NC	No Connect

## **FUNCTION TABLE** $(Y = \overline{A})$

Input	Output
Α	Υ
L	Н
Н	L

H = HIGH Logic Level L = LOW Logic Level

#### NC7S04

#### **ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter		Min	Max	Unit
V <sub>CC</sub>	Supply Voltage		-0.5	6.5	V
I <sub>IK</sub>	DC Input Diode Current	V <sub>IN</sub> < 0 V	-	-20	mA
		V <sub>IN</sub> > V <sub>CC</sub>	-	+20	
V <sub>IN</sub>	DC Input Voltage		-0.5	V <sub>CC</sub> + 0.5	V
I <sub>OK</sub>	DC Output Diode Current V <sub>OUT</sub> < 0 V		-	-20	mA
		V <sub>OUT</sub> > V <sub>CC</sub>	-	+20	
V <sub>OUT</sub>	DC Output Voltage		-0.5	V <sub>CC</sub> + 0.5	V
I <sub>OUT</sub>	DC Output Source or Sink Current		-	±12.5	mA
I <sub>CC</sub> or I <sub>GND</sub>	DC V <sub>CC</sub> or Ground Current per Output Pin		-	±25	mA
T <sub>STG</sub>	Storage Temperature		-65	+150	°C
TJ	Junction Temperature		-	+150	°C
TL	Lead Temperature (Soldering, 10 Seconds)		-	+260	°C
$P_{D}$	Power Dissipation in Still Air	SC-74A / SOT23-5	-	390	mW
		SC-88A	-	332	7
		MicroPak-6	-	812	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage		2.0	6.0	V
V <sub>IN</sub>	Input Voltage		0	V <sub>CC</sub>	V
V <sub>OUT</sub>	Output Voltage		0	V <sub>CC</sub>	V
T <sub>A</sub>	Operating Temperature		-40	+85	°C
t <sub>r</sub> , t <sub>f</sub>	Input Rise and Fall Times	V <sub>CC</sub> at 2.0 V	0	20	ns
		V <sub>CC</sub> at 3.0 V	0	20	
		V <sub>CC</sub> at 4.5 V	0	10	
		V <sub>CC</sub> at 6.0 V	0	5	
$\theta_{\sf JA}$	Thermal Resistance	SC-74A / SOT23-5	-	320	°C/W
		SC-88A	-	377	
		MicroPak-6	-	154	

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

<sup>1.</sup> Unused inputs must be held HIGH or LOW. They may not float.

#### **NC7S04**

#### DC ELECTICAL CHARACTERISTICS

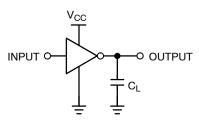
				-	Γ <sub>A</sub> = +25°C	;	T <sub>A</sub> = -40	to +85°C	
Symbol	Parameter	V <sub>CC</sub> (V)	Conditions	Min	Тур	Max	Min	Max	Unit
V <sub>IH</sub>	HIGH Level Input Voltage	2.0 3.0 - 6.0		1.50 0.7 V <sub>CC</sub>	- -	- -	1.50 0.7 V <sub>CC</sub>	- -	٧
V <sub>IL</sub>	LOW Level Input Voltage	2.0 3.0 - 6.0		- -	- -	0.50 0.3 V <sub>CC</sub>	- -	0.50 0.3 V <sub>CC</sub>	V
V <sub>OH</sub>	HIGH Level Output Voltage	2.0 3.0 4.5 6.0	$I_{OH} = -20 \mu A$ $V_{IN} = V_{IH} \text{ or } V_{IL}$	1.90 2.90 4.40 5.90	2.0 3.0 4.5 6.0	- - - -	1.90 2.90 4.40 5.90	- - - -	V
		3.0 4.5 6.0	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OH} = -1.3 \text{ mA}$ $I_{OH} = -2.0 \text{ mA}$ $I_{OH} = -2.6 \text{ mA}$	2.68 4.18 5.68	2.85 4.35 5.85	- - -	2.63 4.13 5.63	- - -	V
V <sub>OL</sub>	LOW Level Output Voltage	2.0 3.0 4.5 6.0	$I_{OL}$ = 20 $\mu A$ $V_{IN}$ = $V_{IH}$ or $V_{IL}$	- - - -	0.0 0.0 0.0 0.0	0.10 0.10 0.10 0.10	- - - -	0.10 0.10 0.10 0.10	V
		3.0 4.5 6.0	$\begin{aligned} &V_{IN} = V_{IH} \text{ or } V_{IL} \\ &I_{OL} = 1.3 \text{ mA} \\ &I_{OL} = 2.0 \text{ mA} \\ &I_{OL} = 2.6 \text{ mA} \end{aligned}$	- - -	0.1 0.1 0.1	0.26 0.26 0.26	- - -	0.33 0.33 0.33	V
I <sub>IN</sub>	Input Leakage Current	6.0	V <sub>IN</sub> = V <sub>CC</sub> , GND	-	-	±0.1	-	±1.0	μΑ
I <sub>CC</sub>	Quiescent Supply Current	6.0	V <sub>IN</sub> = V <sub>CC</sub> , GND	-	-	1.0	-	10.0	μΑ

## **AC ELECTRICAL CHARACTERISTICS**

				-	Γ <sub>A</sub> = +25°0	•	T <sub>A</sub> = -40	to +85°C	
Symbol	Parameter	V <sub>CC</sub> (V)	Conditions	Min	Тур	Max	Min	Max	Unit
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay (Figure 4, 6)	5.0	C <sub>L</sub> = 15 pF	=	3.0	15.0	-	-	ns
		2.0 3.0 4.5 6.0	C <sub>L</sub> = 50 pF	- - -	18.0 10.0 7.0 6.0	100.0 27.0 20.0 17.0	- - -	125.0 35.0 25.0 21.0	ns
t <sub>TLH</sub> , t <sub>THL</sub>	Output Transition Time	5.0	C <sub>L</sub> = 15 pF	-	3.0	10.0	-	-	ns
	(Figure 4, 6)	2.0 3.0 4.5 6.0	C <sub>L</sub> = 50 pF	- - -	25.0 16.0 11.0 9.0	125.0 35.0 25.0 21.0	- - -	155.0 45.0 31.0 26.0	ns
C <sub>IN</sub>	Input Capacitance (Figure 4, 6)	Open		_	2.0	10.0	-	10.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Figure 5)	5.0	(Note 2)	-	6.0	-	-	-	pF

<sup>2.</sup> C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I<sub>CCD</sub>) at no output loading and operating at 50% duty cycle. C<sub>PD</sub> is related to I<sub>CCD</sub> dynamic operating current by the expression: I<sub>CCD</sub> = (C<sub>PD</sub>) (V<sub>CC</sub>) (f<sub>IN</sub>) + (I<sub>CC</sub>static).

#### **AC Loading and Waveforms**



 $C_L$  includes load and stray capacitance Input PRR = 1.0 MHz,  $t_{\rm W}$  = 500 ns

Figure 4. AC Test Circuit

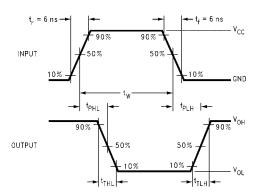
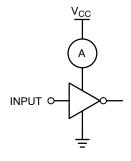


Figure 6. AC Waveforms



Input = AC Waveforms;

PRR = Variable; Duty Cycle = 50%.

Figure 5. I<sub>CCD</sub> Test Circuit

#### **DEVICE ORDERING INFORMATION**

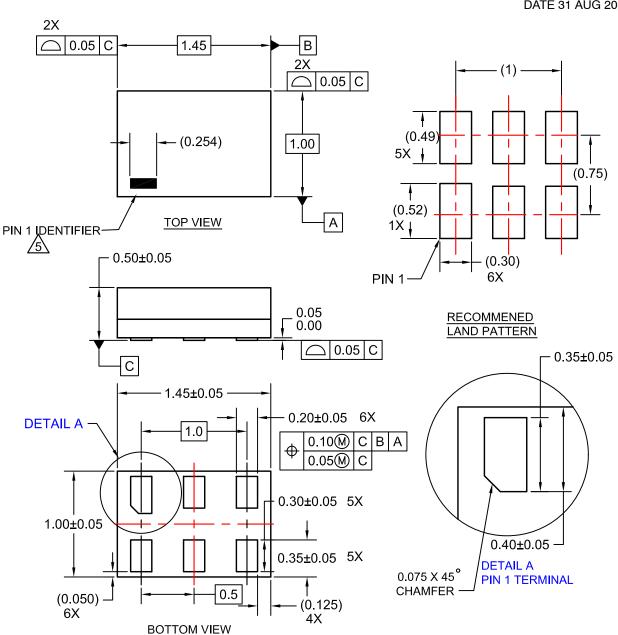
Device	Top Mark	Packages	Shipping <sup>†</sup>
NC7S04M5X	7S04	SC-74A	3000 / Tape & Reel
NC7S04M5X-L22090	7S04	SOT23-5	3000 / Tape & Reel
NC7S04P5X	S04	SC-88A	3000 / Tape & Reel
NC7S04P5X-L22057	S04	SC-88A	3000 / Tape & Reel
NC7S04L6X	AA	SIP6, MicroPak	5000 / Tape & Reel
NC7S04L6X-L22175	AA	SIP6, MicroPak	5000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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**DATE 31 AUG 2016** 



NOTES:

- 1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-2009
  4. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY

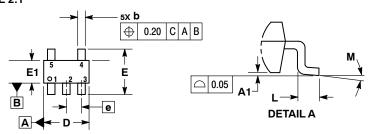
  - OTHER LINE IN THE MARK CODE LAYOUT.

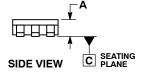
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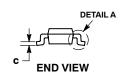
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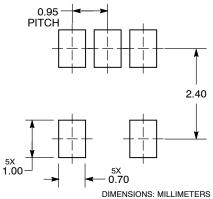
**DATE 18 JAN 2018** 







## **RECOMMENDED SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME
  Y14.5M, 1994.
  CONTROLLING DIMENSION: MILLIMETERS.
  MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH
  THICKNESS. MINIMUM LEAD THICKNESS IS THE
  MINIMUM THICKNESS OF BASE MATERIAL.
- DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE.

	MILLIMETERS			
DIM	MIN	MAX		
Α	0.90	1.10		
A1	0.01	0.10		
b	0.25	0.50		
С	0.10	0.26		
D	2.85	3.15		
E	2.50	3.00		
E1	1.35	1.65		
е	0.95 BSC			
L	0.20	0.60		
М	0 °	10°		

#### **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code

Μ = Date Code = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ", may or may not be present. Some products may not follow the Generic Marking.

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#### SC-88A (SC-70-5/SOT-353) CASE 419A-02 **ISSUE L**

**DATE 17 JAN 2013** 



- TES:
  DIMENSIONING AND TOLERANCING
  PER ANSI Y14.5M, 1982.
  CONTROLLING DIMENSION: INCH.
  419A-01 OBSOLETE. NEW STANDARD 3.
- 419A-02.
  DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.071	0.087	1.80	2.20
В	0.045	0.053	1.15	1.35
C	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026	BSC	0.65 BSC	
Н		0.004		0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20	REF
S	0.079	0.087	2.00	2.20

#### **GENERIC MARKING DIAGRAM\***



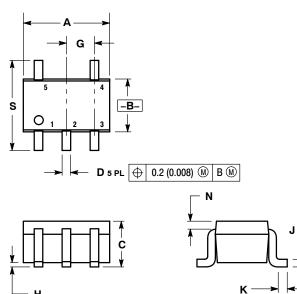
XXX = Specific Device Code

= Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



## 0.50 0.0197 0.65 0.025 0.65 0.025 0.40 0.0157 1.9 mm 0.0748 SCALE 20:1

**SOLDER FOOTPRINT** 

STYLE 1: PIN 1. BASE 2. EMITTER 3. BASE 4. COLLECTOR 5. COLLECTOR	STYLE 2: PIN 1. ANODE 2. EMITTER 3. BASE 4. COLLECTOR 5. CATHODE	STYLE 3: PIN 1. ANODE 1 2. N/C 3. ANODE 2 4. CATHODE 2 5. CATHODE 1	STYLE 4: PIN 1. SOURCE 1 2. DRAIN 1/2 3. SOURCE 1 4. GATE 1 5. GATE 2	STYLE 5: PIN 1. CATHODE 2. COMMON ANODE 3. CATHODE 2 4. CATHODE 3 5. CATHODE 4

5. COLLECTOR	5. CATHODE	5. CATHODE I	5. GATE 2	5. CATHODE 4
STYLE 6: PIN 1. EMITTER 2 2. BASE 2 3. EMITTER 1 4. COLLECTOR 5. COLLECTOR 2/BASE 1	STYLE 7: PIN 1. BASE 2. EMITTER 3. BASE 4. COLLECTOR 5. COLLECTOR	STYLE 8: PIN 1. CATHODE 2. COLLECTOR 3. N/C 4. BASE 5. EMITTER	STYLE 9: PIN 1. ANODE 2. CATHODE 3. ANODE 4. ANODE 5. ANODE	Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

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REFERENCE



A

F1 F

В

**DATE 09 JUN 2021** 

#### NUTES

DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 19894

DIM

- CONTROLLING DIMENSION: MILLIMETERS
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE
- DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS, MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.25 PER SIDE. D AND E1 DIMENSIONS ARE DETERMINED AT DATUM D.

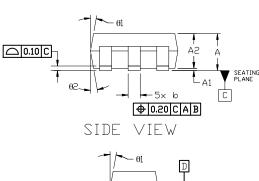
MIN.

DIMENSION 'b' DOES NOT INCLUDE DAMBAR PROTRUSION. DAMBAR PROTRUSION SHALL BE O. 08mm TOTAL IN EXCESS OF THE 'b' DIMENSION AT MAXIMUM MATERIAL CONDITION. MINIMUM SPACE BETWEEN PROTRUSION AND AN ADJACENT LEAD SHALL NOT BE LESS THAN 0.07mm.

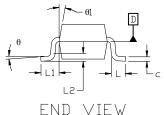
MILLIMETERS

ИПМ.

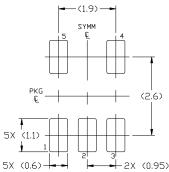
MAX.



TOP VIEW



#### Α 0.90 1.45 A1 0.00 0.15 Α2 0.90 1.15 1.30 b 0.30 0.50 0.08 0.22 n 2.90 BSC 2.80 BSC E1 1.60 BSC 0.95 BSC е 0.45 0.30 0.60 L1 0.60 REF 0.25 REF L2 4° θ 0° 10° 15° θ1 0° 10° θ2 15°



## **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

#### RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the  $\square N$  Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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