

# Silicon Carbide (SiC) MOSFET - 20 mohm, 900 V, M1, TO-247-3L

# NTHL020N090SC1

#### Features

- Typ.  $R_{DS(on)} = 20 \text{ m}\Omega$  @  $V_{GS} = 15 \text{ V}$
- Typ.  $R_{DS(on)} = 16 \text{ m}\Omega @ V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge (Q<sub>G(tot)</sub> = 196 nC)
- Low Effective Output Capacitance (Coss = 296 pF)
- 100% UIL Tested
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb–Free 2LI (on second level interconnection)

#### **Typical Applications**

- UPS
- DC-DC Converter
- Boost Inverter

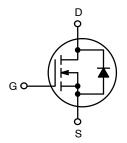
#### **MAXIMUM RATINGS** (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			$V_{DSS}$	900	V
Gate-to-Source Voltage	ge		$V_{GS}$	+22/-8	V
Recommended Operation Values of Gate – Source Voltage			$V_{GSop}$	+15/-5	V
Continuous Drain Current R <sub>0</sub> JC	Steady State	T <sub>C</sub> = 25°C	I <sub>DC</sub>	118	Α
Power Dissipation $R_{\theta JC}$			P <sub>DC</sub>	503	W
Continuous Drain Current R <sub>0</sub> JC	Steady State	T <sub>C</sub> = 100°C	I <sub>DC</sub>	83	Α
Power Dissipation $R_{\theta JC}$			P <sub>DC</sub>	251	W
Pulsed Drain Current (Note 2) T <sub>A</sub> = 25°C			I <sub>DM</sub>	472	Α
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C
Source Current (Body Diode)			Is	153	Α
Single Pulse Drain-to-Source Avalanche Energy ( $I_L = 23  A_{pk},  L = 1  mH$ ) (Note 3)			E <sub>AS</sub>	264	mJ

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature.
- 3. E<sub>AS</sub> of 162 mJ is based on starting  $T_J = 25^{\circ}C$ ; L = 1 mH, I<sub>AS</sub> = 23 A,  $V_{DD} = 100 \text{ V}$ ,  $V_{GS} = 15 \text{ V}$ .

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
900 V	28 mΩ @ 15 V	118 A

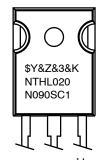


**N-CHANNEL MOSFET** 



TO-247 LONG LEADS CASE 340CX

#### MARKING DIAGRAM



\$Y = **onsemi** Logo &Z = Assembly Plant Code &3 = Date Code (Year & Week) &K = Lot

NTHL020N090SC1 = Specific Device Code

#### **ORDERING INFORMATION**

Device	Package	Shipping
NTHL020N090SC1	TO-247 Long Lead	30 Units / Tube

**Table 1. THERMAL CHARACTERISTICS** 

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-to-Case (Note 1)	$R_{ heta JC}$	0.30	°C/W
Thermal Resistance Junction-to-Ambient (Note 1)	$R_{ hetaJA}$	40	°C/W

Table 2. ELECTRICAL CHARACTERISTICS (T<sub>.I</sub> = 25°C unless otherwise stated)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS		•				
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA	900			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /	I <sub>D</sub> = 1 mA, refer to 25°C		500		mV/
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, T <sub>J</sub> = 25°C			100	μΑ
		V <sub>DS</sub> = 900 V T <sub>J</sub> = 175°C			250	μΑ
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = +22/-8 V, V <sub>DS</sub> = 0 V			±1	μΑ
ON CHARACTERISTICS	•		•	•		•
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}$ , $I_D = 20 \text{ mA}$	1.8	2.7	4.3	V
Recommended Gate Voltage	$V_{GOP}$		-5		+15	V
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 15 V, I <sub>D</sub> = 60 A, T <sub>J</sub> = 25°C		20	28	mΩ
		V <sub>GS</sub> = 18 V, I <sub>D</sub> = 60 A, T <sub>J</sub> = 25°C		16		1
		V <sub>GS</sub> = 15 V, I <sub>D</sub> = 60 A, T <sub>J</sub> = 175°C		27		
Forward Transconductance	9FS	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 60 A		49		S
CHARGES, CAPACITANCES & GATE RES	ISTANCE	•		•		
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> = 0 V, f = 1 MHz,		4415		pF
Output Capacitance	C <sub>OSS</sub>	V <sub>DS</sub> = 450 V		296		
Reverse Transfer Capacitance	C <sub>RSS</sub>	1		24		
Total Gate Charge	Q <sub>G(TOT)</sub>	$V_{GS} = -5/15 \text{ V}, V_{DS} = 720 \text{ V},$		196		nC
Threshold Gate Charge	Q <sub>G(TH)</sub>	I <sub>D</sub> = 60 A		42		
Gate-to-Source Charge	Q <sub>GS</sub>	1		78		
Gate-to-Drain Charge	$Q_{GD}$	1		55		
Gate-Resistance	$R_{G}$	f = 1 MHz		1.6		Ω
SWITCHING CHARACTERISTICS		•				
Turn-On Delay Time	t <sub>d(ON)</sub>	$V_{GS} = -5/15 \text{ V}, V_{DS} = 720 \text{ V},$		40		ns
Rise Time	t <sub>r</sub>	$I_D$ = 60 A, $R_G$ = 2.5 $\Omega$ , Inductive Load		63		1
Turn-Off Delay Time	t <sub>d(OFF)</sub>	1		55		1
Fall Time	t <sub>f</sub>	1		13		1
Turn-On Switching Loss	E <sub>ON</sub>	1		2025		μJ
Turn-Off Switching Loss	E <sub>OFF</sub>	1		201		1
Total Switching Loss	E <sub>TOT</sub>	1		2226		1
DRAIN-SOURCE DIODE CHARACTERIST			•			
Continuous Drain-Source Diode Forward Current	I <sub>SD</sub>	V <sub>GS</sub> = -5 V, T <sub>J</sub> = 25°C			153	Α
Pulsed Drain–Source Diode Forward Current (Note 2)	I <sub>SDM</sub>	V <sub>GS</sub> = -5 V, T <sub>J</sub> = 25°C			472	Α
,						

Table 2. ELECTRICAL CHARACTERISTICS (T<sub>.I</sub> = 25°C unless otherwise stated)

Table 2. ELECTRICAL CHARACTERIO 100 (1) = 20 0 unicos cureivise statedy							
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
DRAIN-SOURCE DIODE CHARACTERISTICS							
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = -5/15 \text{ V}, I_{SD} = 60 \text{ A},$ $dI_S/dt = 1000 \text{ A}/\mu\text{s}, V_{DS} = 720 \text{ V}$		28		ns	
Reverse Recovery Charge	Q <sub>RR</sub>			199		nC	
Reverse Recovery Energy	E <sub>REC</sub>			4		μJ	
Peak Reverse Recovery Current	I <sub>RRM</sub>			14		Α	
Charge Time	Ta			16		ns	
Discharge Time	Tb			12		ns	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### **TYPICAL CHARACTERISTICS**

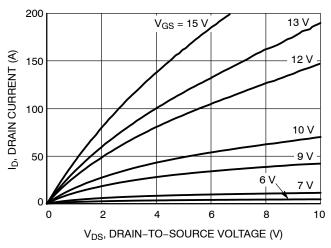


Figure 1. On-Region Characteristics

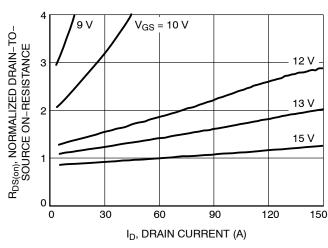


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

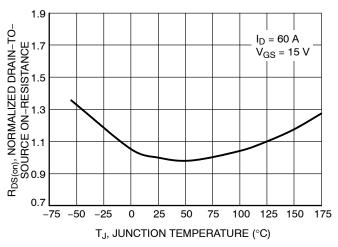


Figure 3. On–Resistance Variation with Temperature

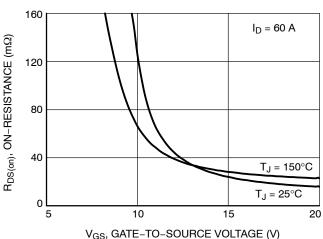


Figure 4. On-Resistance vs. Gate-to-Source Voltage

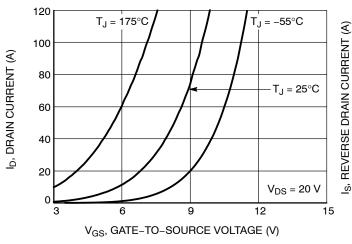


Figure 5. Transfer Characteristics

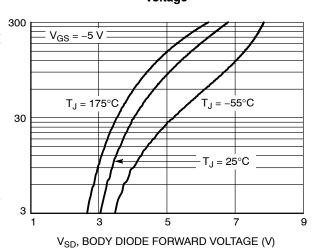


Figure 6. Diode Forward Voltage vs. Current

#### TYPICAL CHARACTERISTICS (continued)

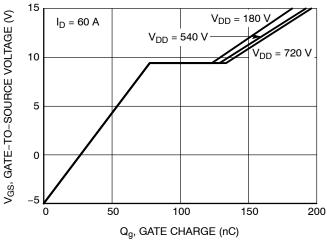


Figure 7. Gate-to-Source Voltage vs. Total Charge

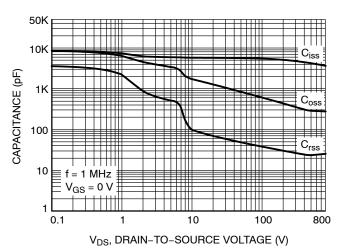


Figure 8. Capacitance vs. Drain-to-Source Voltage

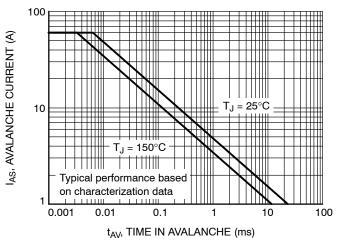


Figure 9. Unclamped Inductive Switching Capability

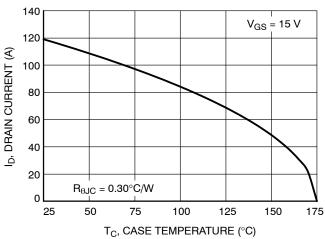


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

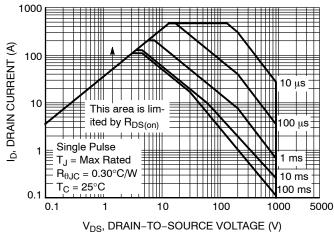


Figure 11. Safe Operating Area

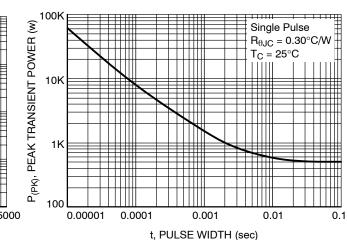


Figure 12. Single Pulse Maximum Power Dissipation

# TYPICAL CHARACTERISTICS (continued)

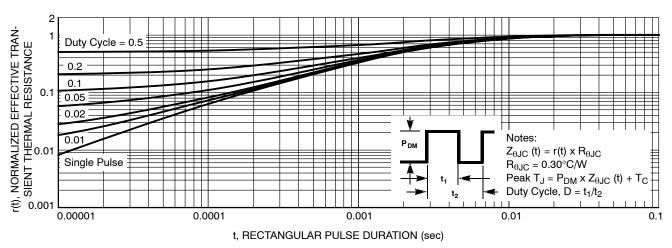
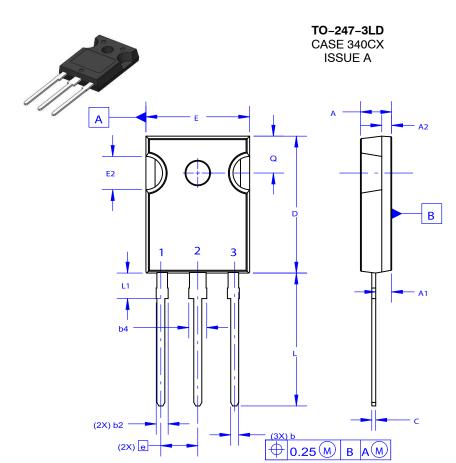
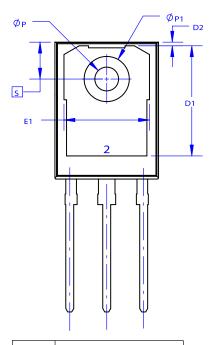


Figure 13. Junction-to-Ambient Transient Thermal Response Curve



**DATE 06 JUL 2020** 

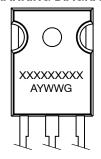


#### NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

  B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

# **GENERIC MARKING DIAGRAM\***



XXXXX = Specific Device Code = Assembly Location

= Year WW = Work Week G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " =", may or may not be present. Some products may not follow the Generic Marking.

DIM	MILLIMETERS					
DIM	MIN	NOM	MAX			
Α	4.58	4.70	4.82			
<b>A</b> 1	2.20	2.40	2.60			
A2	1.40	1.50	1.60			
D	20.32	20.57	20.82			
Е	15.37	15.62	15.87			
E2	4.96	5.08	5.20			
е	~	5.56	~			
L	19.75	20.00	20.25			
L1	3.69	3.81	3.93			
ØΡ	3.51	3.58	3.65			
Q	5.34	5.46	5.58			
S	5.34	5.46	5.58			
b	1.17	1.26	1.35			
b2	1.53	1.65	1.77			
b4	2.42	2.54	2.66			
С	0.51	0.61	0.71			
D1	13.08	~	~			
D2	0.51	0.93	1.35			
E1	12.81	~	~			
ØP1	6.60	6.80	7.00			

DOCUMENT NUMBER:	98AON93302G	Electronic versions are uncontrolled except when accessed directly from the Document Reposi Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	TO-247-3LD		PAGE 1 OF 1	

ON Semiconductor and un are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer pu

#### **PUBLICATION ORDERING INFORMATION**

LITERATURE FULFILLMENT: Email Requests to: orderlit@onsemi.com

onsemi Website: www.onsemi.com

TECHNICAL SUPPORT North American Technical Support: Voice Mail: 1 800-282-9855 Toll Free USA/Canada Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support:

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative