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2014 年 1 月

FDI150N10

N 沟道 PowerTrench® MOSFET

100 V， 57 A， 16 mΩ

特性

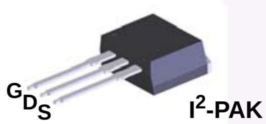
- $R_{DS(on)}$ = 12 mΩ (Typ.)@V_{GS} = 10 V, I_D = 49 A
- 快速开关速度
- 低栅极电荷
- 高性能沟道技术可实现极低的 $R_{DS(on)}$
- 高功率和高电流处理能力
- 符合 RoHS 标准

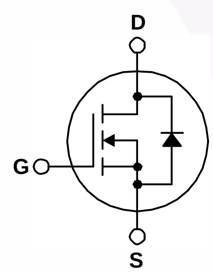
说明

此 N 沟道 MOSFET 采用飞兆半导体先进的 PowerTrench® 工艺生产，这一先进工艺是专为最大限度地降低导通电阻并保持卓越开关性能而定制的。

应用

- 用于 ATX/ 服务器 / 电信 PSU 的同步整流
- 电池保护电路
- 电机驱动和不间断电源
- 微型光伏逆变器





MOSFET 最大额定值 T_C = 25°C 除非另有说明。

符号	参数		FDI150N10	单位
V_{DSS}	漏极－源极电压		100	V
V_{GSS}	栅极－源极电压		±20	V
I_D	漏极电流	- 连续 (T_C = 25°C)	57	A
		- 连续 (T_C = 100°C)	40	A
I_{DM}	漏极电流	- 脉冲 （说明 1）	228	A
E_{AS}	单脉冲雪崩能量 （说明 2）		132	mJ
dv/dt	二极管恢复 dv/dt 峰值 （说明 3）		7.5	V/ns
P_D	功耗	(T_C = 25°C)	110	W
		- 降低至 25°C 以上	0.88	W/°C
T_J, T_{STG}	工作和存储温度范围		-55 至 +150	°C
T_L	用于焊接的最大引线温度，距离外壳 1/8"，持续 5 秒		300	°C

热性能

符号	参数	FDI150N10	单位
$R_{\theta JC}$	结至外壳热阻最大值	1.13	°C/W
$R_{\theta JA}$	结至环境热阻最大值	62.5	

封装标识与定购信息

器件编号	顶标	封装	包装方法	卷尺寸	带宽	数量
FDI150N10	FDI150N10	I^2-PAK	塑料管	不适用	不适用	50 个

电气特性 T_C = 25°C 除非另有说明。

符号	参数	测试条件	最小值	典型值	最大值	单位
关断特性						
BV_{DSS}	漏极－源极击穿电压	I_D = 250 μA, V_{GS} = 0 V, T_C= 25°C	100	-	-	V
ΔBV_{DSS} / ΔT_J	击穿电压温度系数	I_D = 250 μA，参考温度为 25°C	-	0.1	-	V/°C
I_{DSS}	零栅极电压漏极电流	V_{DS} = 100 V, V_{GS} = 0 V	-	-	1	μA
		V_{DS} = 100 V, V_{GS} = 0 V, T_C = 150°C	-	-	500	μA
I_{GSS}	栅极 - 体漏电流	V_{GS} = ±20 V, V_{DS} = 0 V	-	-	±100	nA
导通特性						
$V_{GS(th)}$	栅极阈值电压	V_{GS} = V_{DS}, I_D = 250 μA	2.5	-	4.5	V
$R_{DS(on)}$	漏极至源极静态导通电阻	V_{GS} = 10 V, I_D = 49 A	-	12	16	mΩ
g_{FS}	正向跨导	V_{DS} = 20 V, I_D = 49 A	-	156	-	S
动态特性						
C_{iss}	输入电容	V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz	-	3580	4760	pF
C_{oss}	输出电容		-	340	450	pF
C_{rss}	反向传输电容		-	140	210	pF
开关特性						
$t_{d(on)}$	导通延迟时间	V_{DD} = 50 V, I_D = 49 A, V_{GS} = 10 V, R_G = 25 Ω （说明 4）	-	47	104	ns
t_r	开通上升时间		-	164	338	ns
$t_{d(off)}$	关断延迟时间		-	86	182	ns
t_f	关断下降时间		-	83	176	ns
$Q_{g(tot)}$	10 V 的栅极电荷总量	V_{DS} = 80 V, I_D = 49 A, V_{GS} = 10 V （说明 4）	-	53	69	nC
Q_{gs}	栅极 - 源极栅极电荷		-	19	-	nC
Q_{gd}	栅极 - 漏极 " 米勒 " 电荷		-	15	-	nC
漏极 - 源极二极管特性						
I_S	漏极 - 源极二极管最大正向连续电流		-	-	57	A
I_{SM}	漏极 - 源极二极管最大正向脉冲电流		-	-	228	A
V_{SD}	漏极 - 源极二极管正向电压	V_{GS} = 0 V, I_{SD} = 49 A	-	-	1.3	V
t_{rr}	反向恢复时间	V_{GS} = 0 V, I_{SD} = 49 A, dI_F/dt = 100 A/μs	-	41	-	ns
Q_{rr}	反向恢复电荷		-	70	-	nC

注意：

1: 重复额定值：脉冲宽度受限于最大结温。
2: L=0.11 mH， I_{AS}=49 A， V_{DD}=50 V， R_G=25 Ω，开始 T_J=25°C。
3: I_{SD} ≤ 49 A， di/dt ≤ 200 A/μs， V_{DD} ≤ BV_{DSS}，开始 T_J=25°C。
4: 本质上独立于工作温度的典型特性。

典型性能特征

图 1. 导通区域特性

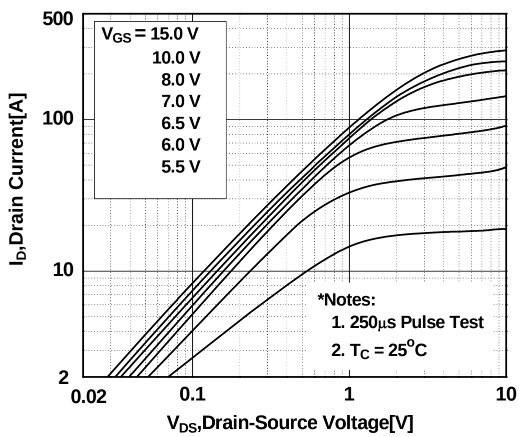


图 2. 传输特性

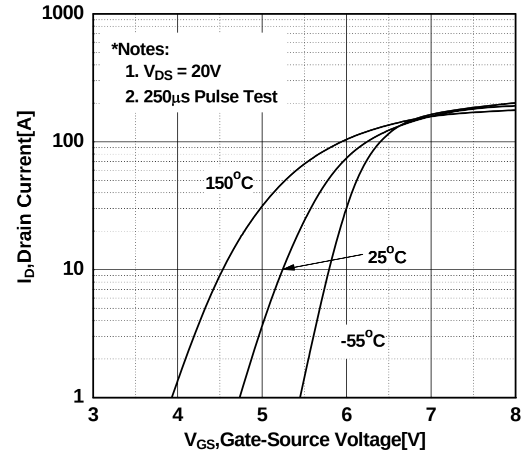


图 3. 导通电阻变化与漏极电流和栅极电压

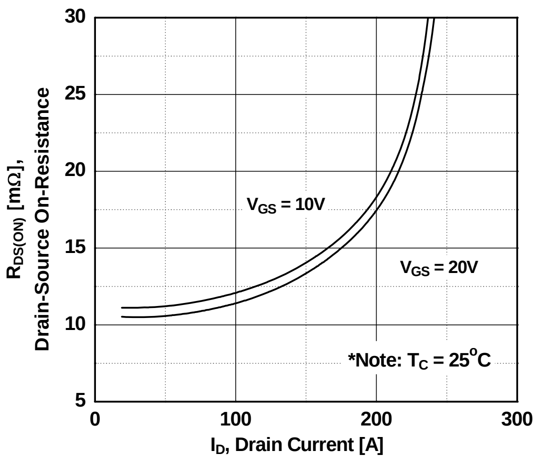


图 4. 体二极管正向电压变化与源极电流和温度

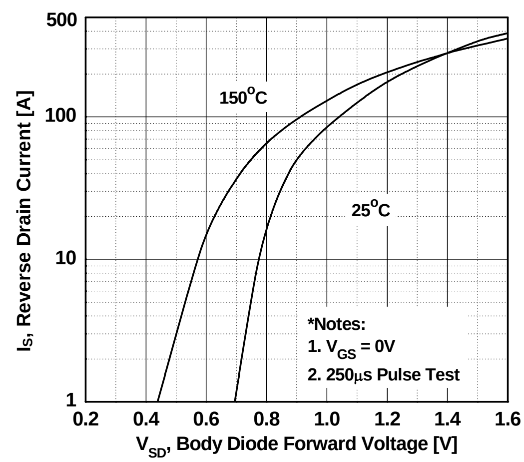


图 5. 电容特性

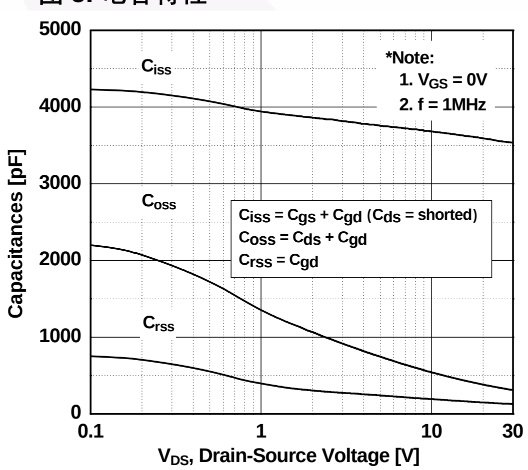
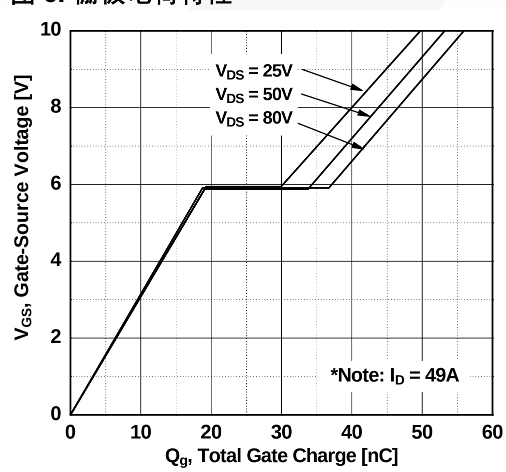


图 6. 栅极电荷特性



典型性能特征（接上页）

图 7. 击穿电压变化与温度

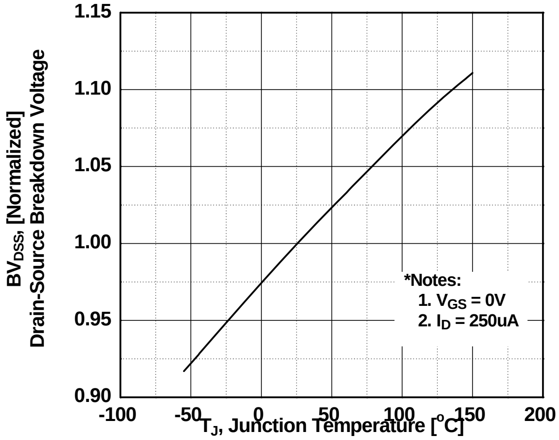


图 8. 导通电阻变化与温度

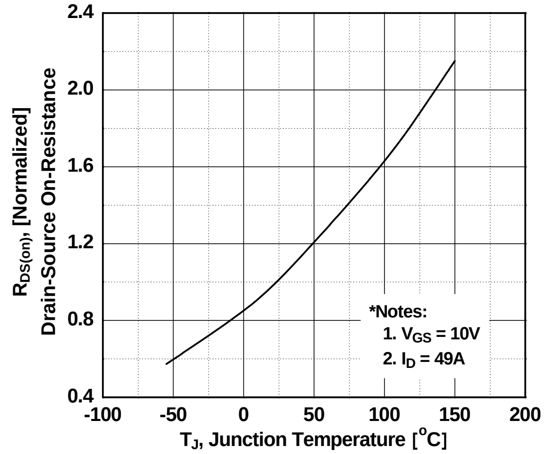


图 9. 最大安全工作区

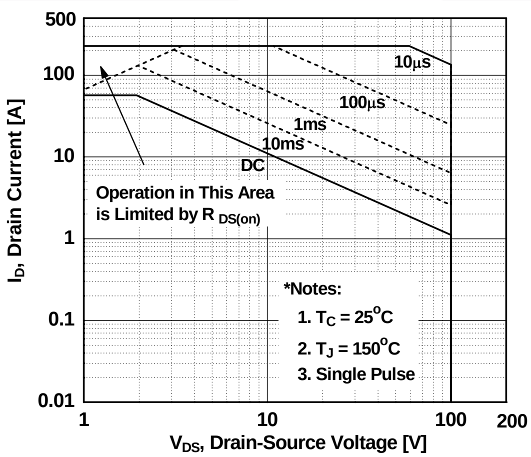


图 10. 最大漏极电流与外壳温度

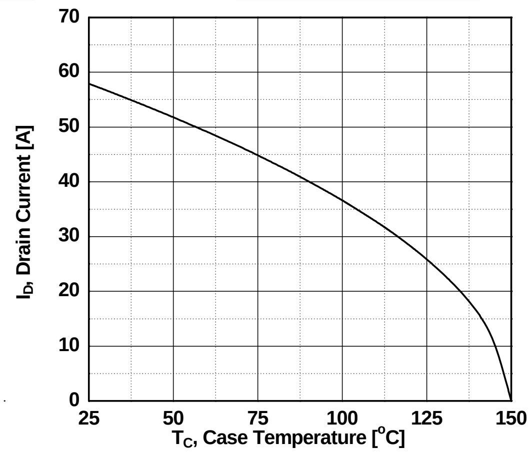
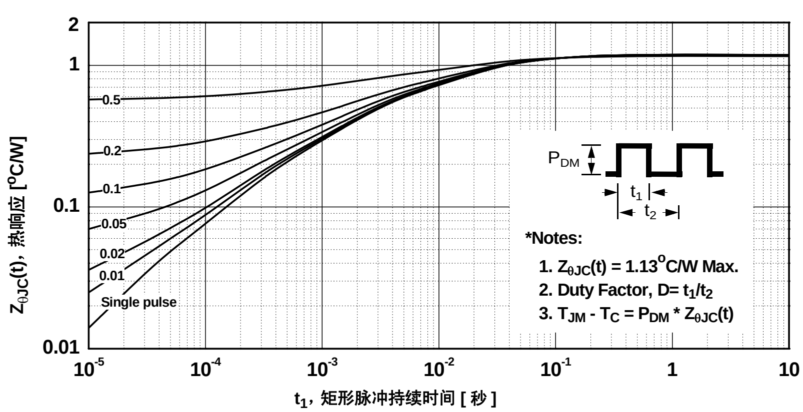


图 11. 瞬态热响应曲线



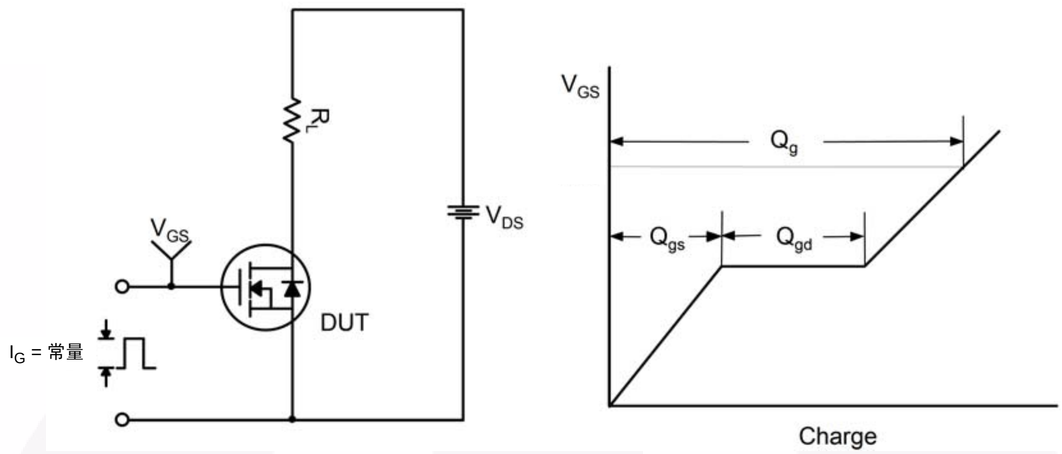


图 12. 栅极电荷测试电路与波形

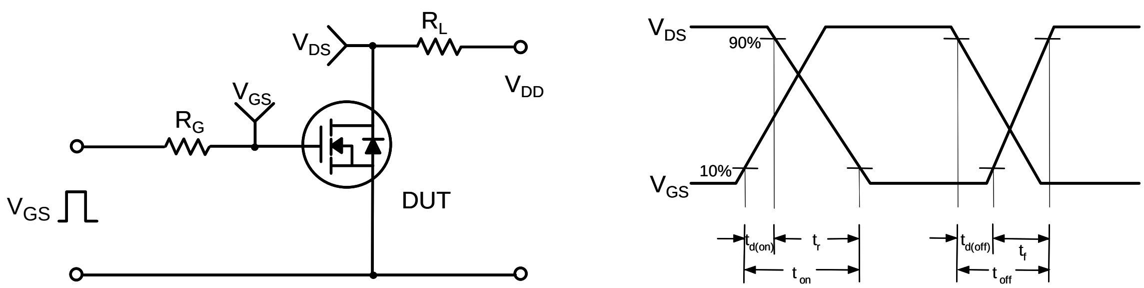


图 13. 阻性开关测试电路与波形

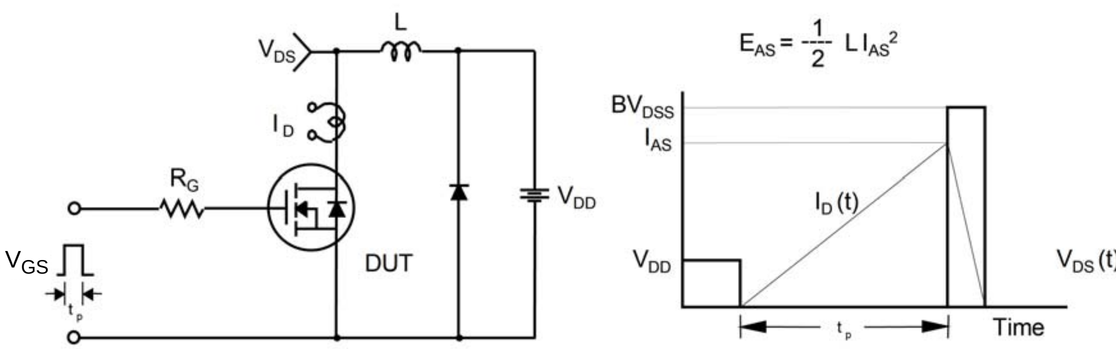


图 14. 非箝位感性开关测试电路与波形

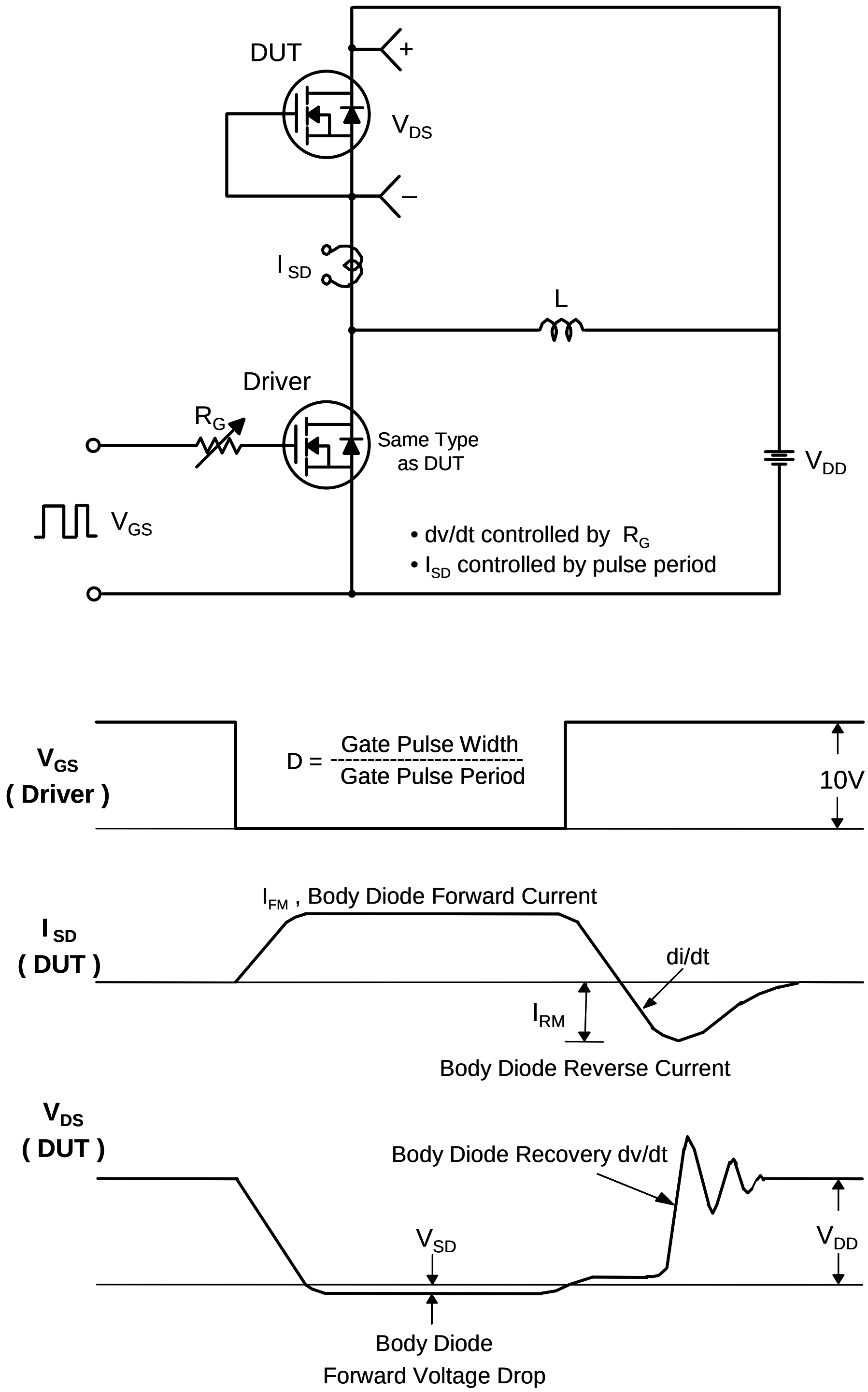


图 15. 峰值二极管恢复 dv/dt 测试电路与波形

机械尺寸

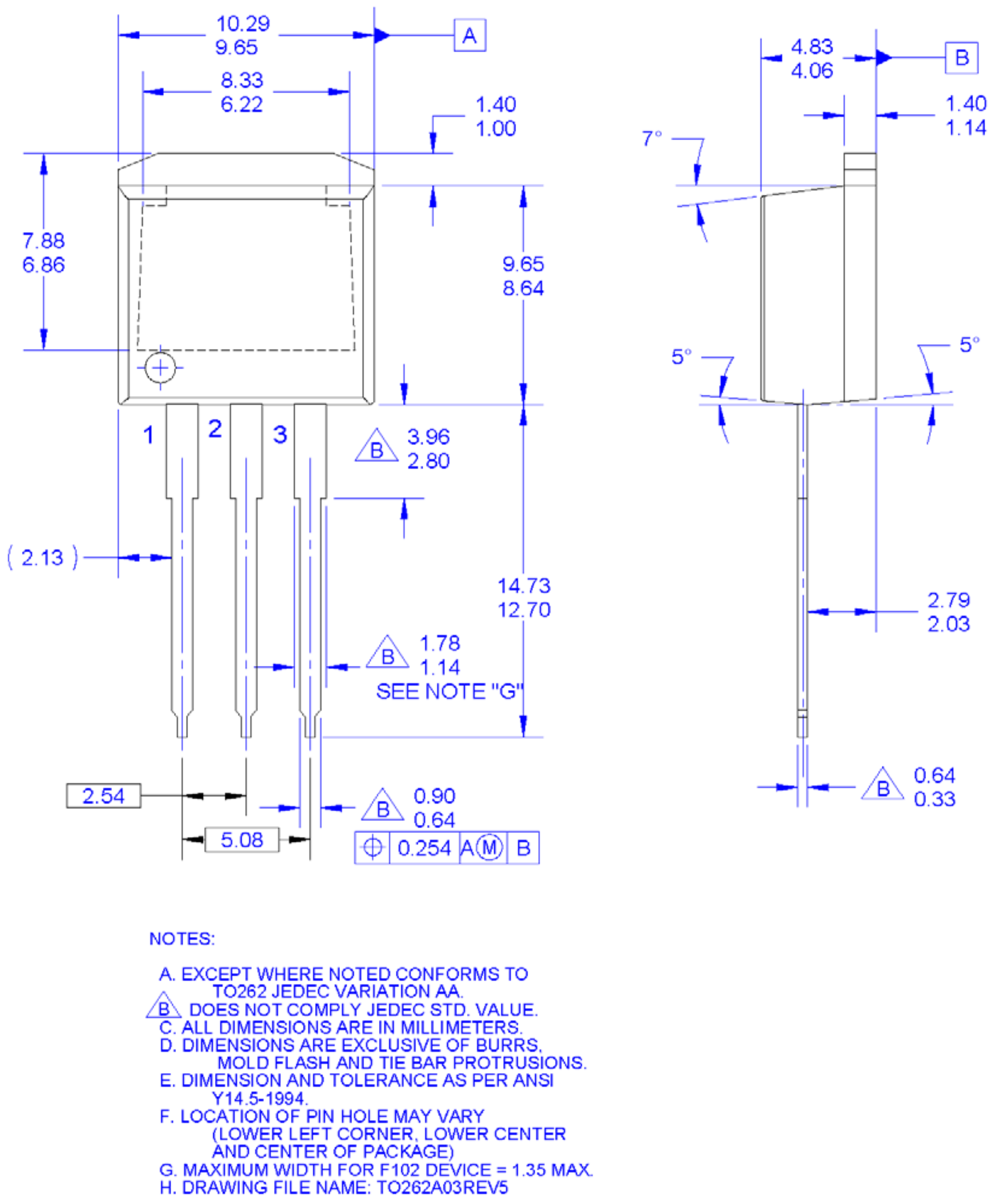


图 16. TO262 (I^2PAK)，模塑， 3 引脚， Jedec 变体 AA

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. I66

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