

650 V Power SiC Merged PIN Schottky Diode, 4 A



LINKS TO ADDITIONAL RESOURCES







PRIMARY CHARACTERISTICS				
I _{F(AV)}	4 A			
V _R	650 V			
V _F at I _F at 150 °C	1.75 V			
T _J max.	175 °C			
I _R at V _R at 175 °C	2.5 μΑ			
$Q_{C} (V_{R} = 400 V)$	11 nC			
Package	2L TO-220AC			
Circuit configuration	Single			

FEATURES

 Majority carrier diode using Schottky technology on SiC wide band gap material



Positive V_F temperature coefficient for easy paralleling

COMPLIANT HALOGEN FREE

- Virtually no recovery tail and no switching losses
- Temperature invariant switching behavior
- 175 °C maximum operating junction temperature
- MPS structure for high ruggedness to forward current surge events
- Meets JESD 201 class 1A whisker test
- Solder bath temperature 275 °C maximum, 10 s per JESD 22-B106
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Wide band gap SiC based 650 V Schottky diode, designed for high performance and ruggedness.

Optimum choice for high speed hard switching and efficient operation over a wide temperature range, it is also recommended for all applications suffering from Silicon ultrafast recovery behavior.

Typical applications include AC/DC PFC and DC/DC ultra high frequency output rectification in FBPS and LLC converters.

MECHANICAL DATA

Case: 2L TO-220AC

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

Mounting torque: 10 in-lbs maximum

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V_{RRM}		650	V	
Average rectified forward current	I _{F(AV)}	T _C = 139 °C (DC)	4	Α	
DC blocking voltage	V_{DC}		650	V	
Repetitive peak surge current	I _{FRM}	$T_C = 25$ °C, f = 50 Hz, square wave, DC = 25 %	18		
Non-repetitive peak forward surge current	I _{FSM}	$T_C = 25$ °C, $t_p = 10$ ms, half sine wave	26	А	
		T_C = 110 °C, t_p = 10 ms, half sine wave	23		
Power dissipation	P _{tot} (1)	T _C = 25 °C	40	147	
		T _C = 110 °C	17	W	
l ² t value	∫i ² dt	T _C = 25 °C	3.4	A ² s	
		T _C = 110 °C	2.6		
Operating junction and storage temperatures	T _J ⁽²⁾ , T _{Stg}		-55 to +175	°C	

Notes

⁽¹⁾ Based on maximum R_{th}

 $^{^{(2)}}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
		I _F = 4 A	-	1.45	1.70		
Forward voltage	V_{F}	I _F = 4 A, T _J = 150 °C	-	1.75	2.20	V	
		I _F = 4 A, T _J = 175 °C	-	1.85	-		
Reverse leakage current I _R		$V_R = V_R$ rated	-	-	25		
	I_R	V _R = V _R rated, T _J = 150 °C	-	-	50	μΑ	
		V _R = V _R rated, T _J = 175 °C	-	2.5	-		
Total capacitance C	0	V _R = 1 V, f = 1 MHz	-	170	-	pF	
		V _R = 400 V, f = 1 MHz	-	19	-	pΓ	
Total capacitive charge	Q _C	V _R = 400 V, f = 1 MHz	-	11	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS (T _A = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction-to-case	R _{thJC}		-	2.7	3.8	°C/W
Marking device				C04E	T07T	

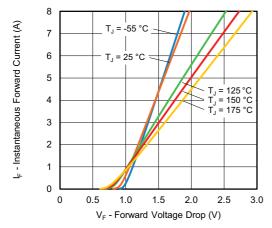


Fig. 1 - Typical Forward Voltage Drop Characteristics

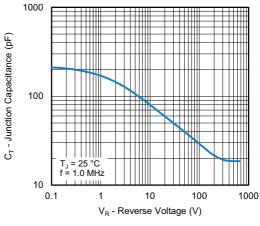


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

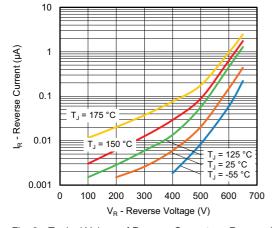


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

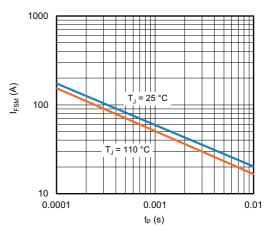


Fig. 4 - Non-Repetitive Peak Forward Surge Current vs. Pulse Duration (Square Wave)



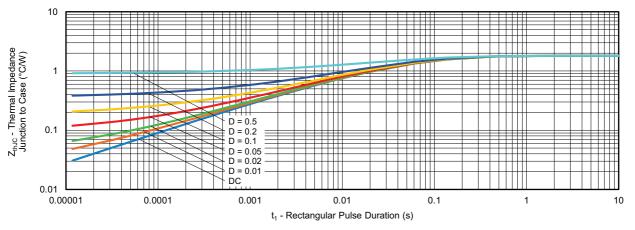


Fig. 5 - Typical Thermal Impedance Z_{thJC} Characteristics

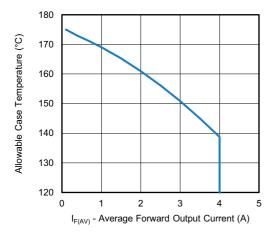


Fig. 6 - Maximum Allowable Case Temperature vs.
Average Forward Current

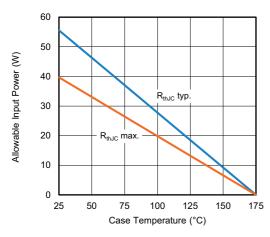


Fig. 7 - Forward Power Loss Characteristics

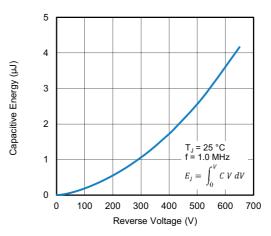


Fig. 8 - Typical Capacitive Energy vs. Reverse Voltage

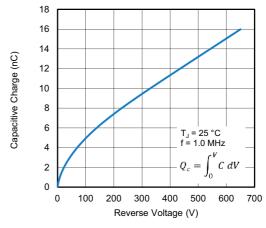
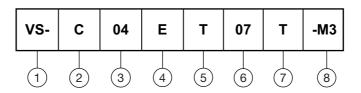


Fig. 9 - Typical Capacitive Charge vs. Reverse Voltage



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

C = SiC diode

Current rating (04 = 4 A)

E = single diode

1 2 3 4 5 6 Package TO-220

Voltage rating: (07 = 650 V)

T = true 2 pin

Environmental digit:

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION					
PREFERRED P/N	BASE QUANTITY	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-C04ET07T-M3	50/tube	1000	Antistatic plastic tubes		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?96069</u>				
Part marking information	www.vishay.com/doc?95391			
SPICE model	www.vishay.com/doc?96822			



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