

## P-Channel Enhancement Mode Field Effect Transistor

### General Description

The P1403EK uses advanced trench technology to provide excellent RDS(ON).

This device is ideal for load switch and battery protection applications.

### Product Summary

BVDSS	RDSON	ID
-30V	10mΩ	-52A

### Applications

- Load Switch
- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

### Features

- Fast switching speed
- Lower On-resistance
- 100% EAS Guaranteed
- Simple Drive Requirement

### DFN-8 5x6 Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current	-52	A
$I_{DM}$	Pulsed Drain Current	-150	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	88	mJ
$P_D @ T_c = 25^\circ C$	Total Power Dissipation	62.5	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Junction-to-Ambient	---	50	°C/W
$R_{\theta JC}$	Junction-to-Case	---	2	°C/W

Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$	-30	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$ , $I_D=-25\text{A}$	---	---	10	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$ , $I_D=-20\text{A}$	---	---	18	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=-250\mu\text{A}$	-1	---	-2.5	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-24\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	-1	$\text{uA}$
		$V_{\text{DS}}=-24\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	-100	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$ , $I_D=-15\text{A}$	---	32	---	S
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	2	---	$\Omega$
$Q_g$	Total Gate Charge	$V_{\text{DS}}=-15\text{V}$ , $I_D=-12\text{A}$	---	40	---	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{GS}}=-10\text{V}$	---	4	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	8	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=-15\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $R_G=6\Omega$	---	18	---	$\text{ns}$
$T_r$	Rise Time		---	30	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	50	---	
$T_f$	Fall Time		---	95	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	2000	---	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		---	400	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	270	---	

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-52	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	-150	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_F=-30\text{A}$	---	---	-1.2	V

Note :

1.The test condition is  $V_{\text{DD}}=50\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{\text{AS}}=42\text{A}$ 

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