

N-Channel Enhancement Mode Field Effect Transistor

General Description

The 60N20 is a N-channel Power MOSFET. It has specifically been designed to minimize input capacitance and gate charge. The device is therefore suitable in advanced high-efficiency switching applications.

Features

- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Lead-Free

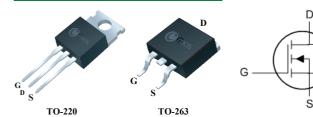
Product Summary

BVDSS	RDSON	ID
200V	28mΩ	60A

Applications

- LED power controller
- DC-DC & DC-AC converters
- High current, High speed switching
- Solenoid and relay drivers
- Motor control, Audio amplifiers

TO-220/263 Pin Configuration



Туре	Package	Marking
CMP60N20	TO-220	CMP60N20
CMB60N20	TO-263	CMB60N20

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
V_{DS}	Drain-Source Voltage	200	V	
V _{GS}	Gate-Source Voltage	±20	V	
I _D @T _C =25°C	Continuous Drain Current,VGS @ 10V	60	Α	
I _D @T _C =100°C	Continuous Drain Current,VGS @ 10V	48	А	
I _{DM}	Pulsed Drain Current	240	А	
EAS	Single Pulse Avalanche Energy	1400	mJ	
P _D @T _C =25°C	Power Dissipation	260	W	
T _{STG}	Storage Temperature Range	-55 to 175	°C	
T_J	Operating Junction Temperature Range	-55 to 175	°C	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit	
$R_{\theta JA}$	Junction-to-Ambient (PCB mount)		62	°C/W	
R _{eJC}	Junction-to-Case		0.57	°C/W	



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Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	200			V
R _{DS(ON)}	Static Drain-Source On-Resistance	V_{GS} =10V , I_D =30A			28	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250uA$	3		5	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =200V, V _{GS} =0V			1	uA
I _{GSS}	Gate-Source Leakage Current	$V_{GS} = \pm 20V$, $V_{DS} = 0V$			±100	nA
gfs	Forward Transconductance	V _{DS} =10V , I _D =25A		35		S
R_g	Gate Resistance	V_{DS} =0V , V_{GS} =0V , f=1MHz		3.2		Ω
Qg	Total Gate Charge	I _D =30A		100		
Q _{gs}	Gate-Source Charge	V _{DS} = 100V		30		nC
Q_{gd}	Gate-Drain Charge	V _{GS} =10 V		40		
$T_{d(on)}$	Turn-On Delay Time	V _{DD} =100V		31		
Tr	Rise Time	I _D =30A		21		ns
$T_{d(off)}$	Turn-Off Delay Time	$R_G=2.5\Omega$		22		115
T _f	Fall Time	V _{GS} =10V		32		
C _{iss}	Input Capacitance			5600		
C _{oss}	Output Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		338		pF
C _{rss}	Reverse Transfer Capacitance			136		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			60	Α
I _{SM}	Pulsed Source Current				240	Α
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =30A , T _J =25℃			1.2	V

Note:

This product has been designed and qualified for the counsumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserver the right to improve product design ,functions and reliability wihtout notice.