

General Description

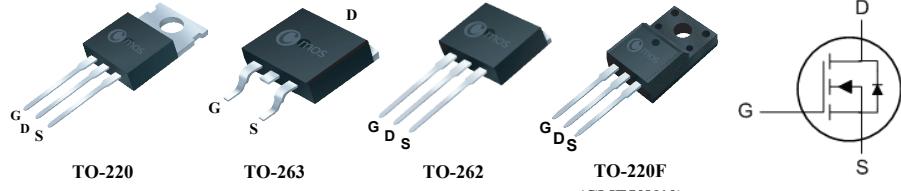
The 50N10 uses advanced process technology and design to provide excellent RDS(ON). This device is ideal for boost converters and synchronous rectifiers for consumer, telecom, industrial power supplies and LED backlighting.

Product Summary

BVDSS	RDSON	ID
100V	17mΩ	50A

Applications

- DC-DC converters
- UPS
- Power Supply
- PWM Motor Controls

TO-220/263/262/220F Pin Configuration**Features**

- Fast Switching
- 100% Avalanche tested
- RoHS compliant

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 25	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current	50	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current	37	A
I_{DM}	Pulsed Drain Current ¹	150	A
EAS	Single Pulse Avalanche Energy ²	200	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	100	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case	---	1.5	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	100	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=25\text{A}$	---	14	17	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=10\text{A}$	---	18	25	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 250\mu\text{A}$	1	---	3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=100\text{V}$, $V_{\text{GS}} = 0\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}} = \pm 25\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=15\text{V}$, $I_D=20\text{A}$	---	15	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2.3	---	Ω
Q_g	Total Gate Charge	$I_D=20\text{A}$	---	50	---	nC
Q_{gs}	Gate-Source Charge		---	10	---	
Q_{gd}	Gate-Drain Charge		---	8	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=20\text{V}$	---	10	---	ns
T_r	Rise Time		---	5	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	30	---	
T_f	Fall Time		---	5	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	3600	---	pF
C_{oss}	Output Capacitance		---	730	---	
C_{rss}	Reverse Transfer Capacitance		---	65	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	50	A
I_{SM}	Pulsed Source Current		---	---	150	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=25\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

1.Repetitive rating; pulse width limited by maximum junction temperature

2.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.5\text{mH}$, $I_{\text{AS}}=28\text{A}$

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