

N-Channel Enhancement Mode Field Effect Transistor

General Description

The 130N07 is N-Channel MOSFET, It has specifically been designed to minimize input capacitance and gate charge. The device is therefore suitable in advanced high-efficiency switching applications.

Features

- Minimize input capacitance and gate charge
- 100% avalanche tested
- Low On-Resistance

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	70	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current	130	A
$I_D@T_C=100^\circ C$	Continuous Drain Current	100	A
I_{DM}	Pulsed Drain Current	390	A
EAS	Single Pulse Avalanche Energy	600	mJ
$P_D@T_C=25^\circ C$	Total Power Dissipation	200	W
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-case	---	0.75	$^\circ C/W$

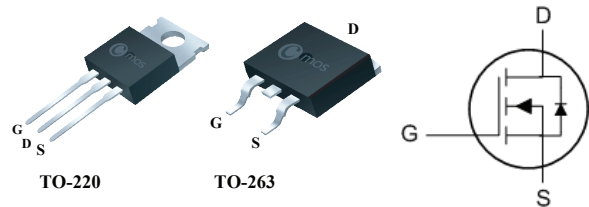
Product Summary

BVDSS	RDSON	ID
70V	6.0m Ω	130A

Applications

- Motor Control
- DC-DC converters
- Switching applications

TO-220/263 Pin Configuration



Type	Package	Marking
CMP130N07	TO-220	CMP130N07
CMB130N07	TO-263	CMB130N07

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Electrical Characteristics (T_J=25°C , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	70	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =30A	---	---	6	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	2	---	4	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =70V , V _{GS} =0V	---	---	1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V , V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =10V , I _D =32A	---	28	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	2.2	---	Ω
Q _g	Total Gate Charge	I _D =30A	---	126	---	nC
Q _{gs}	Gate-Source Charge	V _{DD} =35V	---	25	---	
Q _{gd}	Gate-Drain Charge	V _{GS} = 10V	---	50	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =35V	---	21	---	ns
T _r	Rise Time	I _D =2A	---	20	---	
T _{d(off)}	Turn-Off Delay Time	R _G =2.5Ω	---	71	---	
T _f	Fall Time	V _{GS} =10V	---	50	---	
C _{iss}	Input Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz	---	5900	---	pF
C _{oss}	Output Capacitance		---	411	---	
C _{rss}	Reverse Transfer Capacitance		---	316	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V , Force Current	---	---	130	A
I _{SM}	Pulsed Source Current		---	---	390	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =50A , T _J =25°C	---	---	1.2	V

Note :

This product has been designed and qualified for the consumer market.
 Cmos assumes no liability for customers' product design or applications.
 Cmos reserves the right to improve product design ,functions and reliability without notice.