

#### P-Channel Enhancement Mode Field Effect Transistor

## **General Description**

The CMN5P04M uses advanced trench technology to provide excellent RDS(ON) and low gate charge. This device is suitable for use as a load switch or in PWM applications.

### **Features**

- RDS(ON)<85mΩ @ VGS=-10V
- RDS(ON)<110mΩ @ VGS=-4.5V
- Simple drive requirement
- Surface mount package

## **Product Summary**

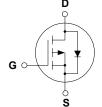
BVDSS	RDSON	ID
-40V	85mΩ	-5A

## **Applications**

- PWM applications
- Load switch
- Power management
- PA Switch

### **SOT-23 Pin Configuration**





COT	22

Туре	Package	Marking
CMN5P04M	SOT-23	5P04M

# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-40	V
$V_{GS}$	Gate-Source Voltage ±20		V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current -5		Α
I <sub>DM</sub>	Pulsed Drain Current	-15	Α
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation	2.0	W
T <sub>STG</sub>	Storage Temperature Range -55 to 150		$^{\circ}$
TJ	Operating Junction Temperature Range -55 to 150		$^{\circ}$

## **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-ambient (Steady State)		125	°C/W



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# Electrical Characteristics (T<sub>J</sub>=25<sup>°</sup>C , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-40			V
D	Static Drain-Source On-Resistance	V <sub>GS</sub> =-10V , I <sub>D</sub> =-2.5A			85	m0
R <sub>DS(ON)</sub>	Static Dialii-Source Off-Resistance	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-2A			110	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250uA$	-1.0		-2.0	٧
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-32V , V <sub>GS</sub> =0V			-1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V, V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-2A		7		S
$Q_g$	Total Gate Charge	I <sub>D</sub> =-3.1A		15		
$Q_gs$	Gate-Source Charge	V <sub>DS</sub> =-20V		3		nC
$Q_gd$	Gate-Drain Charge	V <sub>GS</sub> =-10V		4		
$T_{d(on)}$	Turn-On Delay Time	V <sub>DS</sub> =-20V		10		
Tr	Rise Time	R <sub>G</sub> =3Ω		9		20
$T_{d(off)}$	Turn-Off Delay Time	V <sub>GS</sub> =-10V		29		ns
$T_f$	Fall Time			11		
C <sub>iss</sub>	Input Capacitance			550		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , f=1MHz		90		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			70		

### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A			-1	V

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