

#### P-Channel Enhancement Mode Field Effect Transistor

## **General Description**

The CMN2301M is P-channel enhancement mode Power MOSFET, designed in serried ranks. With fast switching speed, low on-resistance, favorable stabilization.

Used in commercial and industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

# **Features**

- RDS(ON)<110mΩ @ VGS=-4.5V</li>
- RDS(ON)<140mΩ @ VGS=-2.5V</li>
- Simple drive requirement
- Surface mount package

## **Product Summary**

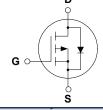
BVDSS	RDSON	ID
-20V	110mΩ	-3A

# **Applications**

- PWM applications
- Load switch
- Power management
- PA Switch

# **SOT-23 Pin Configuration**





SOT-23

Type	Package	Marking
CMN2301M	SOT-23	A1SHB

# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	-20	V	
V <sub>GS</sub>	Gate-Source Voltage	±12	V	
I <sub>D</sub>	Continuous Drain Current -3		Α	
I <sub>DM</sub>	Pulsed Drain Current <sup>b</sup>	-10	Α	
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation	1	W	
T <sub>STG</sub>	Storage Temperature Range -55 to 150		${\mathbb C}$	
T <sub>J</sub>	Operating Junction Temperature Range	150	°C	

## **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>a</sup>		125	°C/W



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# Electrical Characteristics (T<sub>A</sub>=25℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-20			V
D	Static Drain-Source On-Resistance	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-1.0A			110	mΩ
R <sub>DS(ON)</sub>		V <sub>GS</sub> =-2.5V , I <sub>D</sub> =-1.0A			140	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250uA$	-0.5		-1.2	V
I <sub>DSS</sub>	Zero gate voltage drain current	V <sub>DS</sub> =-16V, V <sub>GS</sub> =0V			-1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±12V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> = -5V, I <sub>D</sub> =-0.5A		6		S
$Q_g$	Total Gate Charge	I <sub>D</sub> =-3A		3.5		
$Q_gs$	Gate-Source Charge	V <sub>DS</sub> =-10V		0.8		nC
$Q_{gd}$	Gate-Drain Charge	V <sub>GS</sub> =-2.5V		1.4		
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =-10V		12		
Tr	Rise Time	R <sub>GEN</sub> =10Ω		36		no
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =-1A		31		ns
T <sub>f</sub>	Fall Time	V <sub>GS</sub> =-4.5V		11		
C <sub>iss</sub>	Input Capacitance			520		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =-10V , V <sub>GS</sub> =0V , f=1MHz		75		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			55		

## **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =-1.0A			-1.2	V

## Notes:

a. Surface Mounted on 1in² pad area, t ≤ 10sec.

b. Pulse Width<300µs; Duty Cycle<2.0%

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