

## N- and P-Channel Enhancement Mode MOSFET

### General Description

The CMD609 uses advanced trench technology MOSFETs to provide excellent RDS(ON) and low gate charge.

The complementary MOSFETs may be used in H-bridge, Inverters and other applications.

### Features

- 40V 12A RDS(ON) $\leq$ 21m $\Omega$  @ VGS=10V  
RDS(ON) $\leq$ 25m $\Omega$  @ VGS=4.5V
- -40V -12A RDS(ON) $\leq$ 32m $\Omega$  @ VGS=10V  
RDS(ON) $\leq$ 40m $\Omega$  @ VGS=4.5V
- High Density Cell Design For Ultra Low On Resistance

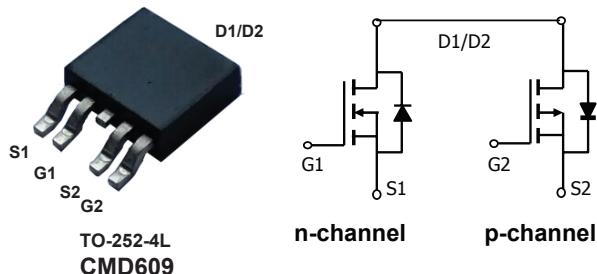
### Product Summary

	BVDSS	RDSON	ID
N-Channel	40V	21m $\Omega$	12A
P-Channel	-40V	32m $\Omega$	-12A

### Applications

- Power Management
- Load Switch
- DC/DC Converter

### TO-252-4L Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Max n-channel	Max p-channel	Units
V <sub>DS</sub>	Drain-Source Voltage	40	-40	V
V <sub>GS</sub>	Gate-Source Voltage	$\pm 20$	$\pm 20$	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current	12	-12	A
I <sub>DM</sub>	Pulsed Drain Current	36	-36	A
E <sub>AR</sub>	Repetitive Avalanche Energy	9.8	20	mJ
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> = 25°C)	27	30	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 175	-55 to 175	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 175	-55 to 175	°C

### Thermal Characteristics: n-channel

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Maximum Junction-to-Ambient	---	60	°C/W
R <sub>θJC</sub>	Maximum Junction-to-Case	---	5.5	°C/W



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## P Channel Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250µA	-40	---	---	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-10V , I <sub>D</sub> =-12A	---	---	32	mΩ
		V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-8A	---	---	40	
V <sub>GSS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250µA	-1	---	-2	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-40V , V <sub>GS</sub> =0V	---	---	-1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-12A	---	18	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz	---	16	---	Ω
Q <sub>g</sub>	Total Gate Charge (10V)	V <sub>DS</sub> =-20V , V <sub>GS</sub> =-10V , I <sub>D</sub> =-12A	---	16	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	4	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	3.5	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DS</sub> =-20V , V <sub>GS</sub> =-10V , R <sub>L</sub> =1.4Ω R <sub>GEN</sub> =3Ω ,	---	6.5	---	ns
T <sub>r</sub>	Rise Time		---	8.5	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	45	---	
T <sub>f</sub>	Fall Time		---	41	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-20V , V <sub>GS</sub> =0V , f=1MHz	---	1300	---	pF
C <sub>oss</sub>	Output Capacitance		---	100	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	70	---	

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>s</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	---	---	-12	A
I <sub>SM</sub>	Pulsed Source Current		---	---	-36	A
t <sub>rr</sub>	Body Diode Reverse Recovery Time	I <sub>F</sub> =-12A , di/dt=100A/µs	---	21	---	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		---	14	---	nC
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =-12A	---	---	1.5	V

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