

General Description

The CMD4511 uses advanced trench technology MOSFETs to provide excellent RDS(ON) and low gate charge.

The complementary MOSFETs may be used in H-bridge, Inverters and other applications.

Features

- 40V 17A RDS(ON)≤13mΩ @ VGS=10V
RDS(ON)≤15mΩ @ VGS=4.5V
- -40V -17A RDS(ON)≤18mΩ @ VGS=-10V
- High Density Cell Design For Ultra Low On Resistance

Absolute Maximum Ratings

Symbol	Parameter	Max n-channel	Max p-channel	Units
V _{DS}	Drain-Source Voltage	40	-40	V
V _{GS}	Gate-Source Voltage	±20	±20	V
I _D @T _C =25°C	Continuous Drain Current	17	-17	A
I _{DM}	Pulsed Drain Current	51	-51	A
EAS	Single Pulse Avalanche Energy	120	225	mJ
P _D @T _C =25°C	Power Dissipation	56	65	W
T _{STG}	Storage Temperature Range	-55 to 150	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	-55 to 150	°C

Thermal Characteristics: n-channel

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Maximum Junction-to-Ambient (Steady-State)	---	60	°C/W
R _{θJc}	Maximum Junction-to-Case (Steady-State)	---	1.4	°C/W

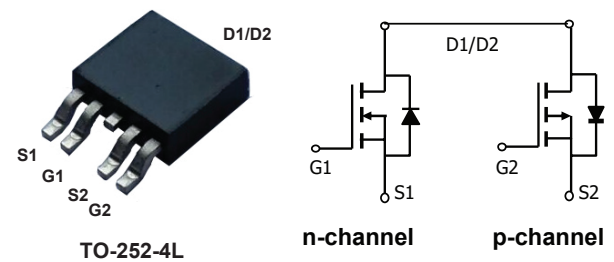
Product Summary

	BVDSS	RDS(ON)	ID
N-Channel	40V	13mΩ	17A
P-Channel	-40V	18mΩ	-17A

Applications

- Power Management
- Load Switch
- DC/DC Converter

TO-252-4L Pin Configuration



Type	Package	Marking
CMD4511	TO-252-4L	CMD4511

Thermal Characteristics: p-channel

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Maximum Junction-to-Ambient (Steady-State)	---	60	$^{\circ}C/W$
$R_{\theta JC}$	Maximum Junction-to-Case (Steady-State)	---	1.4	$^{\circ}C/W$

N Channel Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	40	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=12A$	---	---	13	m Ω
		$V_{GS}=4.5V, I_D=10A$	---	---	16	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1	---	3	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=32V, V_{GS}=0V$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V, I_D=12A$	---	20	---	S
Q_g	Total Gate Charge	$V_{DD}=20V, V_{GS}=10V, I_D=12A$	---	39	---	nC
Q_{gs}	Gate-Source Charge		---	7	---	
Q_{gd}	Gate-Drain Charge		---	9	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=20V, V_{GS}=10V, I_D=12A$ $R_{GEN}=6\Omega$	---	10	---	ns
T_r	Rise Time		---	5	---	
$T_{d(off)}$	Turn-Off Delay Time		---	28	---	
T_f	Fall Time		---	4	---	
C_{iss}	Input Capacitance	$V_{DS}=20V, V_{GS}=0V, f=1MHz$	---	2000	---	pF
C_{oss}	Output Capacitance		---	255	---	
C_{riss}	Reverse Transfer Capacitance		---	165	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V, \text{ Force Current}$	---	---	17	A
I_{SM}	Pulsed Source Current		---	---	51	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=12A, T_J=25C$	---	---	1.2	V

P Channel Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250μA	-40	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-10V, I _D =-8A	---	---	18	mΩ
		V _{GS} =-4.5V, I _D =-5A	---	---	24	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250μA	-1	---	-3	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-32V , V _{GS} =0V	---	---	-1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V , V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} = -5V, I _D =-8A	---	20	---	S
Q _g	Total Gate Charge (10V)	V _{DD} =-20V, V _{GS} =-10V, I _D =-10A	---	38	---	nC
Q _{gs}	Gate-Source Charge		---	6	---	
Q _{gd}	Gate-Drain Charge		---	9	---	
T _{d(on)}	Turn-On Delay Time	V _{DS} =-20V, V _{GS} =-10V, I _D =-10A R _{GEN} =6Ω	---	10	---	ns
T _r	Rise Time		---	7	---	
T _{d(off)}	Turn-Off Delay Time		---	33	---	
T _f	Fall Time		---	8	---	
C _{iss}	Input Capacitance	V _{DS} =-20V , V _{GS} =0V , f=1MHz	---	2600	---	pF
C _{oss}	Output Capacitance		---	330	---	
C _{rss}	Reverse Transfer Capacitance		---	200	---	

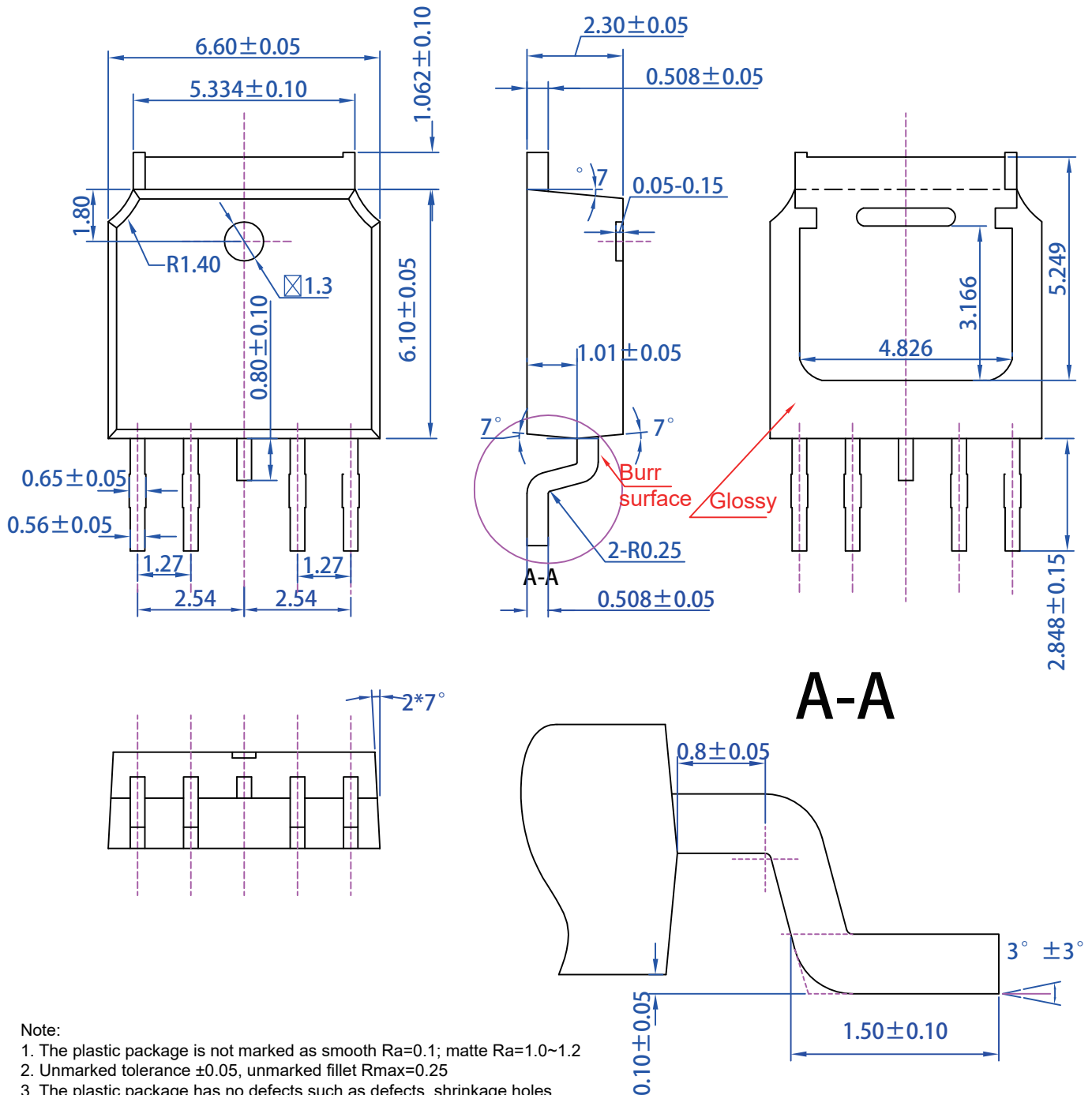
Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V , Force Current	---	---	-17	A
I _{SM}	Pulsed Source Current		---	---	-51	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-8A, T _J =25°C	---	---	-1.2	V

This product has been designed and qualified for the consumer market.
 Cmos assumes no liability for customers' product design or applications.
 Cmos reserves the right to improve product design ,functions and reliability without notice.

Package Dimensions

N- and P-Channel Enhancement Mode MOSFET



Note:

1. The plastic package is not marked as smooth $Ra=0.1$; matte $Ra=1.0\sim 1.2$
2. Unmarked tolerance ± 0.05 , unmarked fillet $R_{max}=0.25$
3. The plastic package has no defects such as defects, shrinkage holes, cracks, bubbles, etc.
4. Marking unit mm
5. The thimble hole is not allowed to protrude from the surface of the plastic package
6. Dislocation of upper and lower plastic package ≤ 0.05 ; plastic package center and lead Center misalignment of wire frames ≤ 0.05