

60V N-Channel MOSFET

## **General Description**

The 30N06AL combines advanced trench MOSFET technology with a low resistance package to provide extremely low RDS(ON). This device is ideal for boost converters and synchronous rectifiers for consumer, telecom, industrial power supplies and LED backlighting.

## **Features**

- N-channel-Enhancement mode
- Lower On-resistance
- 100% Avalanche Tested
- RoHS Compliant

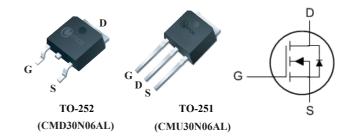
#### **Product Summary**

BVDSS	RDSON	ID
60V	40mΩ	30A

## **Applications**

- DC-DC & DC-AC Converters
- Motor Control, Audio Amplifiers
- High Current, High Speed Switching
- Solenoid And Relay Drivers

## TO-252/251 Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	60	V	
$V_{GS}$	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current <sup>1</sup>	30	Α	
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current <sup>1</sup>	20	А	
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	80	А	
EAS	Single Pulse Avalanche Energy <sup>3</sup>	69	mJ	
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation	40	W	
T <sub>STG</sub>	Storage Temperature Range	-55 to 175	°C	
$T_J$	Operating Junction Temperature Range	-55 to 175	$^{\circ}$	

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup>		50	°C/W
R <sub>eJC</sub>	Thermal Resistance Junction -Case <sup>1</sup>		2.2	°C/W

# CMD30N06AL/CMU30N06AL



60V N-Channel MOSFET

## Electrical Characteristics ( $T_J$ =25 $^{\circ}$ C , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0 $V$ , $I_D$ =250 $\mu A$	60			V
D	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =15A			40	mΩ
R <sub>DS(ON)</sub>		V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A			50	
VGS(th)	Gate Threshold Voltage	$V_{GS}$ = $V_{DS}$ , $I_D$ =250 $\mu$ A	1		3	V
	Drain-Source Leakage Current	V <sub>DS</sub> =60V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃			1	- uA
I <sub>DSS</sub>		V <sub>DS</sub> =60V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃			10	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =20A		34		S
$R_g$	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		4.7		Ω
Qg	Total Gate Charge			28		
$Q_gs$	Gate-Source Charge	V <sub>DS</sub> =30V , V <sub>GS</sub> =10V , I <sub>D</sub> =20A		7.9		nC
$Q_{gd}$	Gate-Drain Charge			8.8		
$T_{d(on)}$	Turn-On Delay Time			10		
Tr	Rise Time	$V_{DD}$ =30V , $V_{G}$ =10V , $R_{G}$ =4.7 $\Omega$		20		ns
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =26A		27		115
T <sub>f</sub>	Fall Time			14		
C <sub>iss</sub>	Input Capacitance			1100		
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		180		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			65		

## **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1</sup>	$V_G$ = $V_D$ = $0V$ , Force Current			30	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2</sup>				80	Α
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =30A ,T <sub>J</sub> =25℃			1.3	V

#### Note:

- 1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq \,$  300us , duty cycle  $\,\leq\,$  2%.
- 3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V,  $V_{GS}$ =10V, L=0.1mH,  $I_{AS}$ =28A.

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