

General Description

The 1402 combines advanced trench MOSFET technology with a low resistance package to provide extremely low RDS(ON). This device is ideal for load switch and battery protection applications.

Features

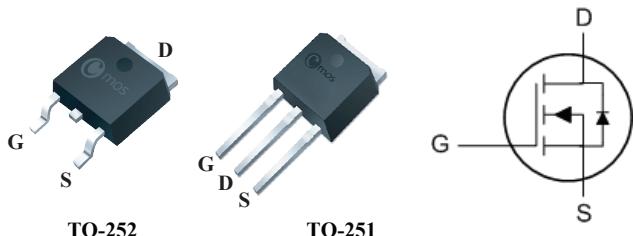
- Simple Drive Requirement
- Ultra-Low RDS(on)
- Green Device Available

Absolute Maximum Ratings**Product Summary**

BVDSS	RDSON	ID
20V	5.5mΩ	50A

Applications

- Server
- DC/DC converter
- Motor drives

TO-252/251 Pin Configuration

Type	Package	Marking
CMD1402	TO-252	CMD1402
CMU1402	TO-251	CMU1402

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current	50	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current	19	A
I_{DM}	Pulsed Drain Current ¹	150	A
EAS	Avalanche energy ⁴	72	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	55	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	110	°C/W
$R_{\theta JC}$	Thermal Resistance Junction -Case	---	2.6	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	20	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=4.5\text{V}$, $I_D=15\text{A}$	---	---	5.5	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$, $I_D=10\text{A}$	---	---	9	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 250\mu\text{A}$	0.5	---	1.2	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=16\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=16\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 12\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance ²	$V_{\text{DS}}=10\text{V}$, $I_D = 20\text{A}$	---	33	---	S
Q_g	Total Gate Charge ($V_{\text{GS}}=4.5\text{V}$)	$V_{\text{DS}}=10\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	---	36	---	nC
Q_{gs}	Gate-Source Charge		---	9	---	
Q_{gd}	Gate-Drain Charge		---	12	---	
$T_{\text{d(on)}}$	Turn-On Delay Time ³	$V_{\text{DS}}=10\text{V}$, $V_{\text{GS}}=5\text{V}$, $R_{\text{GS}}=3.3\Omega$ $I_D \leq 18\text{A}$	---	8	---	ns
T_r	Rise Time ³		---	85	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time ³		---	20	---	
T_f	Fall Time ³		---	25	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=10\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	4000	---	pF
C_{oss}	Output Capacitance		---	450	---	
C_{rss}	Reverse Transfer Capacitance		---	210	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_S = 20\text{A}$	---	---	1.2	V

Note :

- 1.Pulse width limited by maximum junction temperature.
- 2.Pulse test : Pulse Width ≤ 300 usec, Duty Cycle $\leq 2\%$.
- 3.Independent of operating temperature.
- 4.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=15\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.5\text{mH}$, $I_D=17\text{A}$

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