

# **150V N-Channel MOSFET**

## **General Description**

The MOSFETs utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design, provides the designer with anextremely efficient and reliable device for use in a wide variety of applications.

#### Features

- Advanced Process Technology
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Simple Drive Requirements

# **Absolute Maximum Ratings**

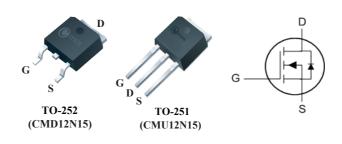
# **Product Summary**

BVDSS	RDSON	ID
150V	0.26Ω	12A

## Applications

- PWM Motor Controls
- LED TV
- DC-DC Converters

#### TO-252/251 Pin Configuration



Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	150	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current	12	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current	9.5	А
I <sub>DM</sub>	Pulsed Drain Current	36	А
EAS	Single Pulse Avalanche Energy	80	mJ
I <sub>AS</sub>	Avalanche Current 7		A
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation	50	W
T <sub>STG</sub>	Storage Temperature Range -55 to 150		°C
TJ	Operating Junction Temperature Range -55 to 1		°C

# **Thermal Data**

Symbol	Parameter	Typ. Max.		Unit	
R <sub>0JA</sub>	Thermal Resistance Junction-ambient		50	°C/W	
R <sub>θJC</sub>	Thermal Resistance Junction -Case		2.78	°C/W	



# **150V N-Channel MOSFET**

# Electrical Characteristics (T\_J=25 $^\circ\!\!\mathbb{C}$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , I <sub>D</sub> =250uA	150			V
_	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =3.5A			260	mΩ
R <sub>DS(ON)</sub>		V <sub>GS</sub> =4.5V , I <sub>D</sub> = 1A			270	
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D = 250 uA$	1		2.5	V
	Drain-Source Leakage Current	$V_{DS}$ =130V , $V_{GS}$ =0V, $T_j$ =25 $^{\circ}$ C			1	- uA
I <sub>DSS</sub>		$V_{DS}$ =120V, $V_{GS}$ =0V, T <sub>j</sub> =150°C			10	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =3A		7.3		S
R <sub>g</sub>	Gate Resistance	$V_{DS}$ =0V , $V_{GS}$ =0V , f=1MHz		2.2		Ω
Qg	Total Gate Charge			10		nC
$Q_gs$	Gate-Source Charge			2.5		
$Q_gd$	Gate-Drain Charge			6		
T <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD}$ =75V , $I_D$ = 9A , $R_G$ =25 $\Omega$		6		
Tr	Rise Time			60		
T <sub>d(off)</sub>	Turn-Off Delay Time			22		ns
T <sub>f</sub>	Fall Time			40		
Ciss	Input Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , f=1MHz		780		
C <sub>oss</sub>	Output Capacitance			24		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			17		

## **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	$-V_G=V_D=0V$ , Force Current			12	А
I <sub>SM</sub>	Pulsed Source Current				36	А
V <sub>SD</sub>	Diode Forward Voltage	$V_{GS}$ =0V , I <sub>S</sub> =6A , T <sub>J</sub> =25 $^{\circ}$ C			1.2	V

This product has been designed and qualified for the counsumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserver the right to improve product design ,functions and reliability wihtout notice.