

P-Channel 30-V (D-S) MOSFET

General Description

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low RDS(on)and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers,PCMCIA cards, cellular and cordless telephones.

Features

- Advanced high cell density Trench technology
- Fast switching speed
- Lower On-resistance
- 100% EAS Guaranteed
- Simple Drive Requirement

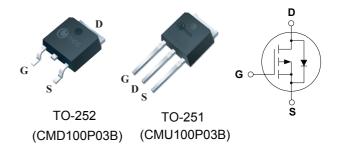
Product Summary

BVDSS	RDSON	ID
-30V	10mΩ	-75A

Applications

- DC-DC Converters
- Desktop PCs
- LED controller

TO-252/251 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
V_{DS}	Drain-Source Voltage	-30	V	
V_{GS}	Gate-Source Voltage	±20	V	
I _D @T _C =25℃	Continuous Drain Current -75		Α	
I _{DM}	Pulsed Drain Current ¹	-180	А	
EAS	Single Pulse Avalanche Energy	115	mJ	
P _D @T _C =25℃	Total Power Dissipation	65	W	
T _{STG}	Storage Temperature Range -55 to 175		°C	
T _J	Operating Junction Temperature Range	-55 to 175	°C	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R _{0JA}	Junction-to-Ambient ²		50	°C/W
$R_{ heta JC}$	Junction-to-Case (Drain)		1.1	°C/W

CMD100P03B/CMU100P03B



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Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-30			V
D	Static Drain-Source On-Resistance ¹	V _{GS} =-10V, I _D =-20A			10	mΩ
R _{DS(ON)}		V _{GS} =-4.5V, I _D =-15A			15	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS} , I_D =-250uA	-1		-3	٧
1	Drain-Source Leakage Current	V_{DS} =-30V, V_{GS} =0V , T_J =25 $^{\circ}$ C			-1	uA
IDSS		V_{DS} =-30V, V_{GS} =0V , T_J =125 $^{\circ}\mathrm{C}$			-50	
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 20 V$, V_{DS} = $0 V$			±100	nA
gfs	Forward Transconductance	V _{DS} =-15V, I _D =-20A		52		S
Qg	Total Gate Charge	V = 24V I = 50A		48		
Q _{gs}	Gate-Source Charge	V _{DS} =-24V , I _D =-50A V _{GS} =0 to -10V		7		nC
Q_{gd}	Gate-Drain Charge			12		
T _{d(on)}	Turn-On Delay Time	V_{DD} =-15V, V_{GS} =-10V, R_{G} =3.5 Ω I_{D} =-50A		10		
Tr	Rise Time			9		
T _{d(off)}	Turn-Off Delay Time			50		ns
T _f	Fall Time			24		
C _{iss}	Input Capacitance	V _{DS} =-25V, V _{GS} =0V , f=1MHz		4200		
Coss	Output Capacitance			800		pF
C _{rss}	Reverse Transfer Capacitance			520		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I _S	Continuous Source Current	-V _G =V _D =0V , Force Current			-75	Α
I _{SM}	Pulsed Source Current ¹				-180	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _F =-30A			-1.5	V

Notes

1. Pulse test; pulse width≤ 300µs, duty cycle≤ 2%.

2. When mounted on 1" square PCB (FR-4 material).

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