

Voltage Supervisor with Watchdog Timer, Manual Reset, and Dual Outputs

Features

- Precision 1.8V to 5V Power Supply Monitor
- $\pm 0.5\%$ Threshold Accuracy at $T_A = +25^\circ\text{C}$
- $\pm 1.5\%$ Threshold Accuracy from $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$
- 3.8 μA Supply Current (MIC826S/R/T/W/Y/Z)
- 4.8 μA Supply Current (MIC826L/M)
- Manual Reset Input
- 140 ms (min.) Reset Timeout Period
- 1.6s (typ.) Watchdog Timeout Period
- Float Watchdog Timer Input to Disable Watchdog Timer
- Active-High and Active-Low Push-Pull Outputs
- Ensured Reset Output Valid to $V_{CC} = 1\text{V}$
- -40°C to $+125^\circ\text{C}$ Junction Temperature Range
- 6-Lead 1.6 mm x 1.6 mm x 0.5 mm UDFN Package
- 50% Smaller Version of MAX823/MAX824/MAX825/ADM823/ADM824/ADM825
- 8x Lower Watchdog Input Current than the Competition

Applications

- Microprocessor Systems
- Computers
- Intelligent Instruments

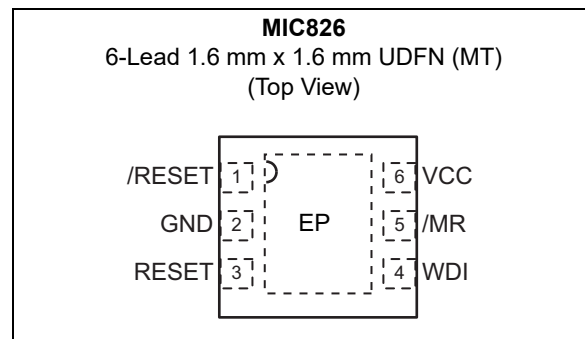
General Description

The MIC826 is a low-current, ultra-small, voltage supervisor with manual reset input, watchdog timer, and active-high and active-low push-pull outputs. The reset outputs are asserted and held when the supply voltage falls below the factory-programmed threshold voltage, when the /MR pin is pulled low, or if the watchdog times out. Reset is asserted for the reset timeout delay after the supply voltage increases above the rising threshold voltage or when manual reset input is asserted high.

The MIC826 features an integrated pull-up resistor on the /MR. An active-low push-pull reset output (/RESET) and an active-high reset output (RESET) provide flexibility when interfacing with various microcontrollers, PMICs, or load switches. The watchdog input can be left unconnected for applications that do not require watchdog monitoring.

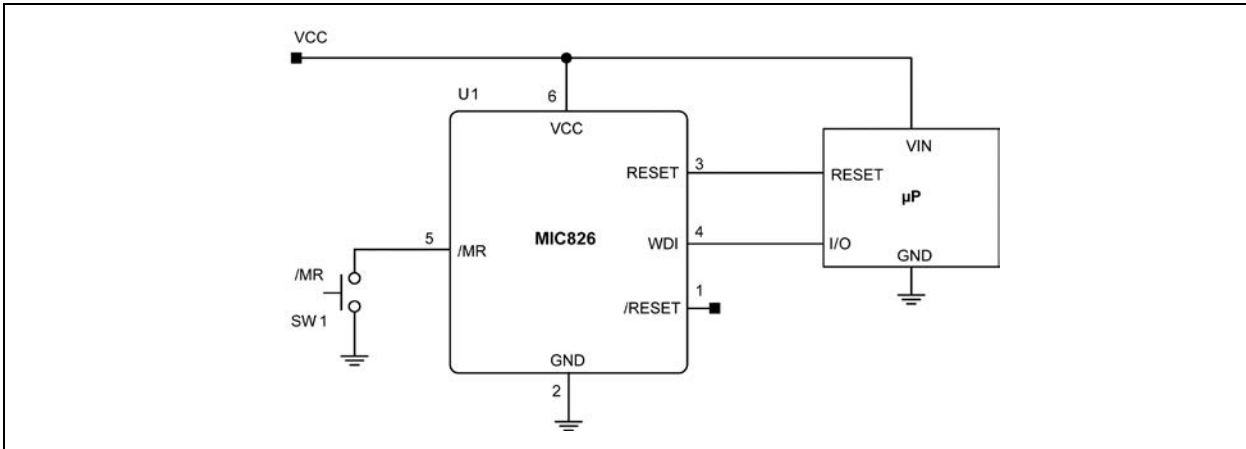
The MIC826 consumes a quiescent current of only 3.8 μA and is offered in a tiny, space-saving, 6-lead 1.6 mm x 1.6 mm UDFN package. It is rated for the -40°C to $+125^\circ\text{C}$ temperature range.

Package Type

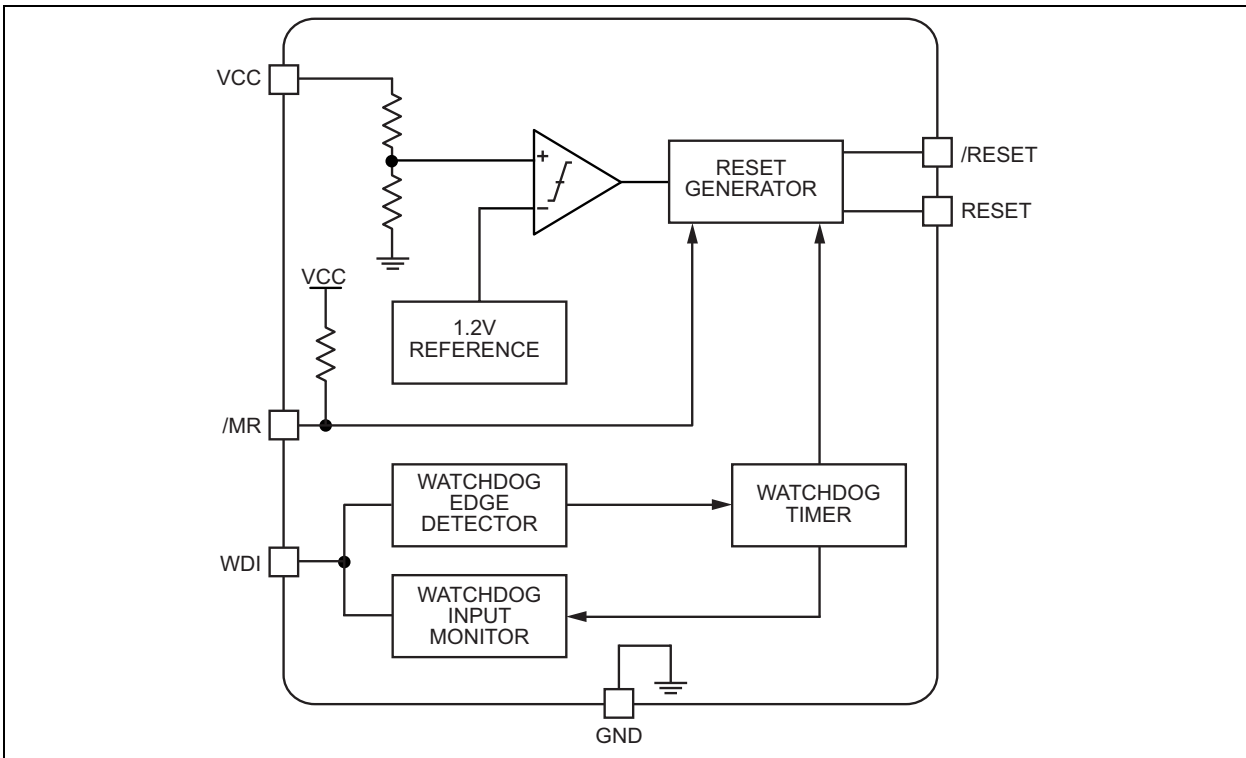


MIC826

Typical Application Circuit



Functional Block Diagram



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

Supply Voltage (V_{CC})	-0.3V to +6.0V
/MR, WDI, RESET, /RESET	-0.3V to $V_{CC} + 0.3V$
Input Current, All Pins except (RESET, /RESET)	20 mA
Output Current (RESET, /RESET)	20 mA
ESD Rating (Note 1) Human Body Model	1.5 kV
ESD Rating (Note 1) Machine Model	200V

Operating Ratings ‡

Supply Voltage (V_{CC})	+1V to +5.5V
Watchdog Input (WDI)	0V to V_{CC}

† **Notice:** Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability.

‡ **Notice:** The device is not guaranteed to function outside its operating ratings.

Note 1: Devices are ESD sensitive; use proper handling precautions. Human body model, 1.5 k Ω in series with 100 pF.

ELECTRICAL CHARACTERISTICS

Electrical Characteristics: $V_{CC} = 4.7V$ to $5.5V$ for MIC826L, $V_{CC} = 4.5V$ to $5.5V$ for MIC826M, $V_{CC} = 3.15V$ to $3.6V$ for MIC826T, $V_{CC} = 3.0V$ to $3.6V$ for MIC826S, $V_{CC} = 2.7V$ to $3.6V$ for MIC826R, $V_{CC} = 2.38V$ to $2.75V$ for MIC826Z, $V_{CC} = 2.25V$ to $2.75V$ for MIC826Y, $V_{CC} = 1.75V$ to $2.0V$ for MIC826W; $T_J = +25^\circ C$, **bold values (Note 2)** valid for $-40^\circ C \leq T_J \leq +125^\circ C$, unless otherwise noted. (Note 1)

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Power Supply Input						
Supply Voltage	V_{CC}	1.0	—	5.5	V	—
Supply Current	I_{VCC}	—	4.8	14	μA	MIC826L/M (WDI and /MR unconnected)
		—	3.8	8.4		MIC826T/S/R/Z/Y/W (WDI and /MR unconnected)
Reset Threshold Voltage						
Reset Threshold Voltage	V_{RST}	4.602	4.625	4.648	V	L (falling) $T_A = 25^\circ C$
		4.556	—	4.694		L (falling) $-40^\circ C \leq T_A \leq +125^\circ C$
		4.353	4.375	4.397		M (falling) $T_A = 25^\circ C$
		4.309	—	4.441		M (falling) $-40^\circ C \leq T_A \leq +125^\circ C$
		3.060	3.075	3.090		T (falling) $T_A = 25^\circ C$
		3.029	—	3.121		T (falling) $-40^\circ C \leq T_A \leq +125^\circ C$
		2.910	2.925	2.940		S (falling) $T_A = 25^\circ C$
		2.881	—	2.969		S (falling) $-40^\circ C \leq T_A \leq +125^\circ C$
		2.612	2.625	2.638		R (falling) $T_A = 25^\circ C$
		2.586	—	2.664		R (falling) $-40^\circ C \leq T_A \leq +125^\circ C$
		2.303	2.315	2.327		Z (falling) $T_A = 25^\circ C$
		2.280	—	2.350		Z (falling) $-40^\circ C \leq T_A \leq +125^\circ C$
		2.177	2.188	2.199		Y (falling) $T_A = 25^\circ C$
		2.155	—	2.221		Y (falling) $-40^\circ C \leq T_A \leq +125^\circ C$
		1.657	1.665	1.673		W (falling) $T_A = 25^\circ C$
		1.640	—	1.690		W (falling) $-40^\circ C \leq T_A \leq +125^\circ C$

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ELECTRICAL CHARACTERISTICS (CONTINUED)

Electrical Characteristics: $V_{CC} = 4.7V$ to $5.5V$ for MIC826L, $V_{CC} = 4.5V$ to $5.5V$ for MIC826M, $V_{CC} = 3.15V$ to $3.6V$ for MIC826T, $V_{CC} = 3.0V$ to $3.6V$ for MIC826S, $V_{CC} = 2.7V$ to $3.6V$ for MIC826R, $V_{CC} = 2.38V$ to $2.75V$ for MIC826Z, $V_{CC} = 2.25V$ to $2.75V$ for MIC826Y, $V_{CC} = 1.75V$ to $2.0V$ for MIC826W; $T_J = +25^\circ C$, **bold** values (Note 2) valid for $-40^\circ C \leq T_J \leq +125^\circ C$, unless otherwise noted. (Note 1)

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Reset Threshold Hysteresis	V_{RST_HYS}	—	10	—	mV	L, M
		—	5	—		T, S, R, Z, Y, W
Reset Threshold Temperature Coefficient		—	40	—	ppm/ $^\circ C$	—
Reset Timeout Period	t_{RP}	140	200	280	ms	—
Reset Time						
VCC to /RESET Delay	t_{RD}	—	10	—	μs	—
Reset Outputs (/RESET, RESET)						
/RESET Output Voltage High	V_{OH}	$V_{CC} - 1.5$	—	—	V	L, M: $V_{CC} = V_{RST(MAX)}$, $I_{SOURCE} = 120 \mu A$
		$0.8 \times V_{CC}$	—	—		T, S, R, Z, Y: $V_{CC} = V_{RST(MAX)}$, $I_{SOURCE} = 30 \mu A$
		$0.8 \times V_{CC}$	—	—		W: $V_{CC} = V_{RST(MAX)}$, $I_{SOURCE} = 20 \mu A$
/RESET Output Voltage Low	V_{OL}	—	—	0.4	V	L, M: $V_{CC} = V_{RST(MIN)}$, $I_{SINK} = 3.2 mA$
		—	—	0.3		T, S, R, Z, Y, W: $V_{CC} = V_{RST(MIN)}$, $I_{SINK} = 1.2 mA$
		—	—	0.3		$T_A = 0^\circ C$ to $+70^\circ C$, $V_{CC} = 1V$, $I_{SINK} = 50 \mu A$
		—	—	0.3		$V_{CC} = 1.2V$, $I_{SINK} = 100 \mu A$
/RESET Output Short-Circuit Current (Note 3)	I_{SC}	—	—	800	μA	L, M: /RESET = 0V, $V_{CC} = 5.5V$
		—	—	400		T, S, R, Z, Y, W: /RESET = 0V, $V_{CC} = 3.6V$
RESET Output Voltage High	V_{OH}	$0.8 \times V_{CC}$	—	—	V	$V_{CC} > 1.6V$, $I_{SOURCE} = 150 \mu A$
RESET Output Voltage Low	V_{OL}	—	—	0.4	V	L, M: $V_{CC} = V_{RST(MAX)}$, $I_{SINK} = 3.2 mA$
		—	—	0.3		T, S, R, Z, Y, W: $V_{CC} = V_{RST(MAX)}$, $I_{SINK} = 1.2 mA$
Watchdog Input (WDI) (Note 4)						
Watchdog Timeout Period	t_{WD}	1.12	1.60	2.40	sec.	—
Watchdog Pulse Width		50	—	—	ns	—
Watchdog Input Low Voltage	V_{IL}	—	—	$0.3 \times V_{CC}$	V	—
Watchdog Input High Voltage	V_{IH}	$0.7 \times V_{CC}$	—	—	V	—
Watchdog Input Current	I_{IN}	—	15	25	μA	WDI = V_{CC}
		-25	-15	—		WDI = 0V
Manual Reset Input (/MR)						
Input Low Voltage	V_{IL}	—	—	$0.3 \times V_{CC}$	V	—
Input High Voltage	V_{IH}	$0.7 \times V_{CC}$	—	—	V	—

ELECTRICAL CHARACTERISTICS (CONTINUED)

Electrical Characteristics: $V_{CC} = 4.7V$ to $5.5V$ for MIC826L, $V_{CC} = 4.5V$ to $5.5V$ for MIC826M, $V_{CC} = 3.15V$ to $3.6V$ for MIC826T, $V_{CC} = 3.0V$ to $3.6V$ for MIC826S, $V_{CC} = 2.7V$ to $3.6V$ for MIC826R, $V_{CC} = 2.38V$ to $2.75V$ for MIC826Z, $V_{CC} = 2.25V$ to $2.75V$ for MIC826Y, $V_{CC} = 1.75V$ to $2.0V$ for MIC826W; $T_J = +25^\circ C$, **bold** values (Note 2) valid for $-40^\circ C \leq T_J \leq +125^\circ C$, unless otherwise noted. (Note 1)

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
/MR Pulse Width		1.0	—	—	μs	—
/MR Noise Immunity (pulse width with no reset)		—	100	—	ns	—
/MR to RESET Delay		—	500	—	ns	—
/MR Internal Pull-Up Resistor		35	52	75	$k\Omega$	—

Note 1: Specifications are for packaged products only.

2: Overtemperature limits are ensured by design and not product tested.

3: The /RESET short-circuit current is the maximum pull-up current when /RESET is driven low by μP bidirectional reset pin.

4: The WDI input is designed to drive a three-state output device with a $10 \mu A$ maximum leakage current and a maximum capacitance of $200 pF$.

TEMPERATURE SPECIFICATIONS

Parameters	Sym.	Min.	Typ.	Max.	Units	Conditions
Temperature Ranges						
Maximum Junction Temperature Range	$T_{J(MAX)}$	-40	—	+150	$^\circ C$	—
Operating Junction Temperature Range	T_J	-40	—	+125	$^\circ C$	Note 1
Storage Temperature Range	T_S	-65	—	+150	$^\circ C$	—
Package Thermal Resistances						
Thermal Resistance, UDFN 6-Ld	θ_{JA}	—	92.4	—	$^\circ C/W$	—
	θ_{JC}	—	56.4	—	$^\circ C/W$	—

Note 1: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction to air (i.e., T_A , T_J , θ_{JA}). Exceeding the maximum allowable power dissipation will cause the device operating junction temperature to exceed the maximum $+125^\circ C$ rating. Sustained junction temperatures above $+125^\circ C$ can impact the device reliability.

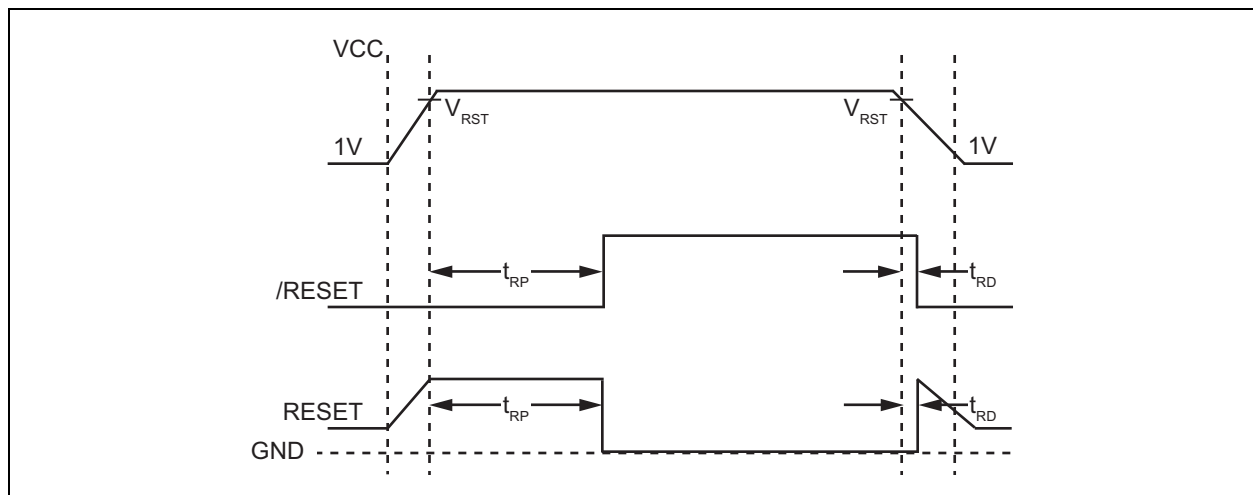


FIGURE 1-1: Timing Diagram.

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2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

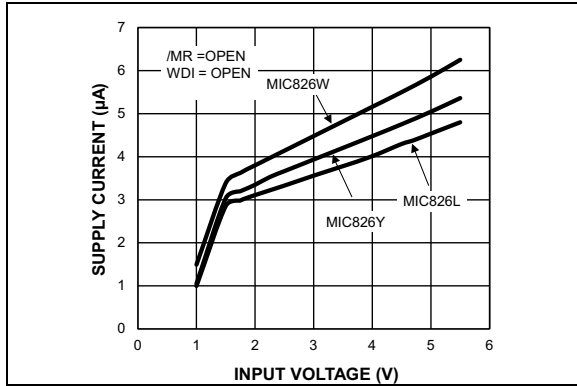


FIGURE 2-1: Supply Current vs. Input Voltage.

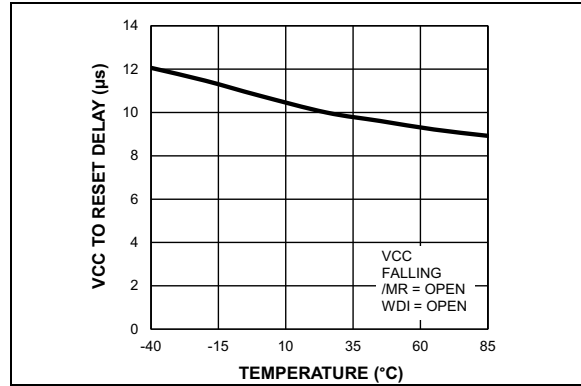


FIGURE 2-4: VCC to RESET Delay vs. Temperature.

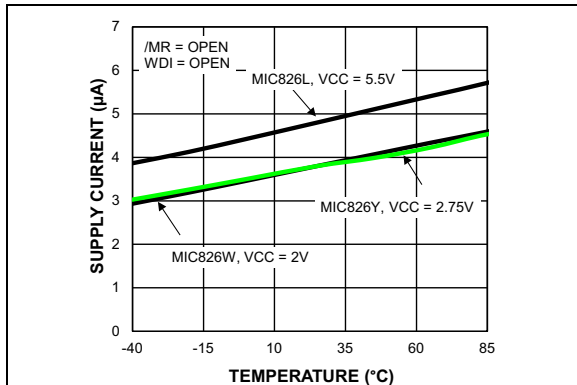


FIGURE 2-2: Supply Current vs. Temperature.

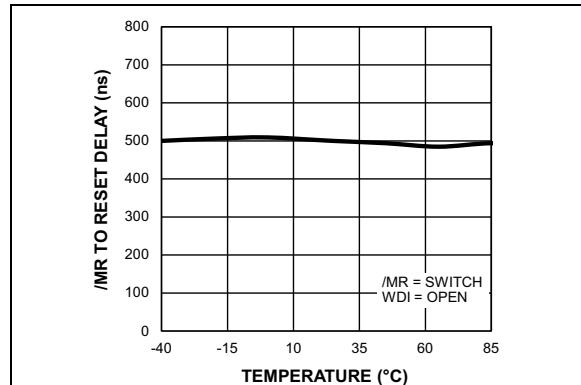


FIGURE 2-5: /MR to RESET Delay vs. Temperature.

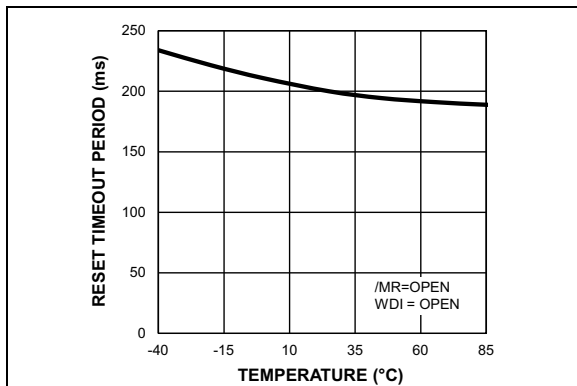


FIGURE 2-3: RESET Timeout Period vs. Temperature.

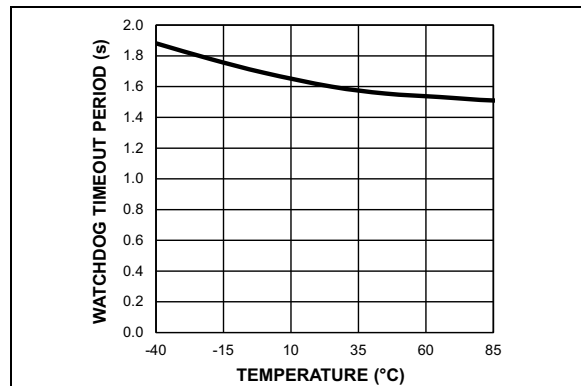


FIGURE 2-6: Watchdog Timeout Period vs. Temperature.

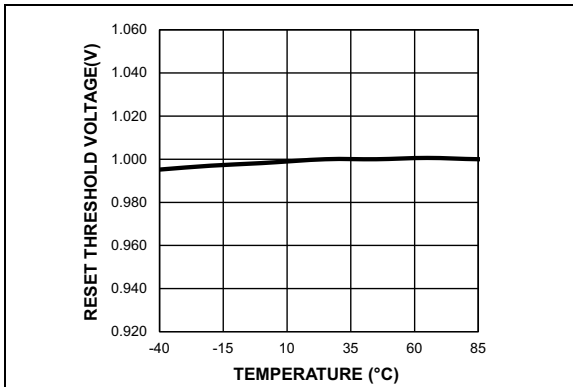


FIGURE 2-7: Normalized Reset Threshold vs. Temperature.

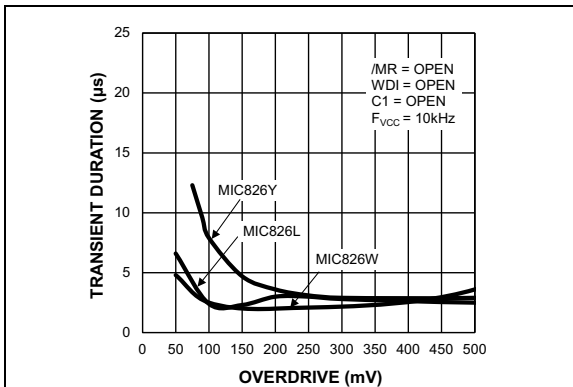


FIGURE 2-8: Supply Transient Duration vs. Overdrive.

MIC826

3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in [Table 3-1](#).

TABLE 3-1: PIN FUNCTION TABLE

Pin Number	Pin Name	Description
1	/RESET	Active-Low Reset Output. Pulses low for 200 ms when triggered, and remains low whenever V_{CC} is below the reset threshold or when /MR is a logic low. It remains low for 200 ms after one of the following occurs: V_{CC} rises above the reset threshold, the watchdog triggers a reset, or /MR goes low to high.
2	GND	Supply Ground.
3	RESET	Active-High Reset Output. Pulses high for 200 ms when triggered, and remains high whenever V_{CC} is below the reset threshold or when /MR is a logic low. It remains high for 200 ms after one of the following occurs: V_{CC} rises above the reset threshold, the watchdog triggers a reset, or /MR goes low to high. RESET is the inverse of /RESET.
4	WDI	Watchdog Input. If WDI remains either high or low for longer than the watchdog timeout period, the internal watchdog timer runs out and a reset is triggered. The internal watchdog timer clears whenever reset is asserted, or whenever WDI sees a rising or falling edge. If WDI is left unconnected or is connected to a three-stated buffer output, the watchdog feature is disabled.
5	/MR	Manual Reset Input. A logic low on /MR asserts reset. Reset remains asserted as long as /MR is held low and for 200 ms after /MR returns high. The active-low input has an internal 52 k Ω pull-up resistor. It can be driven from a CMOS logic line or shorted to ground with a switch. Leave open or connect to VCC if unused.
6	VCC	Supply Voltage Pin. VCC is the input to the supply voltage monitor. When the supply voltage falls below the reset threshold voltage, reset outputs (/RESET and RESET) are triggered immediately. When the supply voltage rises above the reset threshold plus hysteresis voltage, the reset outputs remain asserted for the duration of the reset timeout period. A decoupling ceramic capacitor (minimum 0.1 μ F) must be connected between VCC and GND close to the pins.
EP	ePAD	Exposed Pad. Connect to GND.

4.0 FUNCTIONAL DESCRIPTION

4.1 Design and Product Advantages

The MIC826 is a voltage supervisor reset IC with a manual reset input, watchdog input, and dual reset outputs. The MIC826 asserts and holds a reset when the supply voltage falls below the factory-programmed threshold voltage, when $\overline{\text{MR}}$ is at low logic level, or when watchdog input times out. Reset is asserted for a fixed reset timeout period after the supply voltage increases above the rising threshold voltage or when the manual reset input is asserted high.

The MIC826 features an active-low, push-pull reset output ($\overline{\text{RESET}}$) and an active-high, push-pull reset output (RESET). It also contains 52 k Ω pull-up resistors on the $\overline{\text{MR}}$ input to make it easy to connect to pushbutton inputs. The MIC826 also features a watchdog (WDI) input.

4.2 Manual Reset Input ($\overline{\text{MR}}$)

The $\overline{\text{MR}}$ input pin has integrated pull-up resistors to keep the current consumption low when the pushbutton switch is pressed.

4.3 Reset Outputs ($\overline{\text{RESET}}$ and RESET)

There are two reset outputs: $\overline{\text{RESET}}$ and RESET. Both outputs are push-pull, which eliminates the need for pull-up resistors. The RESET output is the inverse of the $\overline{\text{RESET}}$ output.

4.4 Asserting $\overline{\text{RESET}}$ and RESET Outputs

4.4.1 WHEN FUNCTIONING AS A MANUAL RESET

The reset outputs $\overline{\text{RESET}}$ and RESET are asserted while the $\overline{\text{MR}}$ pin is driven low. Both reset outputs remain asserted for a factory-programmed reset timeout delay time, t_{RP} . The typical value of a reset timeout period is 200 ms. The $\overline{\text{RESET}}$ pin is driven low while the RESET pin is driven high whenever $\overline{\text{MR}}$ is pulled low.

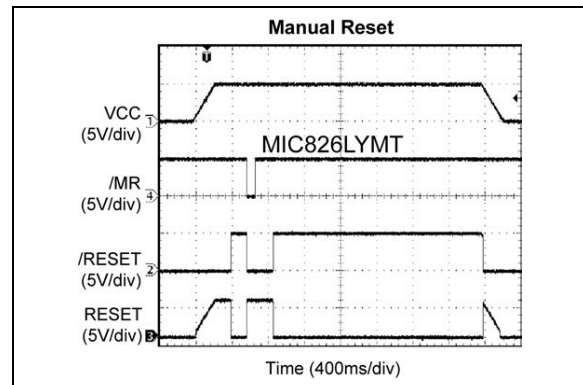


FIGURE 4-1: Manual Reset Function.

4.4.2 WHEN FUNCTIONING AS A VOLTAGE SUPERVISOR

The $\overline{\text{RESET}}$ pin is asserted whenever V_{CC} falls below the reset threshold voltage, V_{RST} ($V_{\text{CC}} < V_{\text{RST}}$). The V_{CC} pin circuitry includes hysteresis to prevent $\overline{\text{RESET}}$ or RESET pin chattering due to noise. The $\overline{\text{RESET}}$ pin remains asserted for the duration of the reset timeout delay (t_{RP}) after V_{CC} has risen above the reset threshold voltage plus the hysteresis. The reset function ensures that a microprocessor is properly reset and powers up in a known condition after a power failure. The $\overline{\text{RESET}}$ remains valid with V_{CC} as low as 1V. The RESET output is a complement of the $\overline{\text{RESET}}$ output.

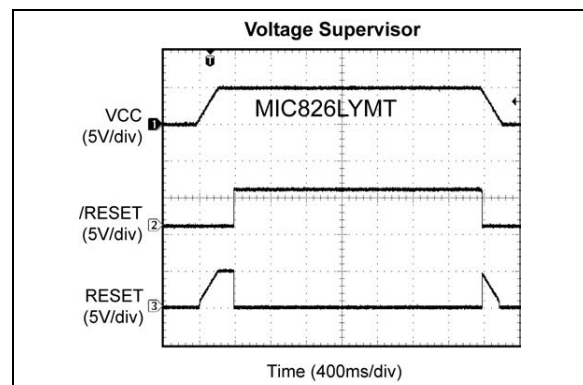


FIGURE 4-2: Voltage Supervisor Function.

4.4.3 WATCHDOG INPUT

The MIC826 has an on-chip watchdog timer that can reset the microprocessor if it fails to strobe the WDI input within a preset timeout period (t_{WD}). If the WDI pin is left unconnected, this function is disabled. The watchdog timeout period starts when either of the reset outputs becomes inactive and either a high-to-low or low-to-high transition occurs before the watchdog timeout period (t_{WD}). If the watchdog timer times out, then the reset outputs will assert and stay asserted for a reset timeout period (t_{RP}). The watchdog timer is also cleared whenever the reset outputs are asserted by

MIC826

/MR, or if V_{CC} falls below V_{RST} . The watchdog timer starts again whenever the reset outputs are de-asserted.

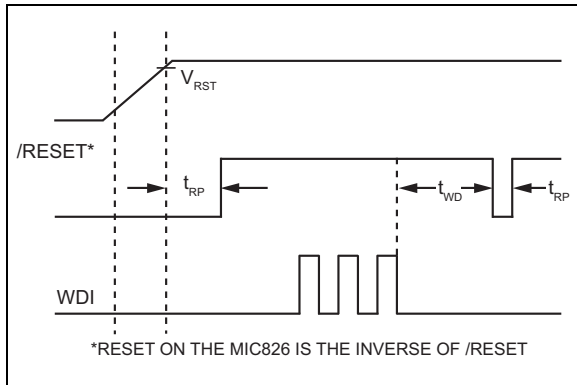


FIGURE 4-3: Watchdog Timing.

4.5 Watchdog Input Current

The WDI input current is the current sourced or sunk by the WDI pin when the WDI input is driven low or high, respectively. The WDI input is designed to drive a tri-state output device with a 10 μ A maximum leakage current and a maximum capacitance of 200 pF.

4.6 V_{CC} Transient

The MIC826 is relatively immune to small negative-going V_{CC} glitches below the reset threshold. As shown in Figure 4-4, the overdrive voltage is the difference between the threshold voltage and the minimum point of the V_{CC} glitch. Typically, an overdrive of 100 mV with duration of 2 μ s or less will not cause a reset.

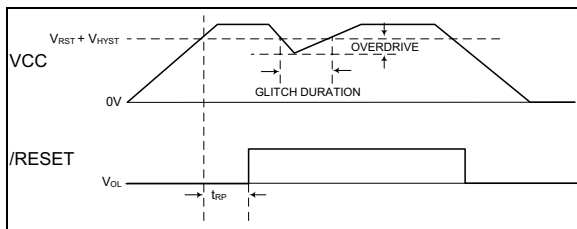


FIGURE 4-4: V_{CC} Transient.

4.7 Reset Valid Up to V_{CC} = 1V

The MIC826 is a supervisory circuit that monitors the power supply voltage level. If the power supply voltage level falls below the factory-programmed threshold level, then the reset signals are asserted. The MIC826 maintains the reset states with V_{CC} as low as 1V.

4.8 Supply Bypass Capacitor

An optional 0.1 μ F input bypass capacitor can be placed from V_{CC} (Pin 6) to GND (Pin 2) to increase glitch rejection.

5.0 TYPICAL APPLICATIONS

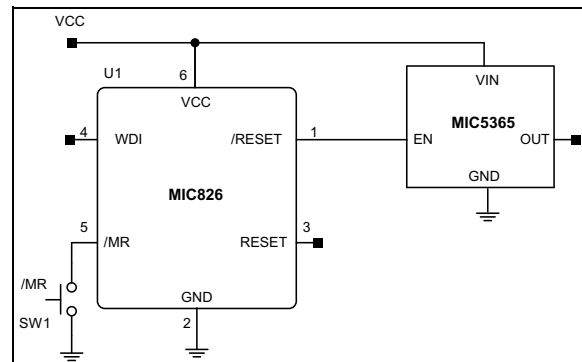


FIGURE 5-1: MIC826 for Precision Power Good Function.

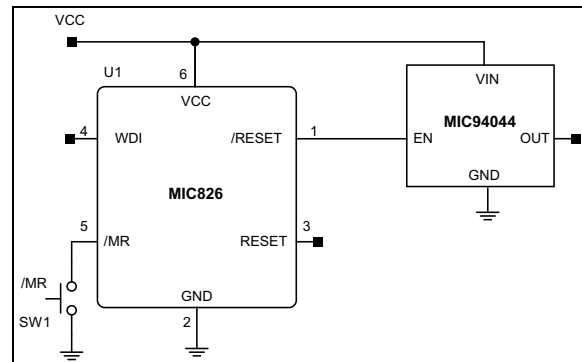


FIGURE 5-2: MIC826 Used for Load Switch.

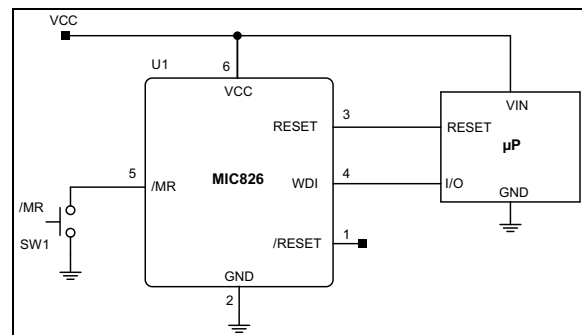


FIGURE 5-3: MIC826 Used for Microcontroller Reset with Watchdog Input.

6.0 EVALUATION BOARD SCHEMATIC

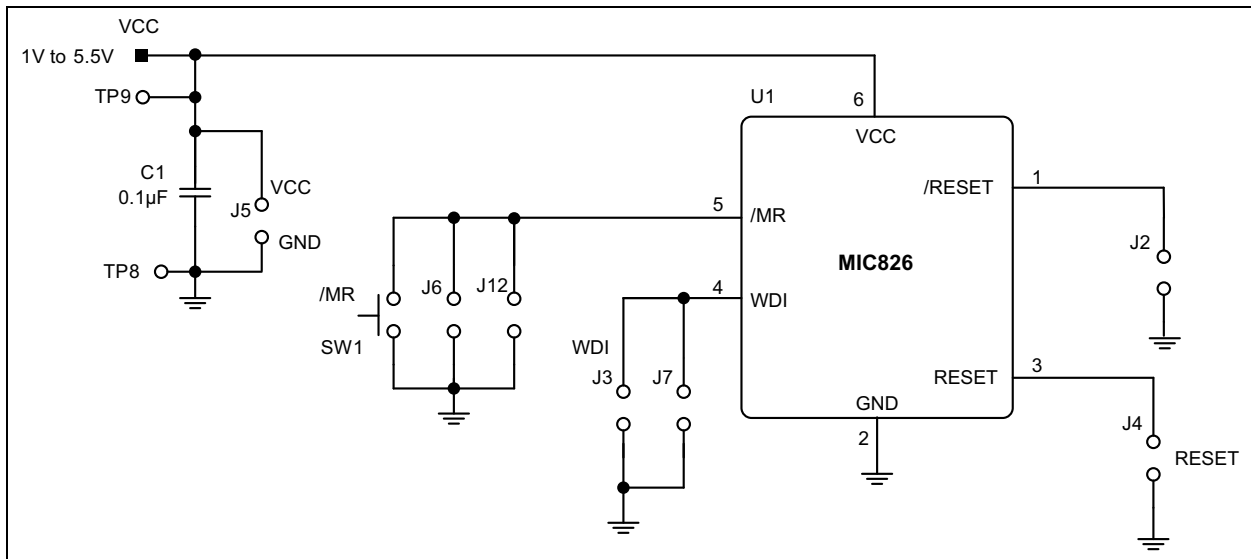


FIGURE 6-1: MIC826 Evaluation Board Schematic.

6.1 Bill of Materials

TABLE 6-1: BILL OF MATERIALS

Item	Part Number	Manufacturer	Description	Qty.
C1	GRM188R71C104KA01D	Murata	0.1 µF, 16V capacitor, X7R, 0603	1
U1	MIC826xYMT	Microchip	Voltage Supervisor with Watchdog Timer, Manual Reset, and Dual Outputs	1

MIC826

7.0 PCB LAYOUT RECOMMENDATIONS

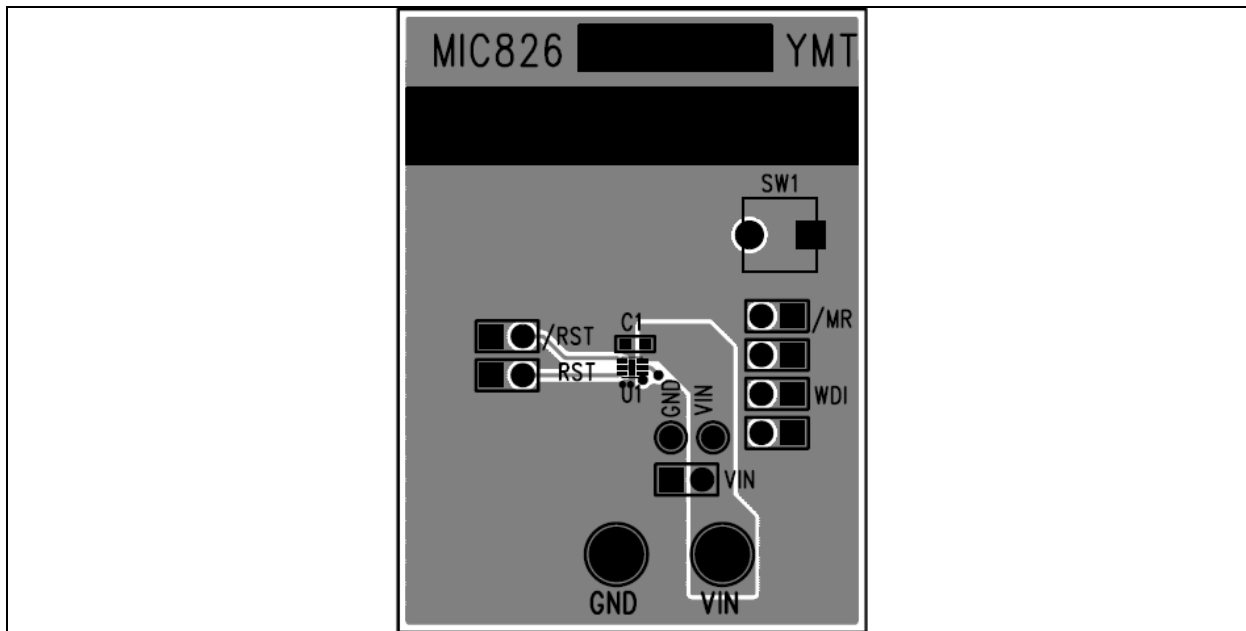


FIGURE 7-1: Top Layer.

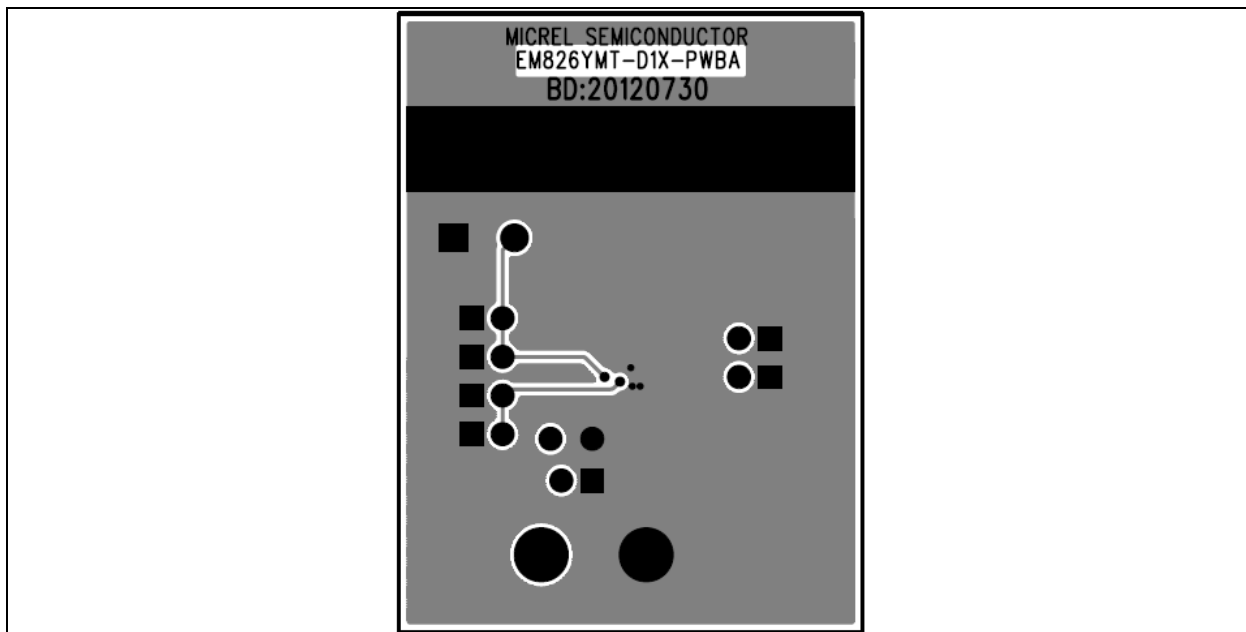
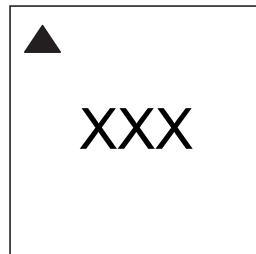


FIGURE 7-2: Bottom Layer.

8.0 PACKAGING INFORMATION

8.1 Package Marking Information

6-Lead UDFN*



Example

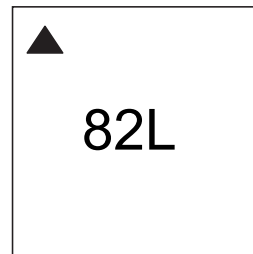


TABLE 8-1: MARKING CODES

Part Number	Marking Code	Nominal Threshold Voltage
MIC826LYMT	82L	4.625
MIC826MYMT	82M	4.375
MIC826TYMT	82T	3.075
MIC826SYMT	82S	2.925
MIC826RYMT	82R	2.625
MIC826ZYMt	82Z	2.315
MIC826YYMT	82Y	2.188
MIC826WYMT	82W	1.665

<p>Legend: XX...X Product code or customer-specific information Y Year code (last digit of calendar year) YY Year code (last 2 digits of calendar year) WW Week code (week of January 1 is week '01') NNN Alphanumeric traceability code (e3) Pb-free JEDEC® designator for Matte Tin (Sn) * This package is Pb-free. The Pb-free JEDEC designator ((e3)) can be found on the outer packaging for this package.</p> <p>•, ▲, ▼ Pin one index is identified by a dot, delta up, or delta down (triangle mark).</p>
<p>Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information. Package may or may not include the corporate logo.</p> <p>Underbar (_) symbol may not be to scale.</p>

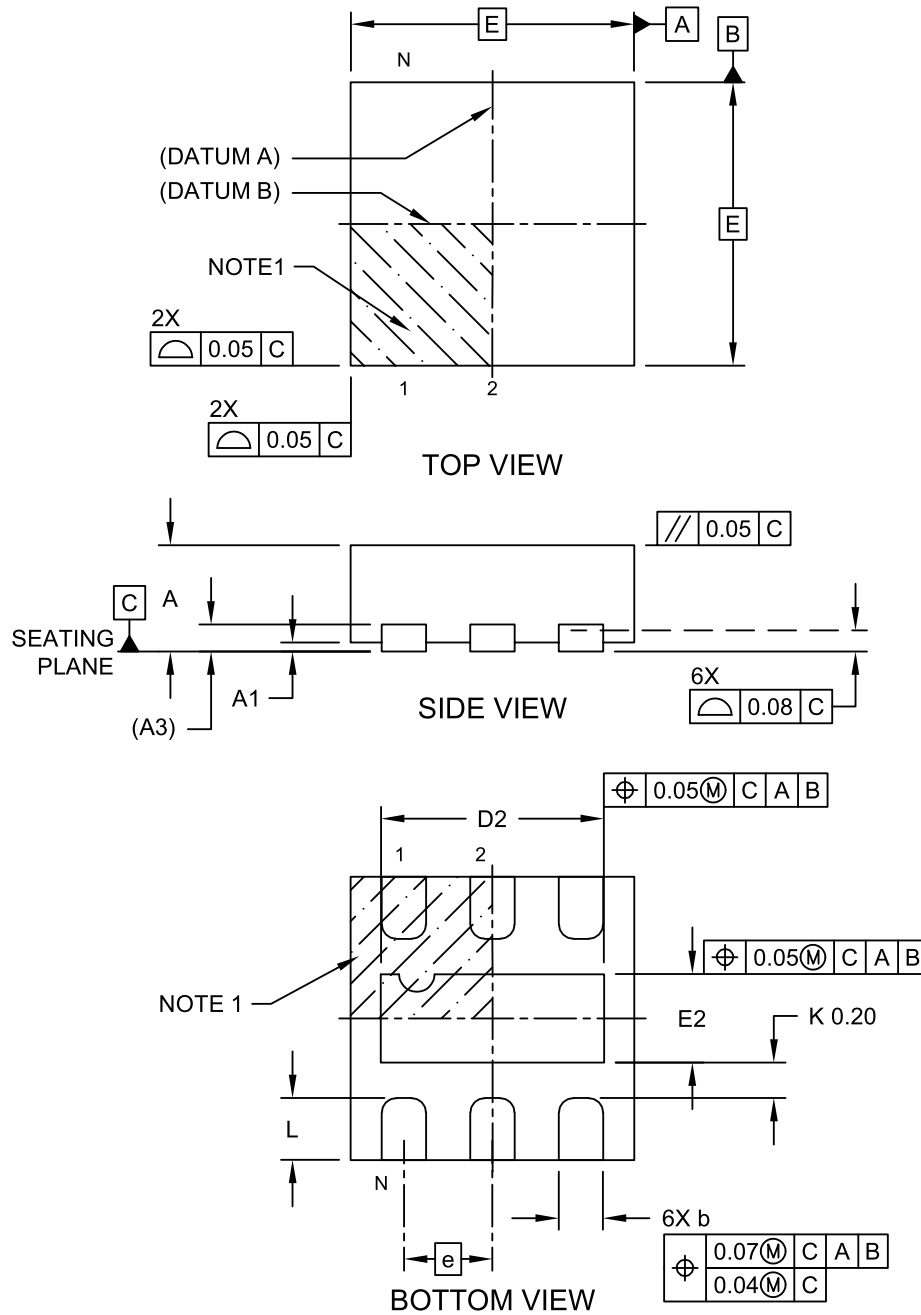
Note: If the full seven-character YYWWNNN code cannot fit on the package, the following truncated codes are used based on the available marking space:
 6 Characters = YWWNNN; 5 Characters = WWNNN; 4 Characters = WNNN; 3 Characters = NNN;
 2 Characters = NN; 1 Character = N

MIC826

6-Lead 1.6 mm x 1.6 mm UDFN Package Outline and Recommended Land Pattern

6-Lead Ultra Thin Plastic Dual Flat, No Lead (HKA) - 1.6x1.6x0.6 mm Body [UDFN] With 1.26x0.50 mm Exposed Pad; Micrel Legacy Package TDFN1616-6LD-PL-1

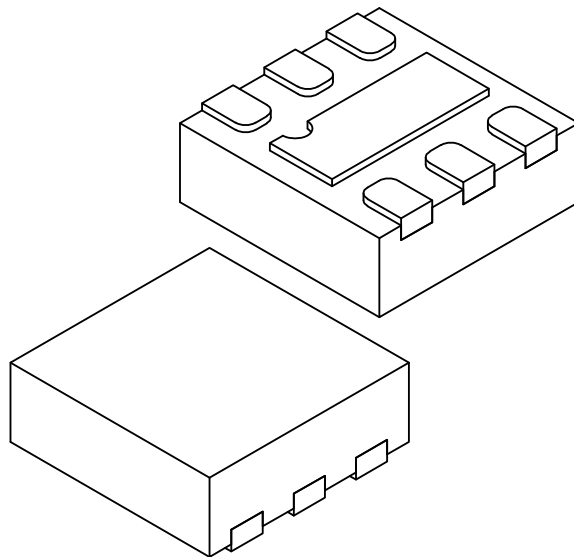
Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Microchip Technology Drawing C04-1154 Rev A Sheet 1 of 2

6-Lead Ultra Thin Plastic Dual Flat, No Lead (HKA) - 1.6x1.6x0.6 mm Body [UDFN] With 1.26x0.50 mm Exposed Pad; Micrel Legacy Package TDFN1616-6LD-PL-1

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Number of Terminals	N	6		
Pitch	e	0.50 BSC		
Overall Height	A	0.50	0.55	0.60
Standoff	A1	0.00	0.02	0.05
Terminal Thickness	A3	0.152 REF		
Overall Length	D	1.60 BSC		
Exposed Pad Length	D2	1.21	1.26	1.31
Overall Width	E	1.60 BSC		
Exposed Pad Width	E2	0.45	0.50	0.55
Terminal Width	b	0.20	0.25	0.30
Terminal Length	L	0.30	0.35	0.40
Terminal-to-Exposed-Pad	K	0.20	-	-

Notes:

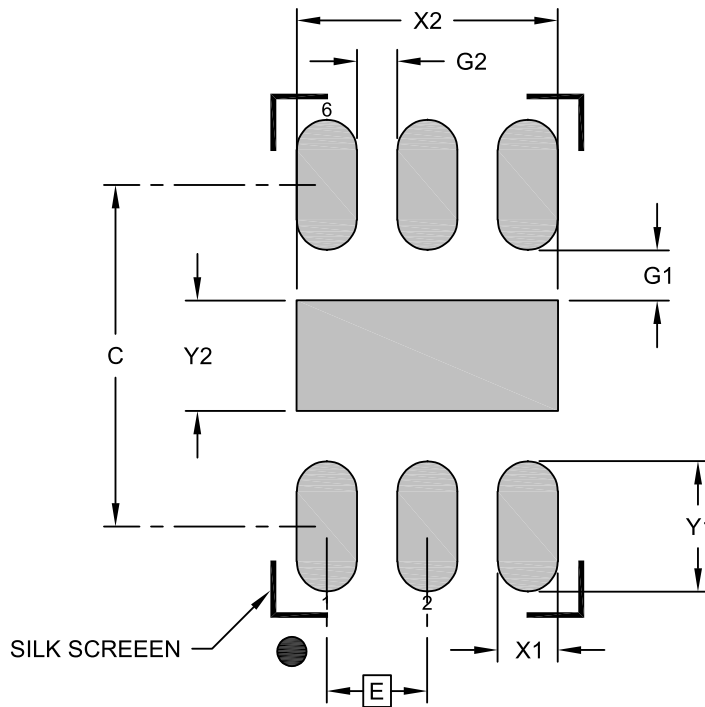
- Pin 1 visual index feature may vary, but must be located within the hatched area.
- Package is saw singulated
- Dimensioning and tolerancing per ASME Y14.5M
 BSC: Basic Dimension. Theoretically exact value shown without tolerances.
 REF: Reference Dimension, usually without tolerance, for information purposes only.

Microchip Technology Drawing C04-1154 Rev A Sheet 2 of 2

MIC826

6-Lead Ultra Thin Plastic Dual Flat, No Lead (HKA) - 1.6x1.6x0.6 mm Body [UDFN] With 1.26x0.50 mm Exposed Pad; Micrel Legacy Package TDFN1616-6LD-PL-1

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



RECOMMENDED LAND PATTERN

Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Contact Pitch	E	0.50 BSC		
Center Pad Width	X2			1.30
Center Pad Length	Y2			0.55
Contact Pad Spacing	C		1.70	
Contact Pad Width (X6)	X1			0.30
Contact Pad Length (X6)	Y1			0.65
Contact Pad to Center Pad (X6)	G1	0.25		
Contact Pad to Contact Pad (X4)	G2	0.20		

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M
BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing C04-3154 Rev A

APPENDIX A: REVISION HISTORY

Revision A (April 2022)

- Converted Micrel document MIC826 to Microchip data sheet DS20006668A.
- Minor text changes throughout.

MIC826

NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

<u>Part Number</u>	<u>X</u>	<u>X</u>	<u>XX</u>	<u>-XX</u>	Examples:
Device	Nominal Threshold Voltage	Temp. Range	Package	Media Type	
Device:	MIC826:	Voltage Supervisor with Watchdog Timer, Manual Reset, and Dual Outputs			
	L = 4.625				
	M = 4.375				
	T = 3.075				
Nominal Threshold Voltage:	S = 2.925				
	R = 2.625				
	Z = 2.315				
	Y = 2.188				
	W = 1.665				
Temperature Range:	Y =	-40°C to +125°C			
Package:	MT =	6-Lead 1.6 mm x 1.6 mm UDFN			
Media Type:	TR =	5,000/Reel			
	T5 =	500/Reel			
					a) MIC826LYMT-TR: MIC826, 4.625 Nom. Threshold Voltage, -40°C to +125°C Temp. Range, 6-Lead UDFN, 5,000/Reel
					b) MIC826MYMT-T5: MIC826, 4.375 Nom. Threshold Voltage, -40°C to +125°C Temp. Range, 6-Lead UDFN, 500/Reel
					c) MIC826TYMT-TR: MIC826, 3.075 Nom. Threshold Voltage, -40°C to +125°C Temp. Range, 6-Lead UDFN, 5,000/Reel
					d) MIC826SYMT-T5: MIC826, 2.925 Nom. Threshold Voltage, -40°C to +125°C Temp. Range, 6-Lead UDFN, 500/Reel
					e) MIC826RYMT-TR: MIC826, 2.625 Nom. Threshold Voltage, -40°C to +125°C Temp. Range, 6-Lead UDFN, 5,000/Reel
					f) MIC826ZYMT-T5: MIC826, 2.315 Nom. Threshold Voltage, -40°C to +125°C Temp. Range, 6-Lead UDFN, 500/Reel
					g) MIC826YYMT-TR: MIC826, 2.188 Nom. Threshold Voltage, -40°C to +125°C Temp. Range, 6-Lead UDFN, 5,000/Reel
					h) MIC826WYMT-T5: MIC826, 1.665 Nom. Threshold Voltage, -40°C to +125°C Temp. Range, 6-Lead UDFN, 500/Reel
					Note 1: Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option.

MIC826

NOTES:

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