



# SGM84782

## 4Ω, High Speed, Low Voltage Analog Switch/Multiplexer

### GENERAL DESCRIPTION

The SGM84782 is a TTL/CMOS compatible analog multiplexer which consists of two 4-channel multiplexers. It operates from a 1.8V to 4.2V single power supply that can handle rail-to-rail analog signals.

The SGM84782 features low voltage, low on-resistance and fast switching times. The high performances make it very suitable for multiple applications, such as cellular phones, audio and video signal routing, etc.

The SGM84782 can and is available in Green TQFN-3×3-16L and TSSOP-16 packages. It operates over an ambient temperature range of -40°C to +85°C.

### FEATURES

- **Single Supply Voltage Range: 1.8V to 4.2V**
- **-3dB Bandwidth: 150MHz**
- **Low On-Resistance: 4Ω (TYP) at 4.2V**
- **Low On-Resistance Flatness**
- **Low Crosstalk: -92dB ( $R_L = 50\Omega$ ,  $f = 10\text{MHz}$ )**
- **Fast Switching Times at  $V_+ = 4.2\text{V}$ :**
  - $t_{\text{ON}}$ : 17ns
  - $t_{\text{OFF}}$ : 9ns
- **High Off-Isolation: -40dB ( $R_L = 50\Omega$ ,  $f = 10\text{MHz}$ )**
- **Rail-to-Rail Input and Output Operation**
- **Low Power Consumption**
- **Break-Before-Make Switching**
- **TTL/CMOS Compatible**
- **-40°C to +85°C Operating Temperature Range**
- **Available in Green TQFN-3×3-16L and TSSOP-16 Packages**

### APPLICATIONS

Cellular Phones  
Portable Equipment  
Computer Peripherals  
Sample-and-Hold Circuits  
Data Acquisition Systems  
Battery-Powered Systems  
Audio and Video Signal Routing

**PACKAGE/ORDERING INFORMATION**

| MODEL    | PACKAGE DESCRIPTION | SPECIFIED TEMPERATURE RANGE | ORDERING NUMBER  | PACKAGE MARKING          | PACKING OPTION      |
|----------|---------------------|-----------------------------|------------------|--------------------------|---------------------|
| SGM84782 | TQFN-3x3-16L        | -40°C to +85°C              | SGM84782YTQ16/TR | 84782<br>XXXXX           | Tape and Reel, 3000 |
|          | TSSOP-16            | -40°C to +85°C              | SGM84782YTS/TR   | SGM84782<br>YTS<br>XXXXX | Tape and Reel, 3000 |

NOTE: XXXXX = Date Code and Vendor Code.

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

**ABSOLUTE MAXIMUM RATINGS**

V<sub>+</sub> to GND .....0V to 4.6V  
 Analog, Digital Voltage Range ..... -0.3V to (V<sub>+</sub>) + 0.3V  
 Continuous Current NO, NC, or COM.....±100mA  
 Junction Temperature .....+150°C  
 Storage Temperature Range .....-65°C to +150°C  
 Lead Temperature (Soldering, 10s) .....+260°C  
 ESD Susceptibility  
 HBM.....4000V  
 MM.....400V

**RECOMMENDED OPERATING CONDITIONS**

Supply Voltage Range .....1.8V to 4.2V  
 Operating Temperature Range .....-40°C to +85°C

**OVERSTRESS CAUTION**

Stresses beyond those listed may cause permanent damage to the device. Functional operation of the device at these or any other conditions beyond those indicated in the operational section of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

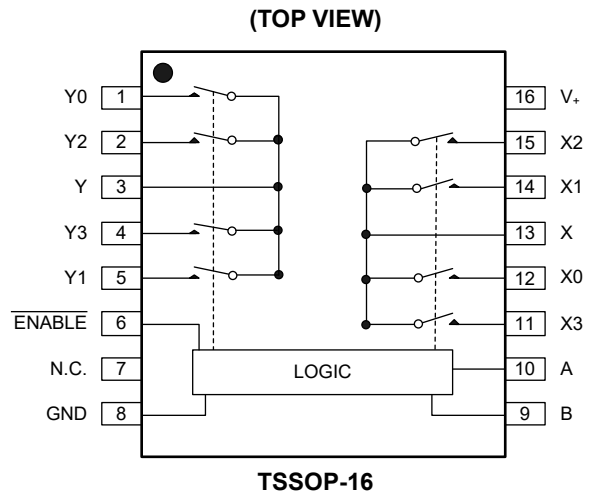
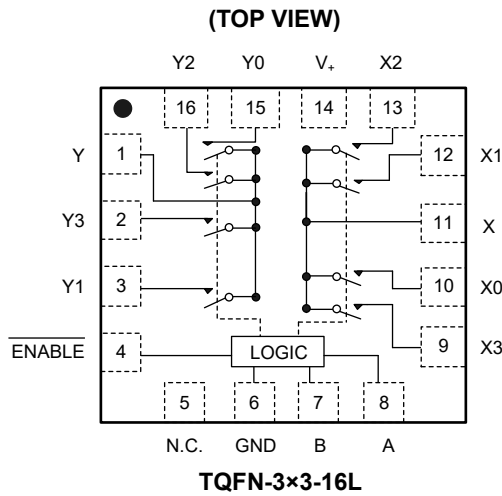
**ESD SENSITIVITY CAUTION**

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

**DISCLAIMER**

SG Micro Corp reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time.

PIN CONFIGURATIONS



PIN DESCRIPTION

| PIN            |               | NAME   | FUNCTION  |
|----------------|---------------|--------|---|
| TSSOP-16       | TQFN-3x3-16L  |        |   |
| 1, 5, 2, 4     | 15, 3, 16, 2  | Y0-Y3  | Analog Switch Y Inputs Y0-Y3.   |
| 3              | 1             | Y      | Analog Switch Y Output Pin.   |
| 6              | 4             | ENABLE | Digital Enable Control Pin. Normally connect to GND. Drive to logic high to set all switches off. |
| 7              | 5             | N.C.   | Not Internally Connected.   |
| 8              | 6             | GND    | Ground.   |
| 9              | 7             | B      | Digital Address B Input Pin.  |
| 10             | 8             | A      | Digital Address A Input Pin.  |
| 12, 14, 15, 11 | 10, 12, 13, 9 | X0-X3  | Analog Switch X Inputs X0-X3.   |
| 13             | 11            | X      | Analog Switch X Output Pin.   |
| 16             | 14            | V+     | Positive Power Supply Pin.  |
| -              | Exposed Pad   | GND    | Exposed Pad. Connect exposed pad to GND.  |

FUNCTION TABLE

| CONTROL INPUTS |        |   | ON SWITCHES       |
|----------------|--------|---|-------------------|
| ENABLE         | SELECT |   |                   |
|                | B      | A |                   |
| L              | L      | L | X-X0 Y-Y0         |
| L              | L      | H | X-X1 Y-Y1         |
| L              | H      | L | X-X2 Y-Y2         |
| L              | H      | H | X-X3 Y-Y3         |
| H              | √      | √ | All Switches Open |

√ = Don't Care.

**ELECTRICAL CHARACTERISTICS**

(V<sub>+</sub> = 4.2V, GND = 0V, V<sub>IH</sub> = 1.6V, V<sub>IL</sub> = 0.6V, Full = -40°C to +85°C. Typical values are at V<sub>+</sub> = 4.2V, T<sub>A</sub> = +25°C, unless otherwise noted.)

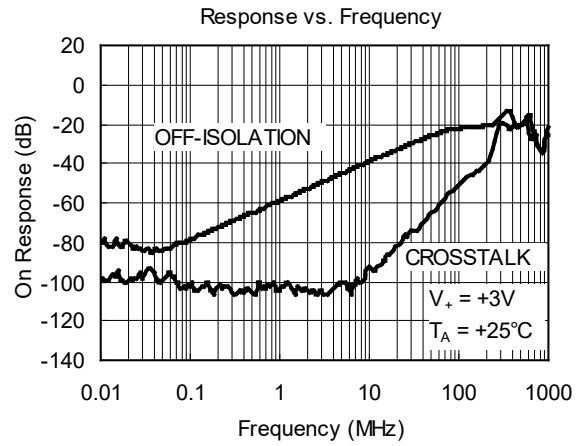
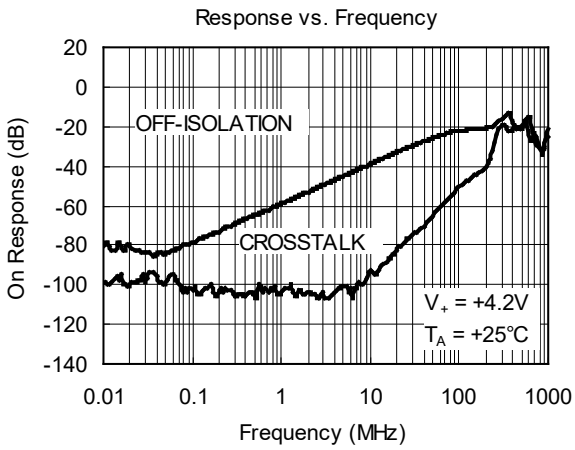
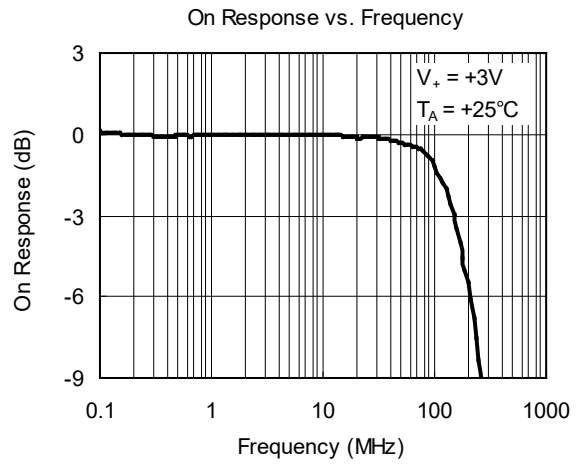
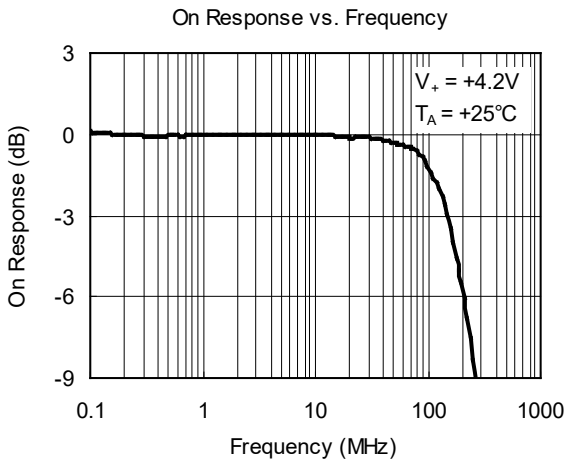
| PARAMETER                            | SYMBOL  | CONDITIONS   | TEMP  | MIN   | TYP  | MAX            | UNITS |
|--------------------------------------|---|--|-------|-------|------|----------------|-------|
| <b>ANALOG SWITCH</b>                 |   |  |       |       |      |                |       |
| Analog Signal Range                  | $V_{X-}, V_{Y-}, V_X, V_Y$  |  | Full  | 0     |      | V <sub>+</sub> | V     |
| On-Resistance                        | R <sub>ON</sub>   | V <sub>+</sub> = 4.2V, I <sub>X-</sub> , I <sub>Y-</sub> = -100mA, V <sub>X</sub> , V <sub>Y</sub> = 1V, Test Circuit 1              | +25°C |       | 4    | 5.5            | Ω     |
|                                      |   |  | Full  |       |      | 6.5            |       |
| On-Resistance Match Between Channels | ΔR <sub>ON</sub>  | V <sub>+</sub> = 4.2V, I <sub>X-</sub> , I <sub>Y-</sub> = -100mA, V <sub>X</sub> , V <sub>Y</sub> = 1V, Test Circuit 1              | +25°C |       | 0.16 | 1              | Ω     |
|                                      |   |  | Full  |       |      | 1.5            |       |
| On-Resistance Flatness               | R <sub>FLAT(ON)</sub>   | V <sub>+</sub> = 4.2V, I <sub>X-</sub> , I <sub>Y-</sub> = -100mA, V <sub>X</sub> , V <sub>Y</sub> = 1V, 2.5V, Test Circuit 1        | +25°C |       | 1.6  | 2.2            | Ω     |
|                                      |   |  | Full  |       |      | 2.8            |       |
| Source Off Leakage Current           | I <sub>X(OFF)</sub> , I <sub>Y(OFF)</sub>   | V <sub>+</sub> = 4.2V, V <sub>X-</sub> , V <sub>Y-</sub> = 3.3V, 0.3V, V <sub>X</sub> , V <sub>Y</sub> = 0.3V, 3.3V                  | Full  |       |      | 1              | μA    |
| Channel On Leakage Current           | I <sub>X(ON)</sub> , I <sub>Y(ON)</sub> , I <sub>X(OFF)</sub> , I <sub>Y(OFF)</sub> | V <sub>+</sub> = 4.2V, V <sub>X</sub> , V <sub>Y</sub> = 0.3V, 3.3V, V <sub>X-</sub> , V <sub>Y-</sub> = 0.3V, 3.3V or floating      | Full  |       |      | 1              | μA    |
| <b>DIGITAL INPUTS</b>                |   |  |       |       |      |                |       |
| Input High Voltage                   | V <sub>INH</sub>  |  | Full  | 1.6   |      |                | V     |
| Input Low Voltage                    | V <sub>INL</sub>  |  | Full  |       |      | 0.5            | V     |
| Input Leakage Current                | I <sub>IN-</sub>  | V <sub>A</sub> , V <sub>B</sub> = V <sub>ENABLE</sub> = 0V or 4.2V   | Full  |       |      | 1              | μA    |
| <b>DYNAMIC CHARACTERISTICS</b>       |   |  |       |       |      |                |       |
| Turn-On Time                         | t <sub>ON</sub>   | V <sub>IN</sub> = 1.5V to 0.5V, V <sub>X</sub> or V <sub>Y</sub> = 2.1V, R <sub>L</sub> = 50Ω, C <sub>L</sub> = 35pF, Test Circuit 2 | +25°C |       | 17.0 |                | ns    |
| Turn-Off Time                        | t <sub>OFF</sub>  |  | +25°C |       | 9.0  |                |       |
| Address Transition Time              | t <sub>TRANS</sub>  | V <sub>IN</sub> = 4.2V to 0V, V <sub>X</sub> or V <sub>Y</sub> = 2.1V, R <sub>L</sub> = 50Ω, C <sub>L</sub> = 35pF, Test Circuit 3   | +25°C |       | 17.2 |                | ns    |
| Break-Before-Make Time Delay         | t <sub>D</sub>  | V <sub>IN</sub> = 4.2V to 0V, V <sub>X</sub> or V <sub>Y</sub> = 2.1V, R <sub>L</sub> = 50Ω, C <sub>L</sub> = 35pF, Test Circuit 4   | +25°C |       | 14.0 |                | ns    |
| Charge Injection                     | Q   | C <sub>L</sub> = 1nF, Test Circuit 5   | +25°C |       | -18  |                | pC    |
| Off Isolation                        | O <sub>ISO</sub>  | Signal = 0dBm, V <sub>BIAS</sub> = 2.1V, R <sub>L</sub> = 50Ω, C <sub>L</sub> = 35pF, Test Circuit 6                                 | 1MHz  | +25°C |      | -60            | dB    |
|                                      |   |  | 10MHz | +25°C |      | -40            |       |
| Channel-to-Channel Crosstalk         | X <sub>TALK</sub>   | Signal = 0dBm, V <sub>BIAS</sub> = 2.1V, R <sub>L</sub> = 50Ω, C <sub>L</sub> = 35pF, Test Circuit 6                                 | 1MHz  | +25°C |      | -105           | dB    |
|                                      |   |  | 10MHz | +25°C |      | -92            |       |
| -3dB Bandwidth                       | BW  | Signal = 0dBm, V <sub>BIAS</sub> = 2.1V, R <sub>L</sub> = 50Ω, Test Circuit 6  | +25°C |       | 150  |                | MHz   |
| Channel On Capacitance               | C <sub>X(ON)</sub> , C <sub>Y(ON)</sub> , C <sub>X(OFF)</sub> , C <sub>Y(OFF)</sub> |  | +25°C |       | 51.6 |                | pF    |
| <b>POWER REQUIREMENTS</b>            |   |  |       |       |      |                |       |
| Power Supply Range                   | V <sub>+</sub>  |  | Full  | 1.8   |      | 4.2            | V     |
| Power Supply Current                 | I <sub>+</sub>  | V <sub>+</sub> = 4.2V, V <sub>A</sub> , V <sub>B</sub> , V <sub>ENABLE</sub> = 4.2V or 0V  | Full  |       |      | 1              | μA    |

**ELECTRICAL CHARACTERISTICS (continued)**

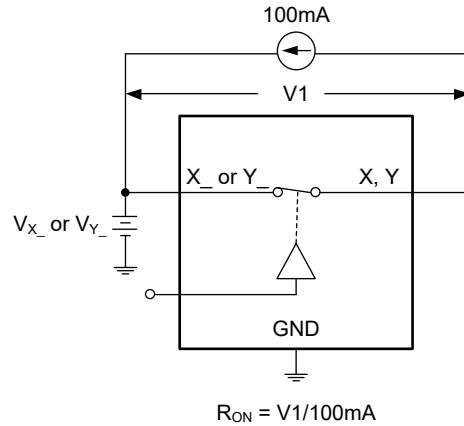
(V<sub>+</sub> = 2.7V to 3.6V, GND = 0V, V<sub>IH</sub> = 1.6V, V<sub>IL</sub> = 0.4V, Full = -40°C to +85°C. Typical values are at V<sub>+</sub> = 3V, T<sub>A</sub> = +25°C, unless otherwise noted.)

| PARAMETER                            | SYMBOL  | CONDITIONS   | TEMP  | MIN   | TYP  | MAX            | UNITS |
|--------------------------------------|---|--|-------|-------|------|----------------|-------|
| <b>ANALOG SWITCH</b>                 |   |  |       |       |      |                |       |
| Analog Signal Range                  | $V_{X-}, V_{Y-}, V_X, V_Y$  |  | Full  | 0     |      | V <sub>+</sub> | V     |
| On-Resistance                        | R <sub>ON</sub>   | V <sub>+</sub> = 2.7V, I <sub>X-</sub> , I <sub>Y-</sub> = -100mA, V <sub>X</sub> , V <sub>Y</sub> = 1V, Test Circuit 1              | +25°C |       | 5    | 6              | Ω     |
|                                      |   |  | Full  |       |      | 7              |       |
| On-Resistance Match Between Channels | ΔR <sub>ON</sub>  | V <sub>+</sub> = 2.7V, I <sub>X-</sub> , I <sub>Y-</sub> = -100mA, V <sub>X</sub> , V <sub>Y</sub> = 1V, Test Circuit 1              | +25°C |       | 0.03 | 1              | Ω     |
|                                      |   |  | Full  |       |      | 1.5            |       |
| On-Resistance Flatness               | R <sub>FLAT(ON)</sub>   | V <sub>+</sub> = 2.7V, I <sub>X-</sub> , I <sub>Y-</sub> = -100mA, V <sub>X</sub> , V <sub>Y</sub> = 1V, 2.5V, Test Circuit 1        | +25°C |       | 1.8  | 2.2            | Ω     |
|                                      |   |  | Full  |       |      | 2.8            |       |
| Source Off Leakage Current           | I <sub>X(OFF)</sub> , I <sub>Y(OFF)</sub>   | V <sub>+</sub> = 3.6V, V <sub>X-</sub> , V <sub>Y-</sub> = 3.3V, 0.3V, V <sub>X</sub> , V <sub>Y</sub> = 0.3V, 3.3V                  | Full  |       |      | 1              | μA    |
| Channel On Leakage Current           | I <sub>X(ON)</sub> , I <sub>Y(ON)</sub> , I <sub>X(ON)</sub> , I <sub>Y(ON)</sub> | V <sub>+</sub> = 3.6V, V <sub>X</sub> , V <sub>Y</sub> = 0.3V, 3.3V, V <sub>X-</sub> , V <sub>Y-</sub> = 0.3V, 3.3V or floating      | Full  |       |      | 1              | μA    |
| <b>DIGITAL INPUTS</b>                |   |  |       |       |      |                |       |
| Input High Voltage                   | V <sub>INH</sub>  |  | Full  | 1.5   |      |                | V     |
| Input Low Voltage                    | V <sub>INL</sub>  |  | Full  |       |      | 0.4            | V     |
| Input Leakage Current                | I <sub>IN-</sub>  | V <sub>A</sub> , V <sub>B</sub> = V <sub>ENABLE</sub> = 0V or 2.7V   | Full  |       |      | 1              | μA    |
| <b>DYNAMIC CHARACTERISTICS</b>       |   |  |       |       |      |                |       |
| Turn-On Time                         | t <sub>ON</sub>   | V <sub>IN</sub> = 1.5V to 0.5V, V <sub>X</sub> or V <sub>Y</sub> = 1.5V, R <sub>L</sub> = 50Ω, C <sub>L</sub> = 35pF, Test Circuit 2 | +25°C |       | 33.0 |                | ns    |
| Turn-Off Time                        | t <sub>OFF</sub>  |  | +25°C |       | 9.0  |                |       |
| Address Transition Time              | t <sub>TRANS</sub>  | V <sub>IN</sub> = 1.5V to 0V, V <sub>X</sub> or V <sub>Y</sub> = 1.5V, R <sub>L</sub> = 50Ω, C <sub>L</sub> = 35pF, Test Circuit 3   | +25°C |       | 17.5 |                | ns    |
| Break-Before-Make Time Delay         | t <sub>D</sub>  | V <sub>IN</sub> = 1.5V to 0V, V <sub>X</sub> or V <sub>Y</sub> = 1.5V, R <sub>L</sub> = 50Ω, C <sub>L</sub> = 35pF, Test Circuit 4   | +25°C |       | 14.5 |                | ns    |
| Charge Injection                     | Q   | C <sub>L</sub> = 1nF, Test Circuit 5   | +25°C |       | -18  |                | pC    |
| Off Isolation                        | O <sub>ISO</sub>  | Signal = 0dBm, V <sub>BIAS</sub> = 1.5V, R <sub>L</sub> = 50Ω, C <sub>L</sub> = 35pF, Test Circuit 6                                 | 1MHz  | +25°C |      | -60            | dB    |
|                                      |   |  | 10MHz | +25°C |      | -40            |       |
| Channel-to-Channel Crosstalk         | X <sub>TALK</sub>   | Signal = 0dBm, V <sub>BIAS</sub> = 1.5V, R <sub>L</sub> = 50Ω, C <sub>L</sub> = 35pF, Test Circuit 6                                 | 1MHz  | +25°C |      | -105           | dB    |
|                                      |   |  | 10MHz | +25°C |      | -92            |       |
| -3dB Bandwidth                       | BW  | Signal = 0dBm, V <sub>BIAS</sub> = 1.5V, R <sub>L</sub> = 50Ω, Test Circuit 6  | +25°C |       | 150  |                | MHz   |
| Channel On Capacitance               | C <sub>X(ON)</sub> , C <sub>Y(ON)</sub> , C <sub>X(ON)</sub> , C <sub>Y(ON)</sub> |  | +25°C |       | 51.6 |                | pF    |

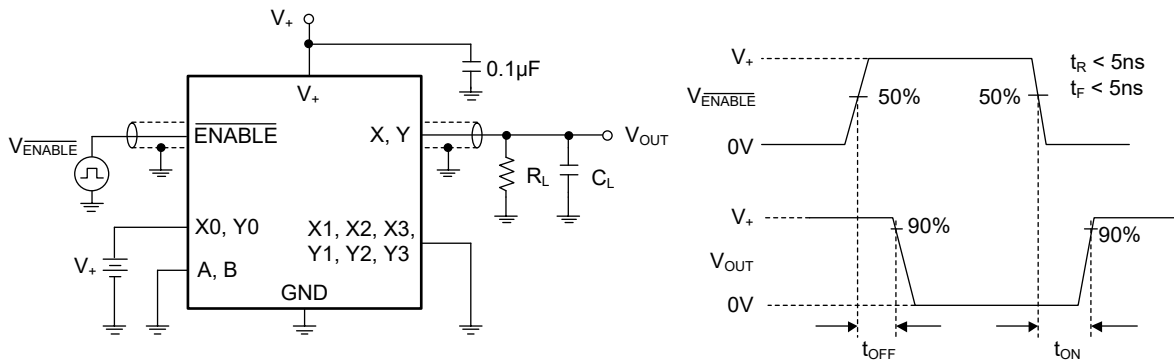
TYPICAL PERFORMANCE CHARACTERISTICS



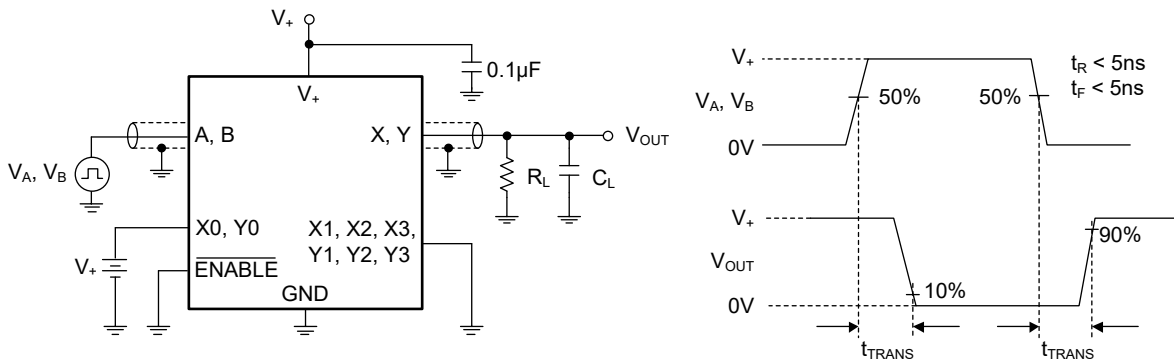
TEST CIRCUITS



Test Circuit 1. On-Resistance

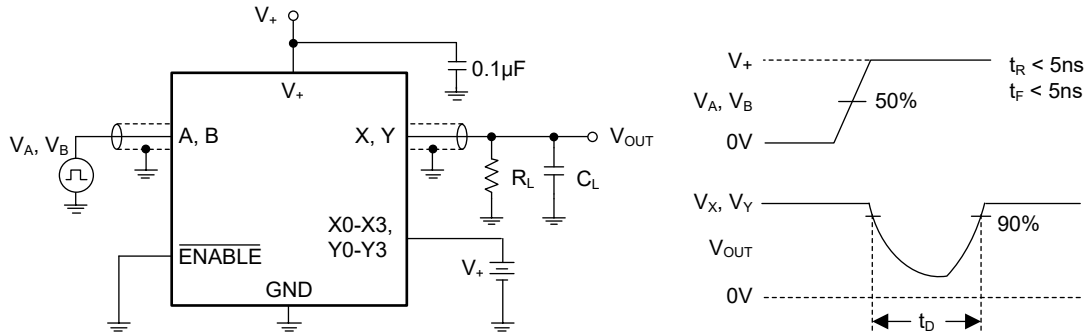


Test Circuit 2. Enable Switching Times ( $t_{ON}$ ,  $t_{OFF}$ )

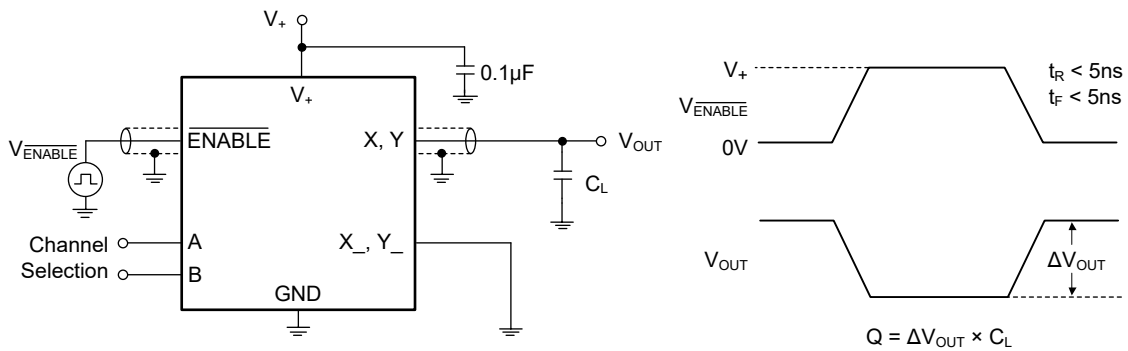


Test Circuit 3. Address Transition Times ( $t_{TRANS}$ )

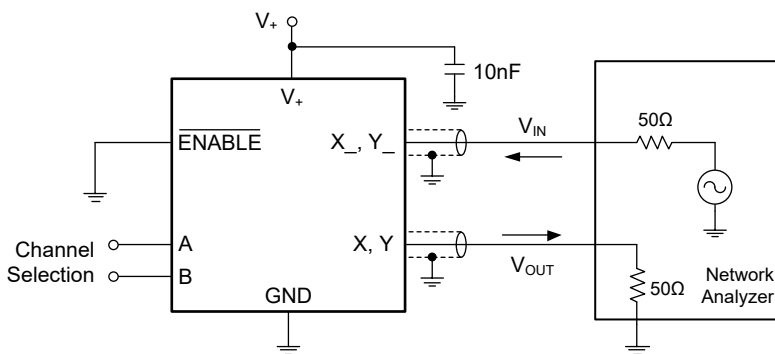
TEST CIRCUITS (continued)



Test Circuit 4. Break-Before-Make Interval ( $t_d$ )



Test Circuit 5. Charge Injection ( $Q$ )



Off Isolation =  $20\log(V_{OUT}/V_{IN})$  (Measured between X and "OFF"  $X_-$  Terminal on Each Switch)

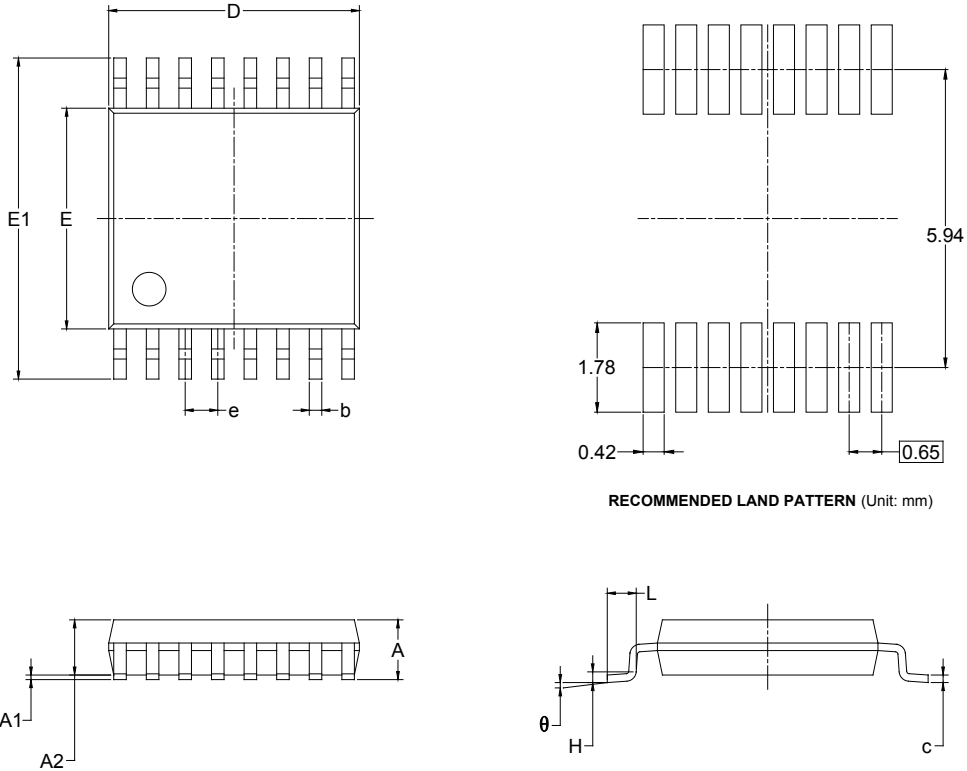
Channel-to-Channel Crosstalk =  $20\log(V_{OUT}/V_{IN})$  (Measured from One Channel (X, Y) to All other Channels)

Test Circuit 6. -3dB Bandwidth, Off Isolation and Crosstalk



PACKAGE OUTLINE DIMENSIONS

TSSOP-16



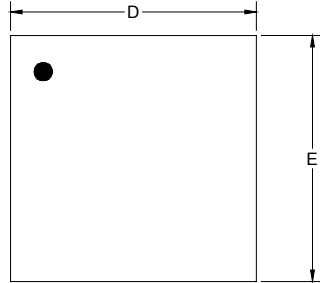
RECOMMENDED LAND PATTERN (Unit: mm)

| Symbol   | Dimensions<br>In Millimeters |       | Dimensions<br>In Inches |       |
|----------|------------------------------|-------|-------------------------|-------|
|          | MIN                          | MAX   | MIN                     | MAX   |
| A        |                              | 1.200 |                         | 0.047 |
| A1       | 0.050                        | 0.150 | 0.002                   | 0.006 |
| A2       | 0.800                        | 1.050 | 0.031                   | 0.041 |
| b        | 0.190                        | 0.300 | 0.007                   | 0.012 |
| c        | 0.090                        | 0.200 | 0.004                   | 0.008 |
| D        | 4.860                        | 5.100 | 0.191                   | 0.201 |
| E        | 4.300                        | 4.500 | 0.169                   | 0.177 |
| E1       | 6.200                        | 6.600 | 0.244                   | 0.260 |
| e        | 0.650 BSC                    |       | 0.026 BSC               |       |
| L        | 0.500                        | 0.700 | 0.02                    | 0.028 |
| H        | 0.25 TYP                     |       | 0.01 TYP                |       |
| $\theta$ | 1°                           | 7°    | 1°                      | 7°    |

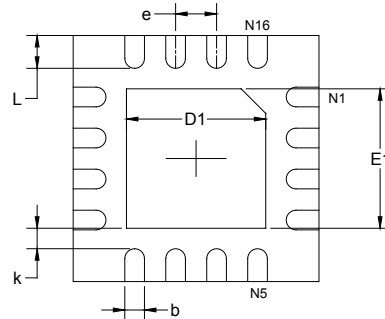
# PACKAGE INFORMATION

## PACKAGE OUTLINE DIMENSIONS

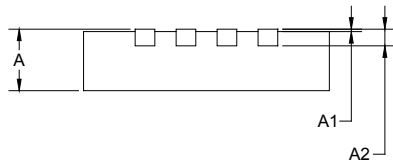
### TQFN-3×3-16L



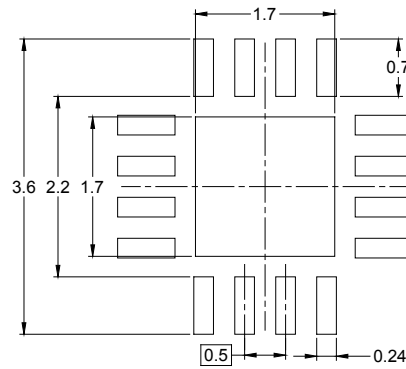
TOP VIEW



BOTTOM VIEW



SIDE VIEW



RECOMMENDED LAND PATTERN (Unit: mm)

| Symbol | Dimensions<br>In Millimeters |       | Dimensions<br>In Inches |       |
|--------|------------------------------|-------|-------------------------|-------|
|        | MIN                          | MAX   | MIN                     | MAX   |
| A      | 0.700                        | 0.800 | 0.028                   | 0.031 |
| A1     | 0.000                        | 0.050 | 0.000                   | 0.002 |
| A2     | 0.203 REF                    |       | 0.008 REF               |       |
| D      | 2.900                        | 3.100 | 0.114                   | 0.122 |
| D1     | 1.600                        | 1.800 | 0.063                   | 0.071 |
| E      | 2.900                        | 3.100 | 0.114                   | 0.122 |
| E1     | 1.600                        | 1.800 | 0.063                   | 0.071 |
| k      | 0.200 MIN                    |       | 0.008 MIN               |       |
| b      | 0.180                        | 0.300 | 0.007                   | 0.012 |
| e      | 0.500 TYP                    |       | 0.020 TYP               |       |
| L      | 0.300                        | 0.500 | 0.012                   | 0.020 |

## TAPE AND REEL INFORMATION

### REEL DIMENSIONS



### TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

### KEY PARAMETER LIST OF TAPE AND REEL

| Package Type | Reel Diameter | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P0 (mm) | P1 (mm) | P2 (mm) | W (mm) | Pin1 Quadrant |
|--------------|---------------|--------------------|---------|---------|---------|---------|---------|---------|--------|---------------|
| TQFN-3×3-16L | 13"           | 12.4               | 3.35    | 3.35    | 1.13    | 4.0     | 8.0     | 2.0     | 12.0   | Q1            |
| TSSOP-16     | 13"           | 12.4               | 6.90    | 5.60    | 1.20    | 4.0     | 8.0     | 2.0     | 12.0   | Q1            |

DD0001

# PACKAGE INFORMATION

## CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

## KEY PARAMETER LIST OF CARTON BOX

| Reel Type | Length (mm) | Width (mm) | Height (mm) | Pizza/Carton |
|-----------|-------------|------------|-------------|--------------|
| 13"       | 386         | 280        | 370         | 5            |

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