

NCE N-Channel Super Trench II Power MOSFET

Description

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

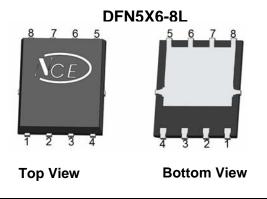
Application

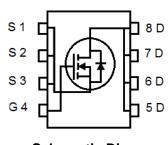
- DC/DC Converter
- •Ideal for high-frequency switching and synchronous rectification

General Features

- V_{DS} =85V, I_D =90A $R_{DS(ON)}$ =4.3m Ω , typical (TO-220)@ V_{GS} =10V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% ΔVds TESTED!





Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P053N85GU	P053N85GU	DFN5X6-8L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	85	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	90	А
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	70	Α
Pulsed Drain Current	I _{DM}	360	Α
Maximum Power Dissipation	P _D	110	W
Derating factor		0.88	W/°C
Single pulse avalanche energy (Note 5)	E _{AS}	599	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{ heta JC}$	1.14	°C/W	
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Electrical Characteristics (T_C=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	<u>, </u>		•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	85		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =85V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20 V , V_{DS} =0 V	-	-	±100	nA
On Characteristics (Note 3)	<u>, </u>		•		•	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =45A	-	4.3	5.3	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =45A		60	-	S
Dynamic Characteristics (Note4)	,		•	l.	·	
Input Capacitance	C _{Iss}	V _{DS} =40V,V _{GS} =0V,	-	3550	-	PF
Output Capacitance	C _{oss}		-	540	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	22	-	PF
Switching Characteristics (Note 4)	<u>, </u>		•		•	
Turn-on Delay Time	t _{d(on)}		-	14.5	-	nS
Turn-on Rise Time	t _r	V_{DD} =40V, I_D =45A V_{GS} =10V, R_G =1.6 Ω	-	12	-	nS
Turn-Off Delay Time	t _{d(off)}		-	35	-	nS
Turn-Off Fall Time	t _f		-	13	-	nS
Total Gate Charge	Qg	\/ 40\/ L 45A	-	67	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =40V, I_{D} =45A,	-	20		nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	20		nC
Drain-Source Diode Characteristics	,		•	l.		
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =45A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	90	Α
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C, I_F = I_S$	-	66	_	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	140	-	nC

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,VDD=40V,VG=10V,L=0.5mH,Rg=25 Ω



Typical Electrical and Thermal Characteristics

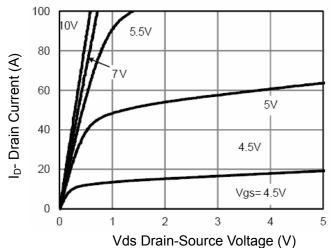
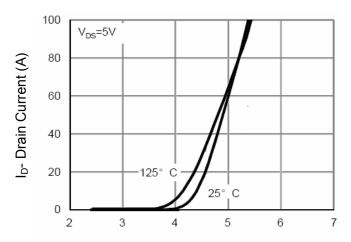
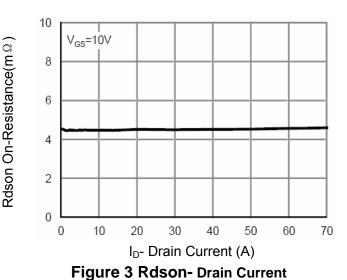


Figure 1 Output Characteristics



Vgs Gate-Source Voltage (V) **Figure 2 Transfer Characteristics**



2.2 2 Normalized On-Resistance V_{GS}=10V I_D=45A 1.8 1.6 1.4 1.2

Figure 4 Rdson-Junction Temperature

100

125

150

175

75

T_J-Junction Temperature(°C)

50

0.8

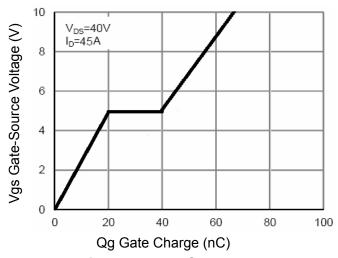


Figure 5 Gate Charge

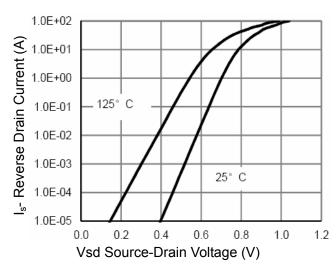


Figure 6 Source- Drain Diode Forward



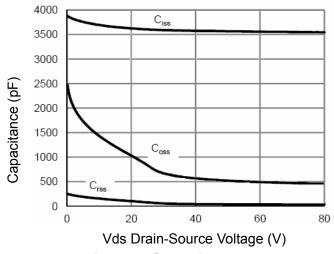


Figure 7 Capacitance vs Vds

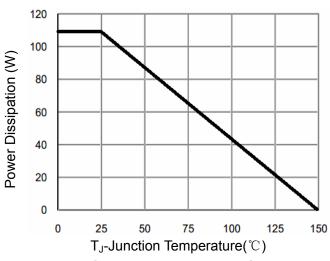


Figure 9 Power De-rating

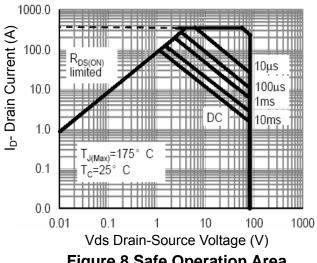


Figure 8 Safe Operation Area

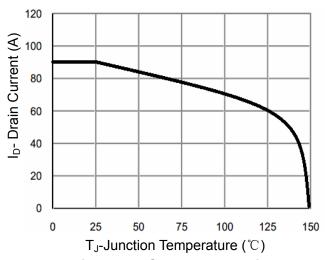


Figure 10 Current De-rating

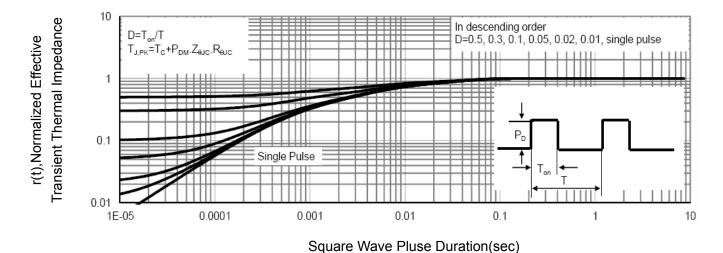
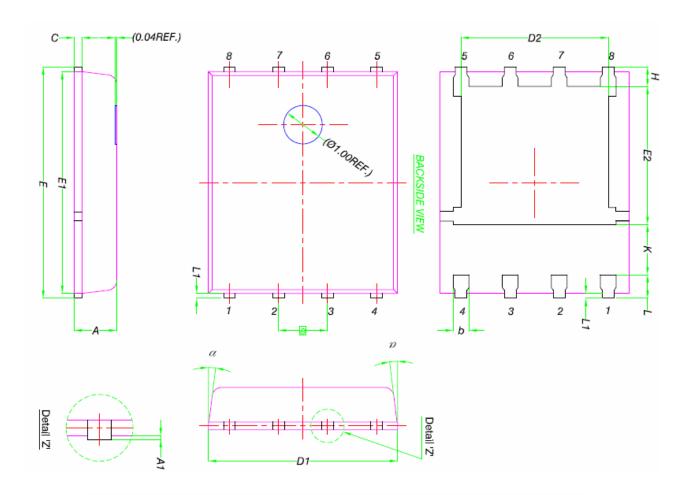


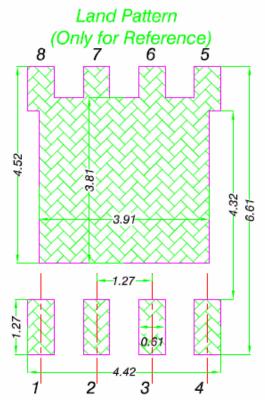
Figure 11 Normalized Maximum Transient Thermal Impedance



DFN5X6-8L Package Information



5/4	MILLIMETERS					
DIM.	MIN.	NOM.	MAX.			
Α	0.90	1.00	1.10			
A1	0 -		0.05			
b	0.33	0.33 0.41				
С	0.20	0.25	0.30			
D1	4.80 4.90		5.00			
D2	3.61	3.81	3.96			
Ε	5.90	6.00	6.10			
E1	5.70	5.75	5.80			
E2	3.38	3.58	3.78			
е						
Н	0.41	0.51	0.61			
K	1.10	-	-			
L	0.51	0.61	0.71 0.20 12°			
L1	0.06	0.13				
α	<i>0</i> °	-				





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