

# NCE N-Channel Super Trench Power MOSFET

#### **Description**

The NCEP85T30LL uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

#### **Application**

- DC/DC Converter
- •Ideal for high-frequency switching and synchronous rectification

#### **General Features**

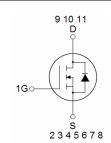
- $V_{DS}$  =85V, $I_D$  =330A  $R_{DS(ON)}$ =1.6m $\Omega$  , typical @  $V_{GS}$ =10V
- Excellent gate charge x R<sub>DS(on)</sub> product(FOM)
- Very low on-resistance R<sub>DS(on)</sub>
- 175 °C operating temperature
- Pb-free lead plating

100% UIS TESTED!

100% AVds TESTED!

#### **TOLL**





**Schematic Diagram** 

# **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP85T30LL	NCEP85T30LL	TOLL	-	-	-

## Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	85	V	
Gate-Source Voltage	V <sub>G</sub> s	±20	V	
Drain Current-Continuous	I <sub>D</sub>	330	Α	
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	231	А	
Pulsed Drain Current	I <sub>DM</sub>	1320	А	
Maximum Power Dissipation	P <sub>D</sub>	450	W	
Derating factor		3.0	W/℃	
Single pulse avalanche energy (Note 5)	Eas	2000	mJ	
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 175	°C	

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	0.33	°C/W
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Electrical Characteristics (T<sub>C</sub>=25 °C unless otherwise noted)

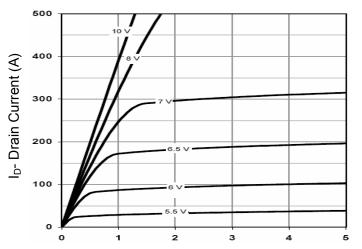
Parameter Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	85		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =85V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}$ = $V_{GS}$ , $I_D$ =250 $\mu$ A	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =165A	-	1.6	1.9	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =165A	-	200	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C <sub>lss</sub>	\/ -40\/\/ -0\/	-	10700	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =40V, $V_{GS}$ =0V, F=1.0MHz	-	1700	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.UIVIHZ	-	76	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	28	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =40 $V$ , $I_D$ =165 $A$	-	73	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =1.6 $\Omega$	-	86	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	33	-	nS
Total Gate Charge	Qg	\/ -40\/  -405A	-	142	-	nC
Gate-Source Charge	$Q_{gs}$	$V_{DS}$ =40V, $I_{D}$ =165A, $V_{GS}$ =10V	-	56		nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> -10V	-	24		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =165A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	330	Α
Reverse Recovery Time	t <sub>rr</sub>	$T_J = 25^{\circ}C$ , $I_F = 165A$	-	115	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	320	-	nC

#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25  $^{\circ}\text{C}$  ,VDD=40V,VG=10V,L=0.5mH,Rg=25 $\Omega$

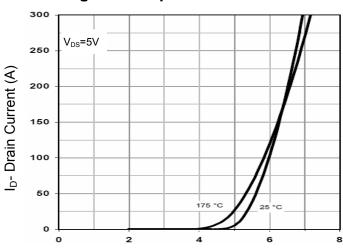


## **Typical Electrical and Thermal Characteristics**



Vds Drain-Source Voltage (V)





Vgs Gate-Source Voltage (V)

# **Figure 2 Transfer Characteristics**

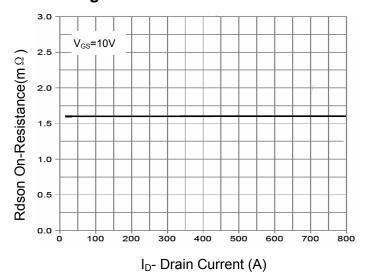
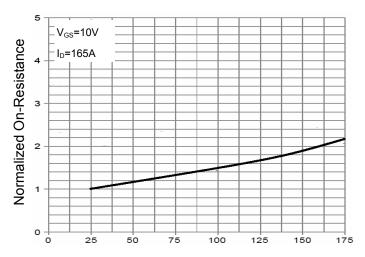
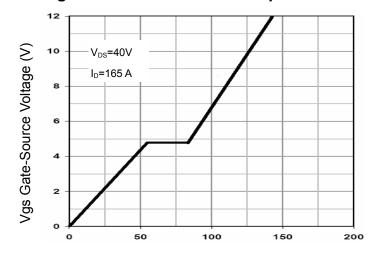


Figure 3 Rdson- Drain Current

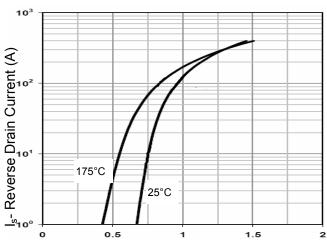


T<sub>J</sub>-Junction Temperature(°C)

## **Figure 4 Rdson-Junction Temperature**



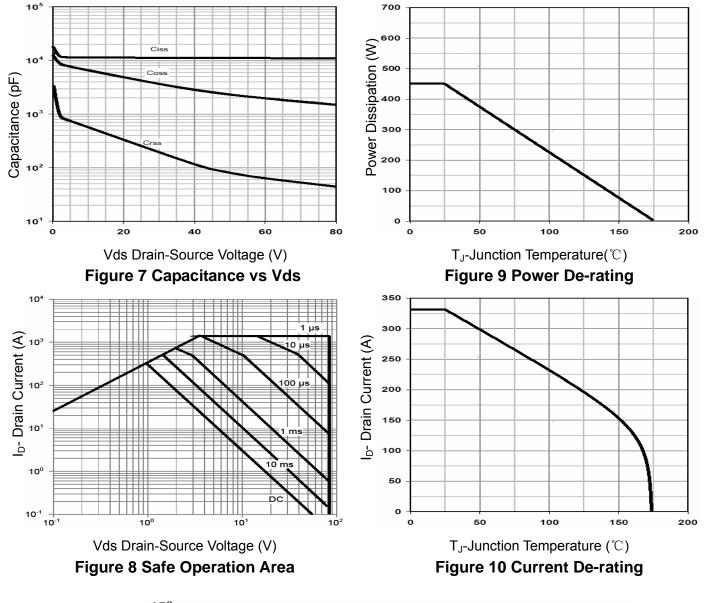
Qg Gate Charge (nC)
Figure 5 Gate Charge

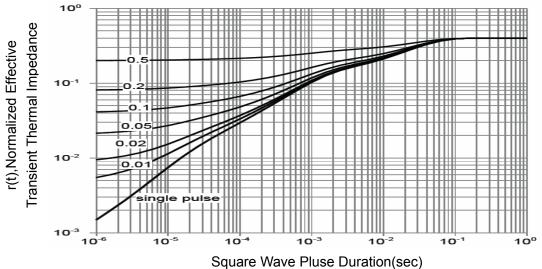


Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward





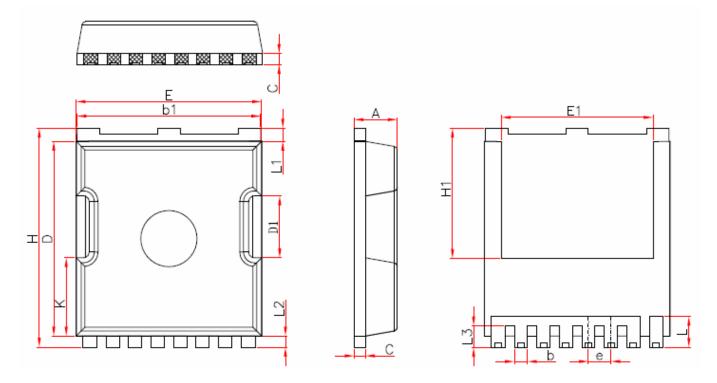


**Figure 11 Normalized Maximum Transient Thermal Impedance** 



# **TOLL Package Information**

Wuxi NCE Power Co., Ltd



Symbo1	Millimeters				
	Min.	Nom.	Max.		
A	2.20	2.30	2.40		
b	0.65	0.75	0.85		
b1	9.70	9.80	9.90		
С	0.50	0.60	0.70		
D	10.30	10.40	10.50		
D1	3.15	3.3	3.45		
Е	9.70	9.90	10.10		
E1	8.00	8.10	8. 20		
е	1.10	1.20	1.30		
Н	11.6	11.7	11.8		
H1	6.85	6.95	7.05		
K	4.08	4.18	4. 28		
L	1.60	1.65	2.10		
L1	0.60	0.70	0.80		
L2	0.50	0.60	0.70		
L3	1.05	1.20	1.30		



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