

## NCE N-Channel Super Trench II Power MOSFET

### Description

The NCEP18N10AG uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

### General Features

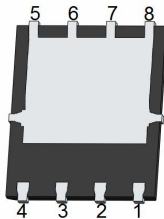
- $V_{DS} = 100V, I_D = 40A$   
 $R_{DS(ON)} = 15.5m\Omega$  (typical) @  $V_{GS} = 10V$   
 $R_{DS(ON)} = 19.5m\Omega$  (typical) @  $V_{GS} = 4.5V$
- Excellent gate charge x  $R_{DS(on)}$  product(FOM)
- Very low on-resistance  $R_{DS(on)}$
- 150 °C operating temperature
- Pb-free lead plating

**100% UIS TESTED!**  
**100% ΔVds TESTED!**

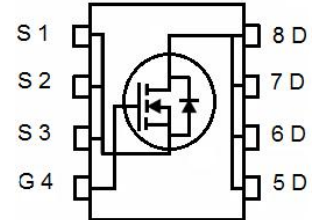
### DFN 5X6



Top View



Bottom View



Schematic Diagram

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P18N10AG	NCEP18N10AG	DFN5X6-8L	-	-	-

### Absolute Maximum Ratings ( $T_c = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	40	A
Drain Current-Continuous( $T_c = 100^\circ C$ )	$I_D(100^\circ C)$	29	A
Pulsed Drain Current (Note 1)	$I_{DM}$	160	A
Maximum Power Dissipation	$P_D$	60	W
Derating factor		0.48	W/°C
Single pulse avalanche energy (Note 5)	$E_{AS}$	115	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

### Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{\theta JC}$	2.08	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	55	°C/W

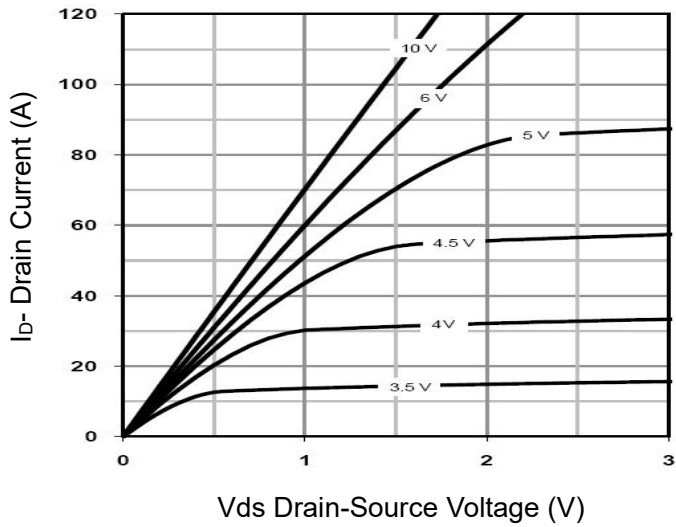
## Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.2	1.7	2.2	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	15.5	18	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A	-	19.5	23	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =20A	20	-	-	S
<b>Dynamic Characteristics</b> (Note4)						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, F=1.0MHz	-	1719.5	-	PF
Output Capacitance	C <sub>oss</sub>		-	147.4	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	16	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =20A V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω	-	14	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	16	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	28	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	8	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	-	37.6	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	6.5		nC
Gate-Drain Charge	Q <sub>gd</sub>		-	9.5		nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A	-		1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	40	A
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = 20A	-	43	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 100A/μs (Note3)	-	90	-	nC

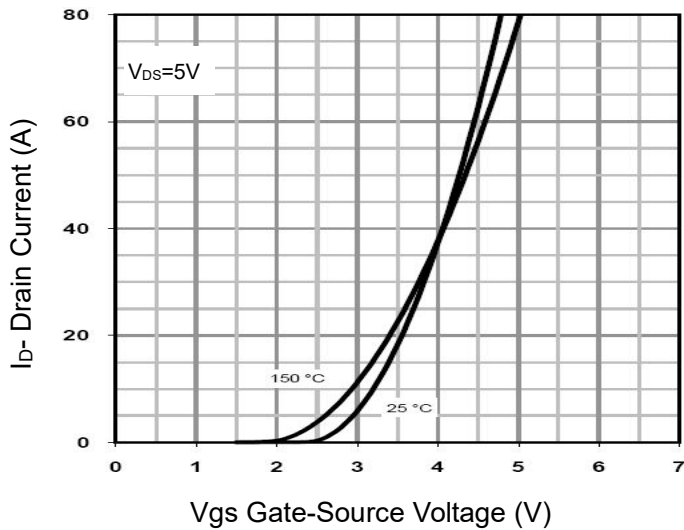
### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. The value of R<sub>θJA</sub> is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub> =25° C. The Power dissipation P<sub>DSM</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150° C. The value in any given application depends on the user's specific board design. The R<sub>θJA</sub> is the sum of the thermal impedance from junction to case R<sub>θJC</sub> and case to ambient.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition : T<sub>J</sub>=25°C, V<sub>DD</sub>=50V, V<sub>G</sub>=10V, L=0.5mH, R<sub>G</sub>=25Ω

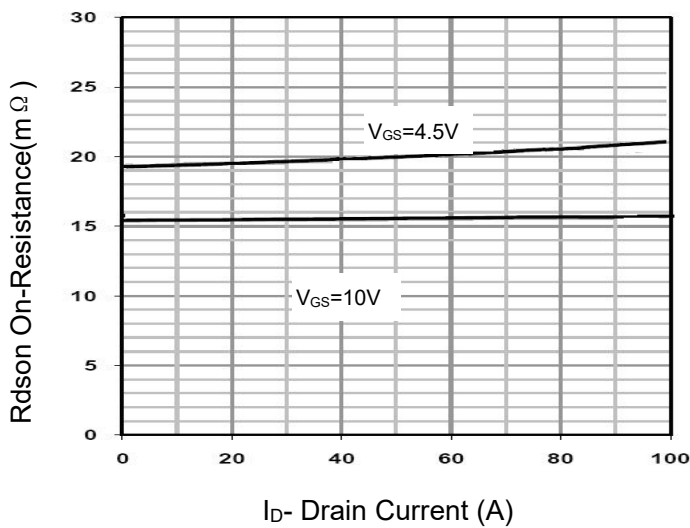
## Typical Electrical and Thermal Characteristics



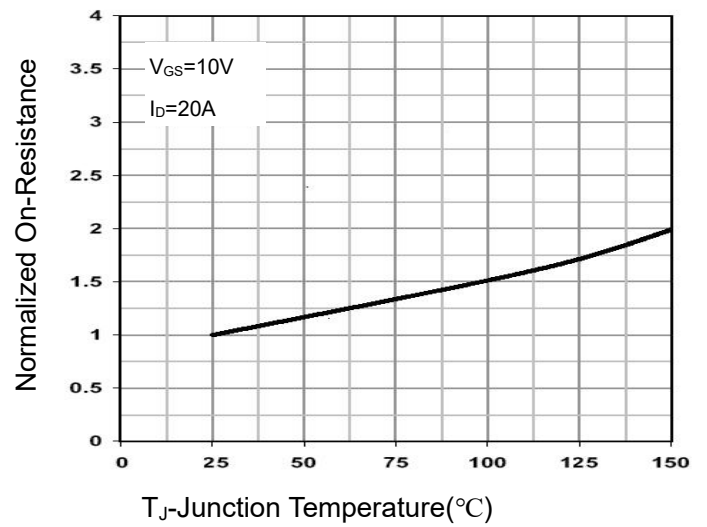
**Figure 1 Output Characteristics**



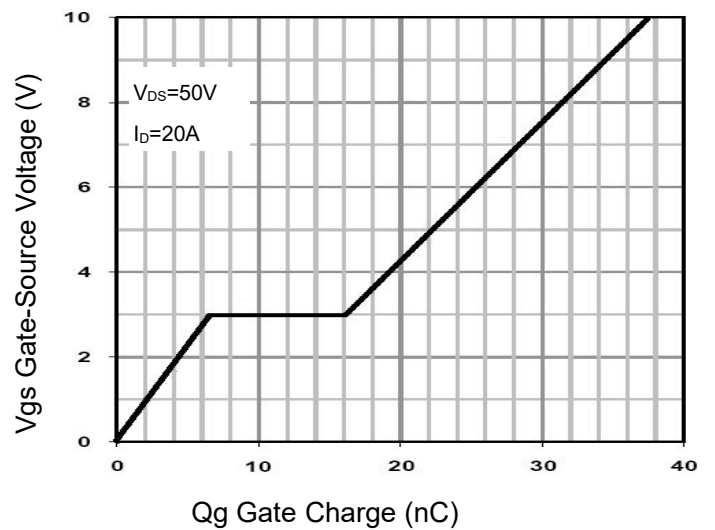
**Figure 2 Transfer Characteristics**



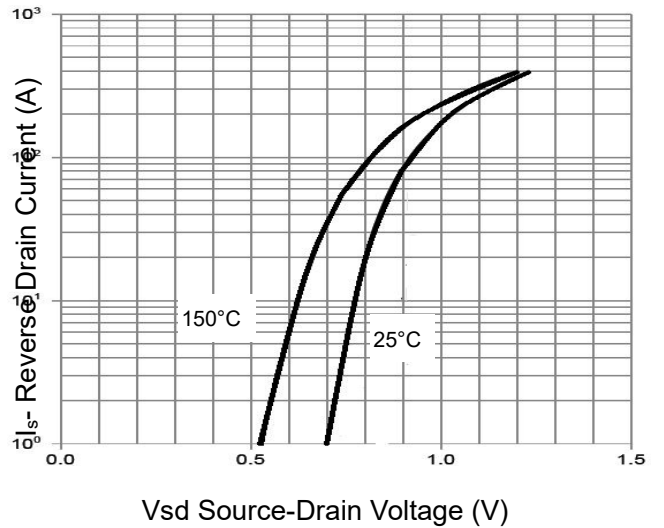
**Figure 3 Rdson- Drain Current**



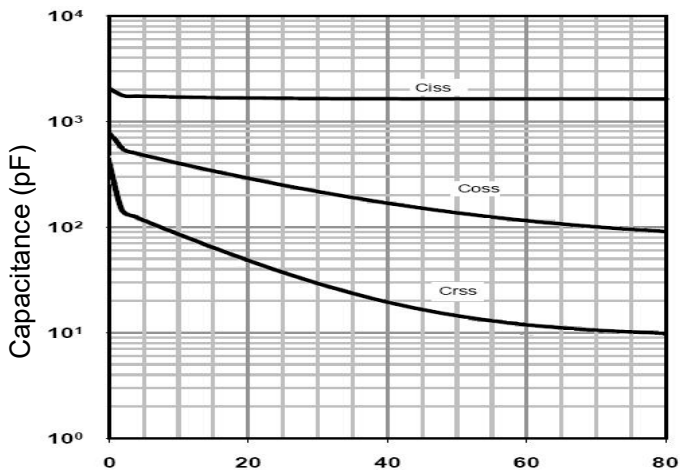
**Figure 4 Rdson-Junction Temperature**



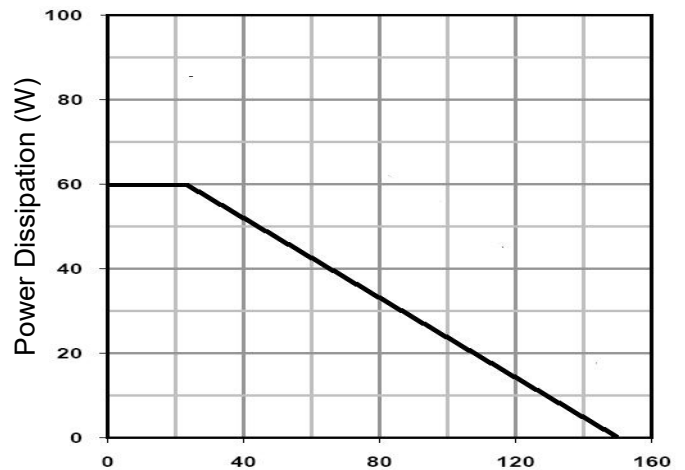
**Figure 5 Gate Charge**



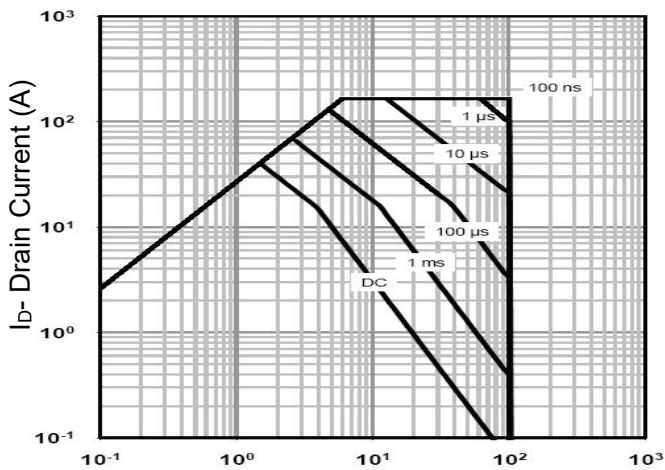
**Figure 6 Source- Drain Diode Forward**



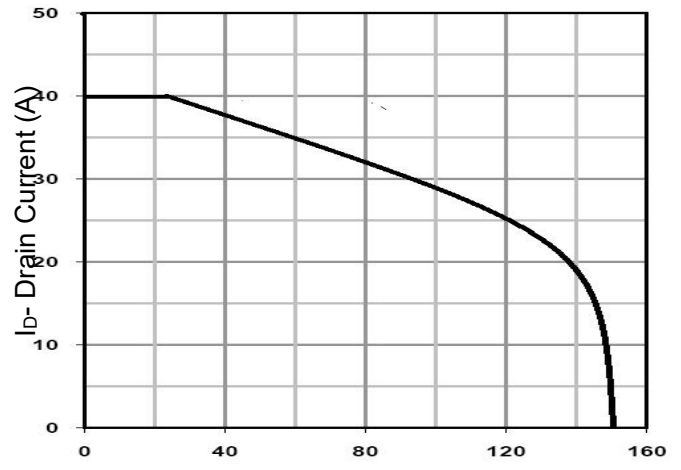
Vds Drain-Source Voltage (V)  
**Figure 7 Capacitance vs Vds**



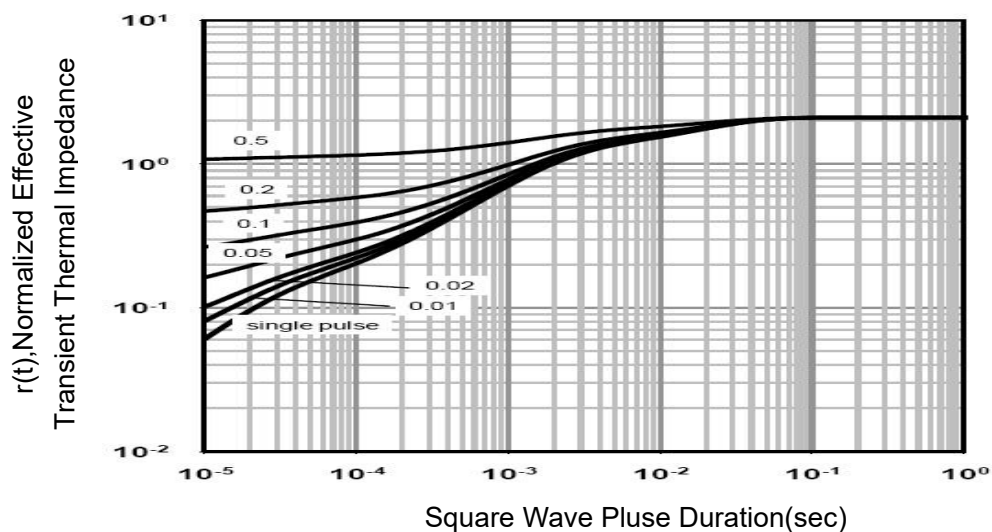
TA-Junction Temperature(°C)  
**Figure 9 Power De-rating**



Vds Drain-Source Voltage (V)  
**Figure 8 Safe Operation Area**

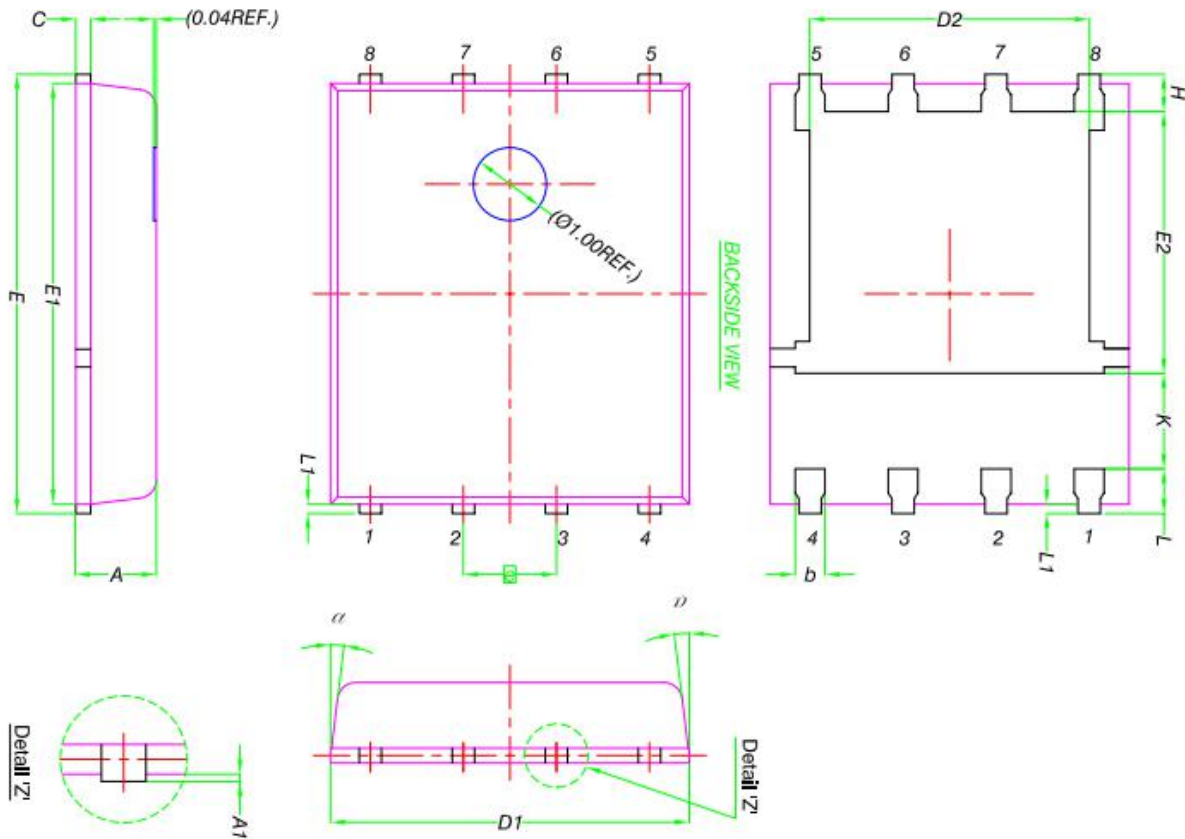


TA-Junction Temperature (°C)  
**Figure 10 Current De-rating**

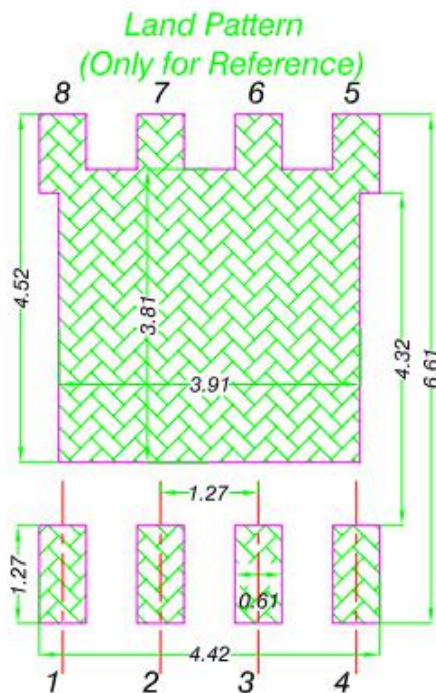


Square Wave Pluse Duration(sec)  
**Figure 11 Normalized Maximum Transient Thermal Impedance**

## DFN5X6-8L Package Information



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
A1	0	-	0.05
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
e	1.27 BSC		
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
α	0°	-	12°



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