

# **NCE N-Channel Super Trench II Power MOSFET**

# **Description**

The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{\text{DS(ON)}}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

# **Application**

- DC/DC Converter
- ●Ideal for high-frequency switching and synchronous rectification

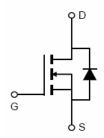
## **General Features**

- $V_{DS}$  =100V, $I_D$  =45A  $R_{DS(ON)}$ =8.1m $\Omega$  , typical (TO-220F)@  $V_{GS}$ =10V
- Excellent gate charge x R<sub>DS(on)</sub> product(FOM)
- Very low on-resistance R<sub>DS(on)</sub>
- 175 °C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% ΔVds TESTED!

## **TO-220F**





Schematic Diagram

**Package Marking and Ordering Information** 

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP080N10F	NCEP080N10F	TO-220F	ı	1	-

# Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>G</sub> S	±20	V
Drain Current-Continuous	I <sub>D</sub>	45	А
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100°C)	32	Α
Pulsed Drain Current (Note 1)	I <sub>DM</sub>	180	Α
Maximum Power Dissipation	P <sub>D</sub>	39	W
Derating factor		0.26	W/°C
Single pulse avalanche energy (Note 4)	E <sub>AS</sub>	420	mJ
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 175	$^{\circ}$

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	3.85	°C/W	
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Electrical Characteristics (T<sub>C</sub>=25 <sup>°</sup>C unless otherwise noted)

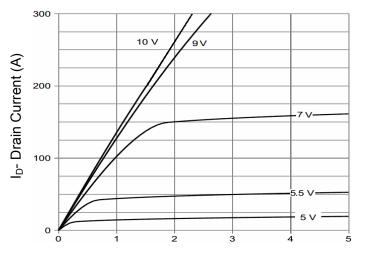
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	100		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V,V <sub>GS</sub> =0V	-	-	1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	•		•			
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_D=250\mu A$	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =22.5A	-	8.1	9.0	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =22.5A		60	-	S
Dynamic Characteristics (Note3)			•			
Input Capacitance	C <sub>lss</sub>		-	3070	-	pF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =50V, $V_{GS}$ =0V, F=1.0MHz	-	290	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	r=1.0lvlm2	-	23	-	pF
Switching Characteristics (Note 3)			•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	15	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =50V, $I_{D}$ =22.5A	-	10	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =1.6 $\Omega$	-	34	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	8	-	nS
Total Gate Charge	Qg	\/ -50\/1 -22.54	-	53	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =50V, $I_{D}$ =22.5A, $V_{GS}$ =10V	-	18	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	16	-	nC
Drain-Source Diode Characteristics			•	•		
Diode Forward Voltage (Note 2)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =22.5A	-	-	1.2	V
Diode Forward Current	Is		-	-	45	Α
Reverse Recovery Time	t <sub>rr</sub>	$T_J = 25^{\circ}C$ , $I_F = 22.5A$	-	60	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	106	-	nC

## Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 3. Guaranteed by design, not subject to production 4. EAS condition : Tj=25  $^{\circ}$ C,V<sub>DD</sub>=50V,V<sub>G</sub>=10V,L=0.5mH,Rg=25 $\Omega$

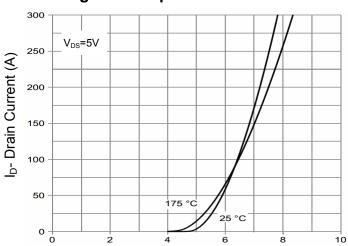


# **Typical Electrical and Thermal Characteristics**

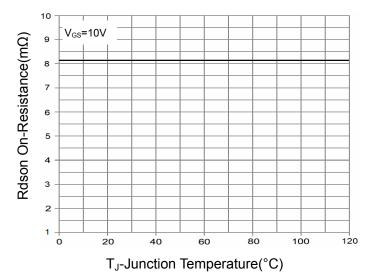


Vds Drain-Source Voltage (V)





Vgs Gate-Source Voltage (V)
Figure 2 Transfer Characteristics



**Figure 3 Rdson-Junction Temperature** 

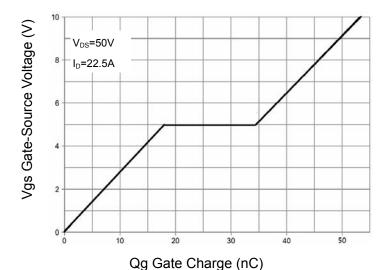


Figure 4 Cata Charge

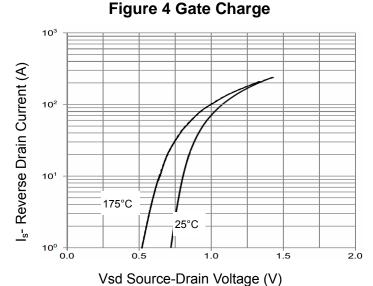
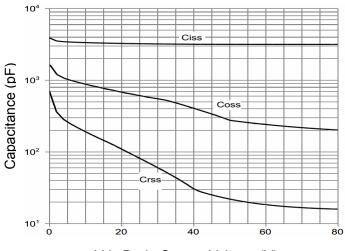


Figure 5 Source- Drain Diode Forward



Vds Drain-Source Voltage (V)

Figure 6 Capacitance vs Vds



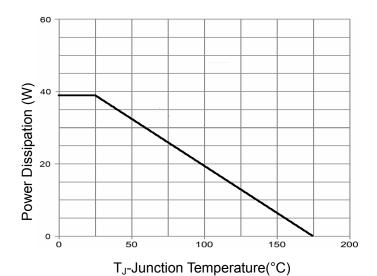


Figure 7 Power De-rating



T<sub>J</sub>-Junction Temperature (°C) **Figure 9 Current De-rating** 

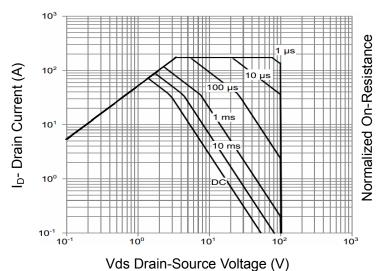
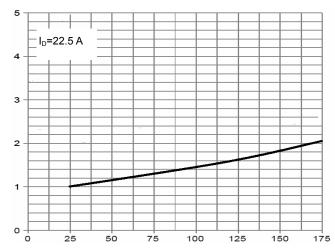


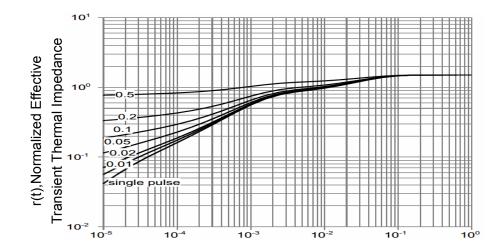
Figure 8 Safe Operation Area



**Figure 10 Rdson-Junction Temperature** 

V3.0

T<sub>J</sub>-Junction Temperature(°C)

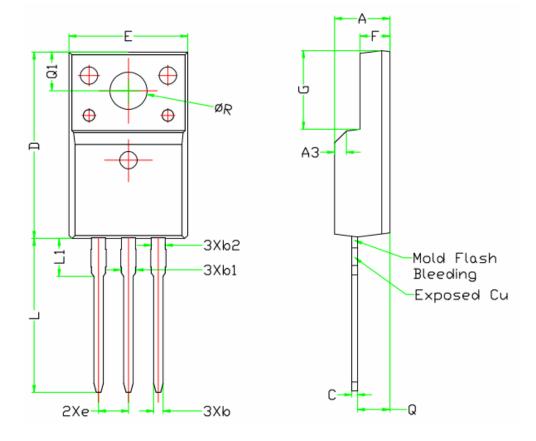


Square Wave Pluse Duration(sec)

**Figure 11 Normalized Maximum Transient Thermal Impedance** 



# **TO-220F Package Information**



	DIMENSIONS				
SYMBOL	Min.	Nom.	Max.		
A	4.60	4.70	4.80		
ь	0.70	0.80	0.91		
ы	1.20	1.30	1.47		
b2	1.10	1.20	1.30		
С	0.45	0.50	0.63		
D	15.80	15.87	15.97		
	2.54				
E	10.00	10.30			
F	2.44	2.54	2.64		
G	6.50	6.70	6.90		
L	12.90	13.10	13.30		
L1	3.13	3.23	3.33		
Q	2.65	2.75	2.85		
Q1	3.20	3.30	3.40		
ΦR	3.06	3.18	3.26		



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