

### 825 Series Full Color STF0A36ZA-B (SFT825B-S)







### **Product Brief**

#### **Description**

- This White Colored surface-mount LED comes in standard package dimension.
   Package Size: 3.5x2.8x0.75mm
- The package design coupled with careful selection of component materials allow these products to perform with high reliability in a larger temperature range -40°C to 110°C.
- The high reliability feature is crucial to Automotive interior and Indoor ESS.

#### **Features and Benefits**

- R.G.B Full Color
- ESD min 2kV
- MSL 2a Level
- Viewing angle 120°
- RoHS compliant

#### **Key Applications**

Automotive Lighting



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### **Performance Characteristics**

Table 1. Electro Optical Characteristics,  $I_F = 20$ mA,  $T_S = 25$ °C

Parameter		O. mark and	Value			
		Symbol	Min.	Тур.	Max.	Unit
Forward Current	R.G.B	I <sub>F</sub>	5	20	50	mA
	Red	$V_{F}$	1.8	2.1	2.4	
Forward Voltage [1]	Green	$V_{F}$	2.8	3.0	3.4	V
	Blue	$V_{F}$	2.8	3.1	3.4	
	Red	$I_v$	560	710	900	
Luminance Intensity [1]	Green	$I_{v}$	1400	1800	2300	mcd
	Blue	$I_{v}$	315	410	620	
	Red	$\Phi_{V}$	-	2380	-	- mlm -
Luminance Flux [3]	Green	$\Phi_V$	-	6200	-	
	Blue	$\Phi_V$	-	1580	-	
	Red	$\lambda_d$	623	626	632	
Dominant Wavelength [1]	Green	$\lambda_d$	520	526	532	
	Blue	$\lambda_d$	462	465	471	-
Viewing Angle [2]	R.G.B	2Θ <sub>1/2</sub>		120		deg.
	Red		-	152		- K/W -
Thermal resistance (J to A)	Green	Rθ <sub>J-A</sub> (ELEC.)	-	210		
	Blue	(LLLO.)	-	165		
	Red	- Rθ <sub>J-S</sub> _ (ELEC.)	-	60		
Thermal resistance (J to S)	Green		-	160		K/W
	Blue	(===)	-	90		

#### Notes:

- (1) Tolerance : VF : $\pm$ 0.1V, IV : $\pm$ 10%, Wd : $\pm$ 0.5nm
- (2)  $\Theta$ 1/2 is the off-axis where the luminous intensity is 1/2 of the peak intensity.
- (3)  $\Phi_{\text{V}}$  is the total luminous flux output as measured with an integrating sphere.

<sup>\*</sup> No values are provided by real measurement. Only for reference purpose.

### **Performance Characteristics**

#### **Table 2. Absolute Maximum Ratings**

Parameter	Symbol	Value			Unit	
r al allietei	- Cyllibol	Red	Green	Blue	Offic	
Power Dissipation (T <sub>a</sub> =25°C)	$P_d$	0.12[1]	0.17 <sup>[1]</sup>	0.17 <sup>[1]</sup>	W	
Forward Current (T <sub>a</sub> =25°C)	I <sub>F</sub>	50	50	50	mA	
Peak Forward Current (tp≤10μsec, D=0.005,T <sub>a</sub> =25°C)	I <sub>FM</sub>	200	250	250	mA	
Operating Temperature	T <sub>opr</sub>		-40 ~ +110		°C	
Storage Temperature	$T_{stg}$		-40 ~ +110		℃	
Junction Temperature	T <sub>j</sub>		125		°C	
Soldering Temperature	$T_{sld}$		oldering : 20 Idering : 3			
ESD <sup>[2]</sup>	-	ESD Class H3A (JESD22-A114-E)				

#### Notes:

- (1) The value for one LED device. (Single color)
- (2) A ESD Protection device is included for protection.
- LED's properties might be different from suggested values like above and below tables if operation condition will be exceeded our parameter range. Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
- · All measurements were made under the standardized environment of Seoul Semiconductor.

Fig 1. Color Spectrum, T<sub>S</sub>=25°C, I<sub>F</sub>=20mA

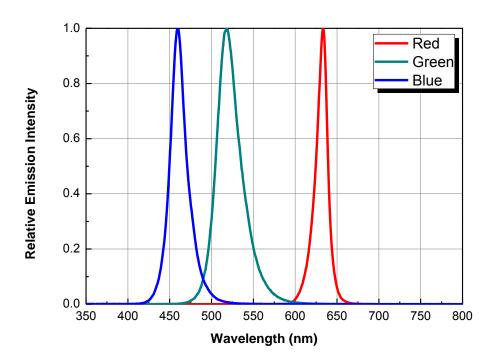


Fig 2.1. Radiant pattern, I<sub>F</sub>=20mA (horizontal)

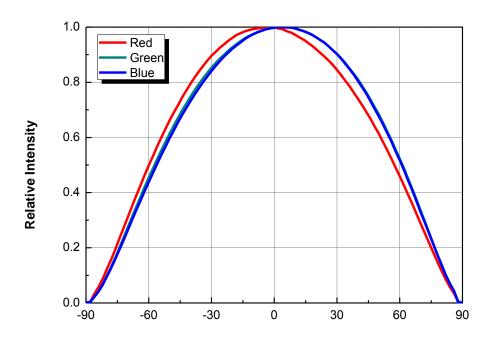


Fig 2.2.Radiant pattern, I<sub>F</sub>=20mA (Vertical)

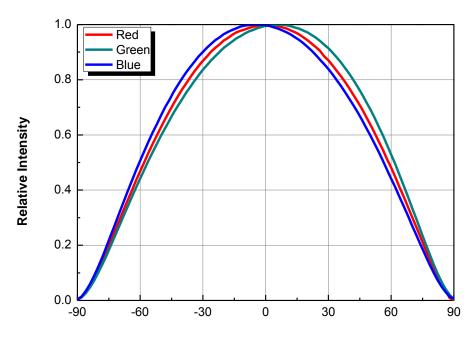


Fig 3.1. Forward Voltage vs. Forward Current, T<sub>i</sub>=25°C (Red)

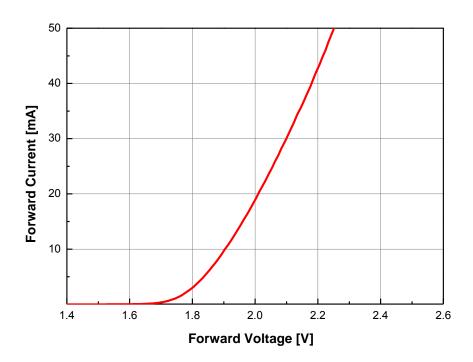


Fig 3.2. Forward Voltage vs. Forward Current, T<sub>i</sub>=25°C (Green)

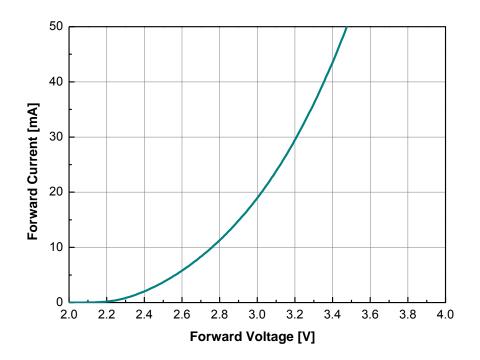


Fig 3.3 Forward Voltage vs. Forward Current, T<sub>i</sub>=25°C (Blue)

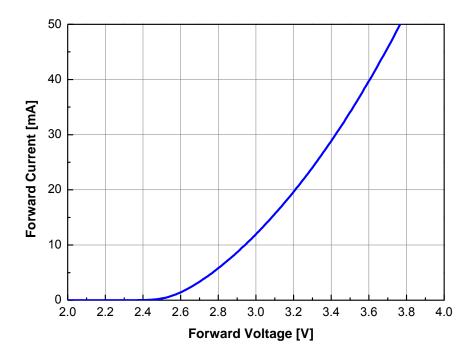


Fig 4.1. Forward Current vs. Relative Luminous Flux, T<sub>i</sub>=25°C (Red)

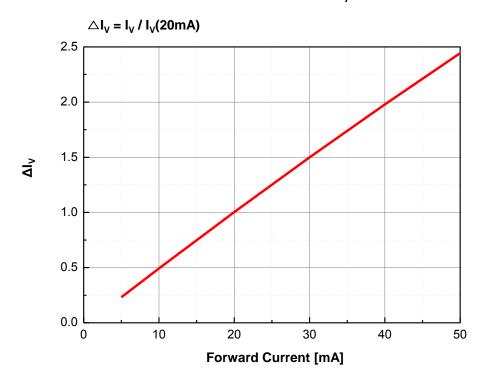


Fig 4.2. Forward Current vs. Relative Luminous Flux, T<sub>i</sub>=25°C (Green)

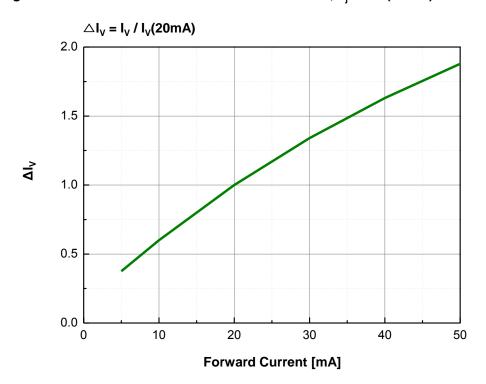


Fig 4.3. Forward Current vs. Relative Luminous Flux, T<sub>i</sub>=25°C (Blue)

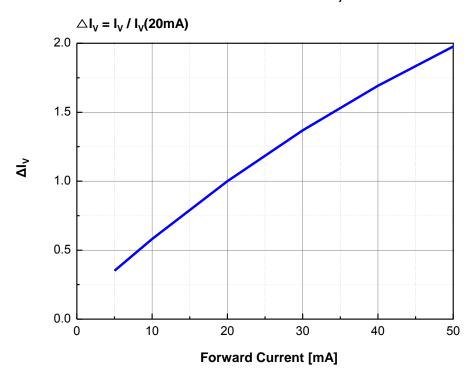


Fig 5.1. Forward Current vs. Dominant Wavelength Shift, T<sub>i</sub>=25°C (Red)

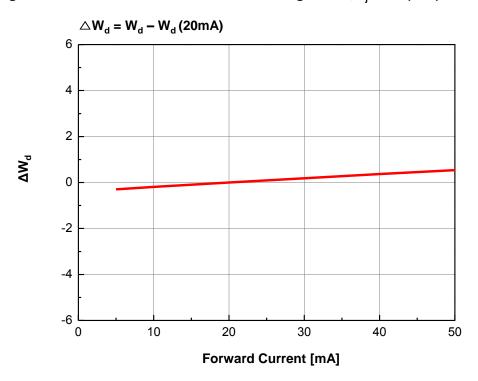


Fig 5.2. Forward Current vs. Dominant Wavelength Shift, T<sub>i</sub>=25°C (Green)

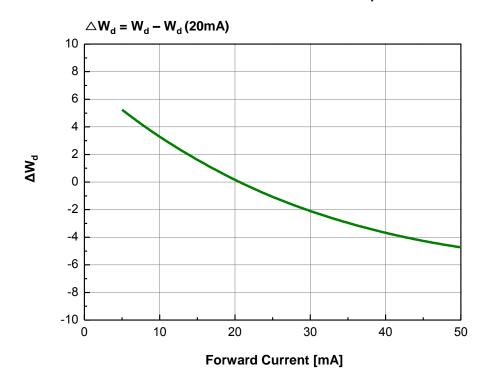


Fig 5.3. Forward Current vs. Dominant Wavelength Shift, T<sub>i</sub>=25°C (Blue)

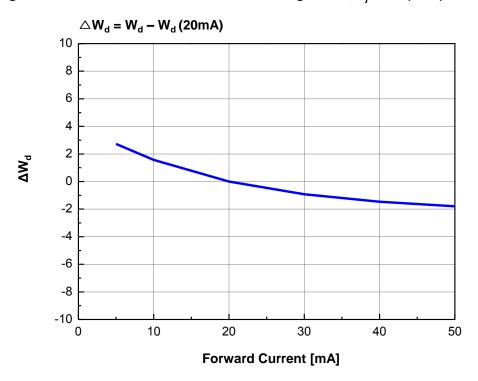


Fig 6.1. Junction Temperature vs. Relative Light Output, I<sub>F</sub>=20mA (Red)

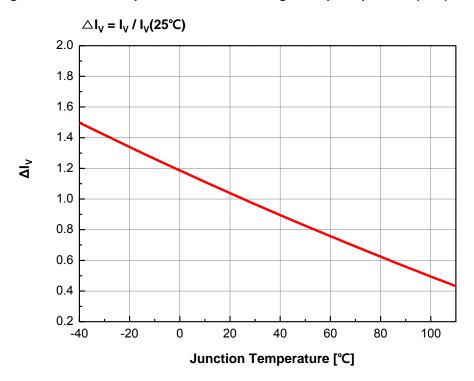


Fig 6.2. Junction Temperature vs. Relative Light Output, I<sub>F</sub>=20mA (Green)

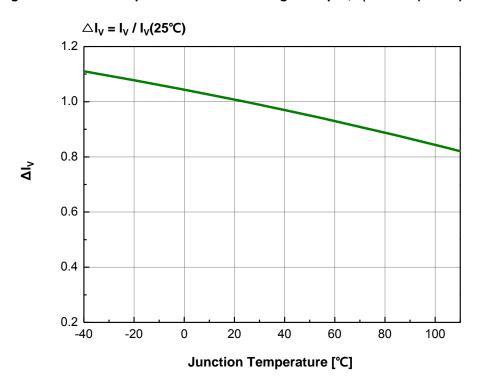


Fig 6.3. Junction Temperature vs. Relative Light Output, I<sub>F</sub>=20mA (Blue)

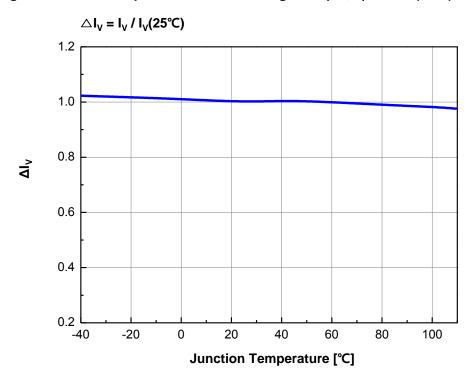


Fig 7.1. Junction Temperature vs. Forward Voltage, I<sub>F</sub>=20mA (Red)

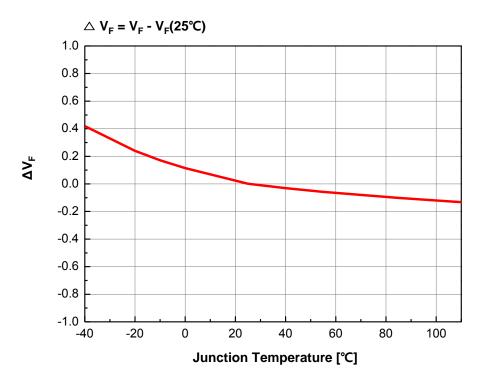


Fig 7.2. Junction Temperature vs. Forward Voltage, I<sub>F</sub>=20mA (Green)

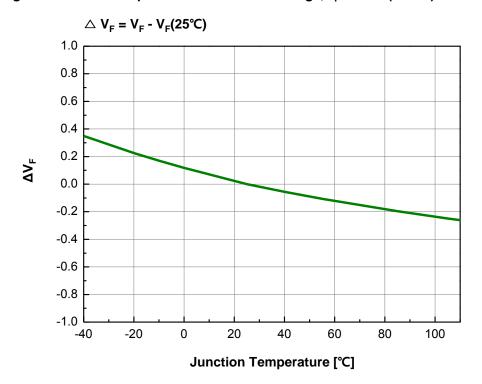


Fig 7.3. Junction Temperature vs. Forward Voltage, I<sub>F</sub>=20mA (Blue)

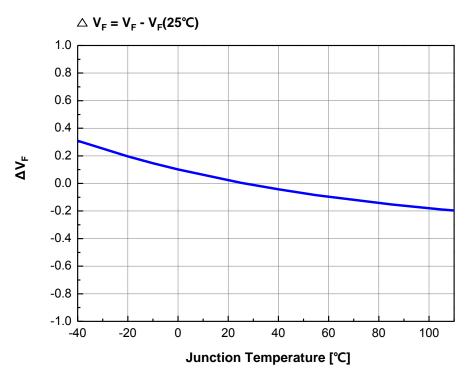


Fig 8.1.Junction Temperature vs. Dominant Wavelength Shift, I<sub>F</sub>=20mA (Red)

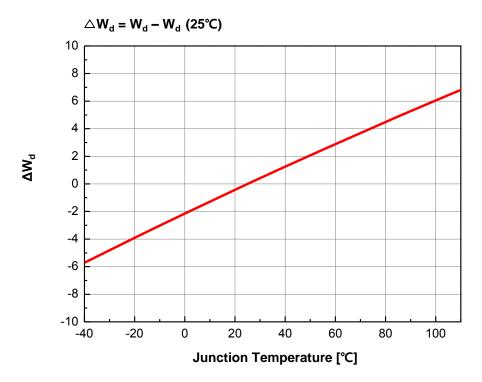


Fig 8.2.Junction Temperature vs. Dominant Wavelength Shift, I<sub>F</sub>=20mA (Green)

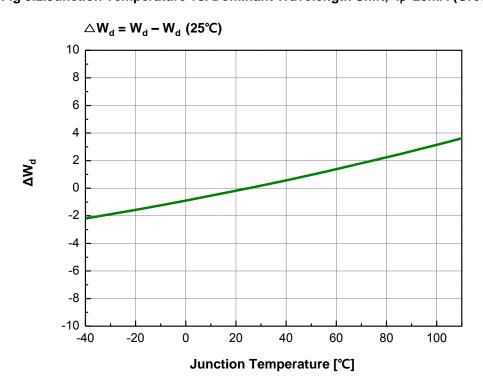


Fig 8.3. Junction Temperature vs. Dominant Wavelength Shift, I<sub>F</sub>=20mA (Blue)

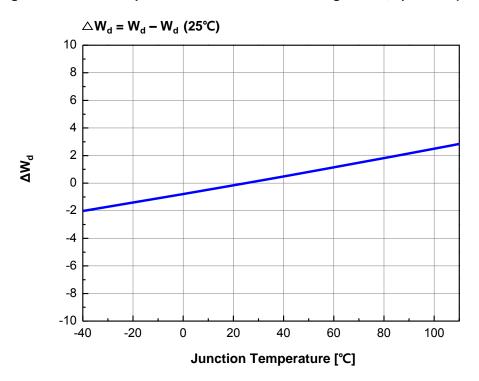


Fig 9.1. Maximum Forward Current vs. Temperature, 1 chip on (Red)

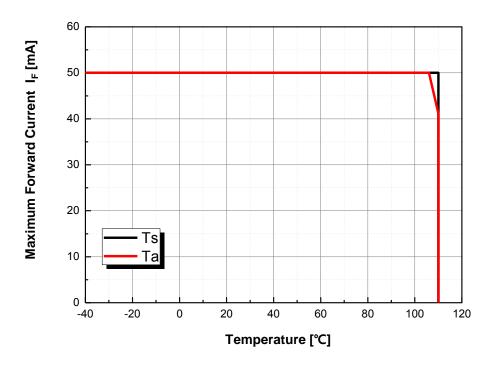


Fig 9.2. Maximum Forward Current vs. Temperature, 1 chip on (Green)

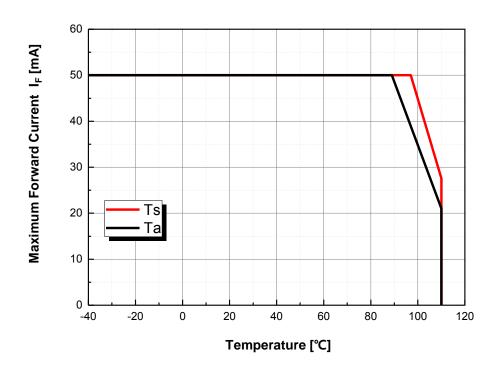


Fig 9.3. Maximum Forward Current vs. Temperature, 1 chip on (Blue)

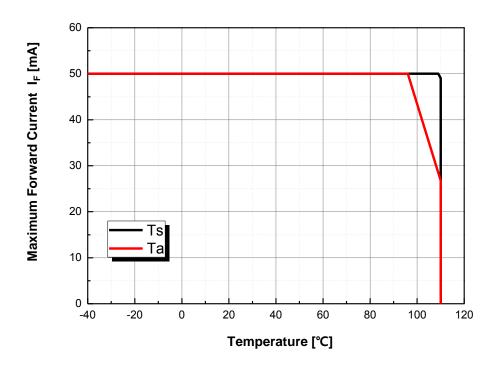


Fig 10.1. Pulse Permissibility (Red, T<sub>i</sub> = 25°C)

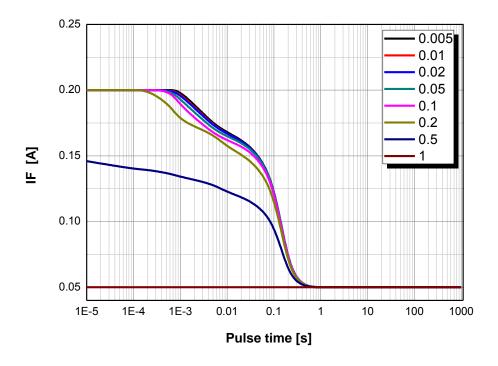
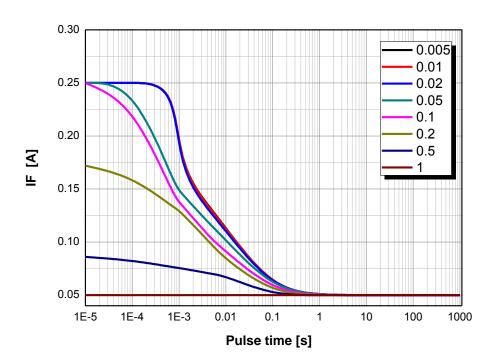


Fig 10.2. Pulse Permissibility (Green, Blue,  $T_j = 25^{\circ}C$ )



### **Color Bin Structure**

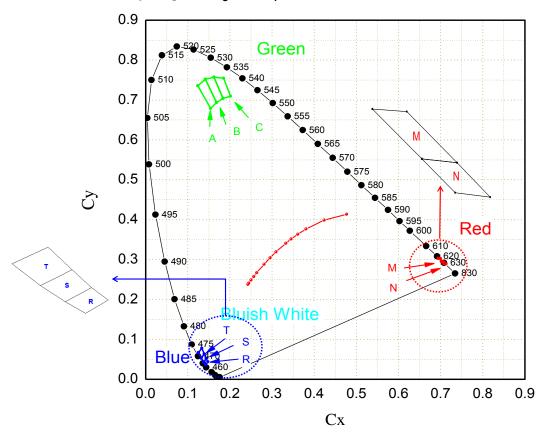
Table 3. Bin Code description

	Dominant Wavelength (nm)		Color	Luminous Intensity (mcd)			
Color	Bin Code	Min.	Max.	Chromaticity Coordinate	Bin Code	Min.	Max.
Red	М	623	628	Refer to page.22	R1	560	710
Red	N	628	632		R2	710	900
	Α	520	524		G1	1400	1800
Green	В	524	528		G2	1800	2300
	С	528	532				
	R	462	465		В3	315	400
Blue	S	465	468		B4	400	500
	Т	468	471		B5	500	620

Available rank

### **Color Bin Structure**

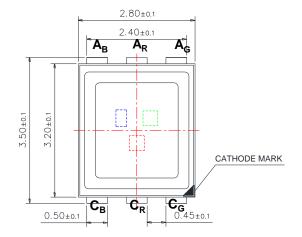
### CIE Chromaticity Diagram, T<sub>S</sub>=25°C, I<sub>F</sub>=20mA



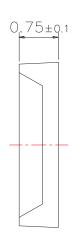
		Re	ed		
N	1	1	N		
CIE x	CIE y	CIE x	CIE y		
0.6943	0.3029	0.7022	0.2951		
0.7022	0.2951	0.7075	0.2898		
0.7052	0.2948	0.7105	0.2895		
0.6972	0.3027	0.7052	0.2948		
		Gre	een		
A		E	3	C	;
CIE x	CIE y	CIE x	CIE y	CIE x	CIE y
0.1237	0.7359	0.1415	0.7518	0.1621	0.7570
0.1541	0.6790	0.1681	0.6938	0.1835	0.7040
0.1681	0.6938	0.1835	0.7040	0.2011	0.7096
0.1415	0.7518	0.1621	0.7570	0.1849	0.7548
		Bl	ue		
R	!	5	5	Ţ	
CIE x	CIE y	CIE x	CIE y	CIE x	CIE y
0.1375	0.0428	0.1317	0.0532	0.1251	0.0672
0.1422	0.0353	0.1375	0.0428	0.1317	0.0532
0.1475	0.0439	0.1436	0.0519	0.1391	0.0634
0.1436	0.0519	0.1391	0.0634	0.1335	0.0779

### **Mechanical Dimensions**

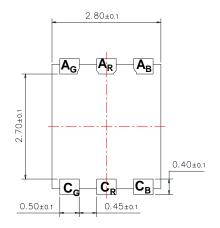
# **Top View**



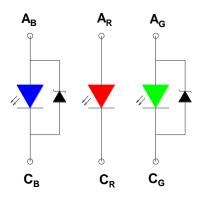
### **Side View**



### **Bottom View**



### Circuit



#### Notes:

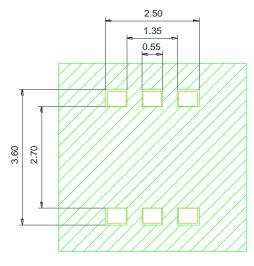
- (1) All dimensions are in millimeters.
- (2) Scale: none
- (3) Undefined tolerance is  $\pm 0.2$ mm

### **Recommended Solder Pad**

#### **Recommended PCB Solder Pad**

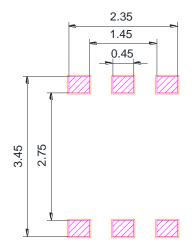
# 2.40 1.40 0.50 0.20

#### **Solder Resist**



Solder Resist

#### **Recommended Stencil Pattern**





#### Notes:

- (1) All dimensions are in millimeters.
- (2) Scale: none
- (3) This drawing without tolerances are for reference only.
- (4) Undefined tolerance is  $\pm 0.1$ mm.

### **Reflow Soldering Characteristics**

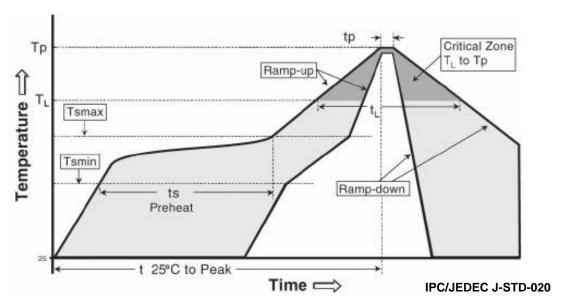


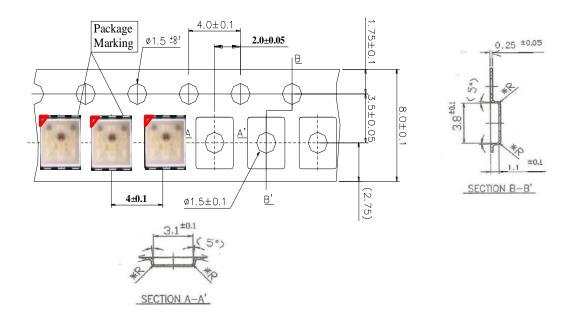
Table 4.

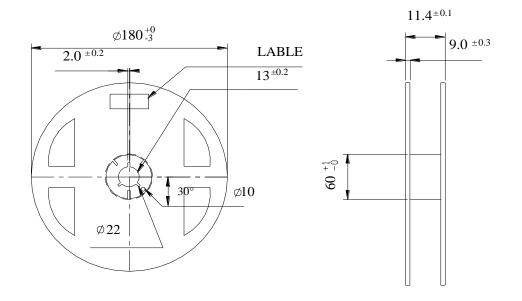
Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (Tsmax to Tp)	3° C/second max.	3° C/second max.
Preheat - Temperature Min (Tsmin) - Temperature Max (Tsmax) - Time (Tsmin to Tsmax) (ts)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-180 seconds
Time maintained above: - Temperature (TL) - Time (tL)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak Temperature (Tp)	215℃	260℃
Time within 5°C of actual Peak Temperature (tp)2	10-30 seconds	20-40 seconds
Ramp-down Rate	6 °C/second max.	6 °C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

#### **Caution**

- (1) Reflow soldering is recommended not to be done more than two times. In the case of more than 24 hours passed soldering after first, LEDs will be damaged.
- (2) Repairs should not be done after the LEDs have been soldered. When repair is unavoidable, suitable tools must be used.
- (3) Die slug is to be soldered.
- (4) When soldering, do not put stress on the LEDs during heating.
- (5) After soldering, do not warp the circuit board.

# **Emitter Tape & Reel Packaging**





#### Notes:

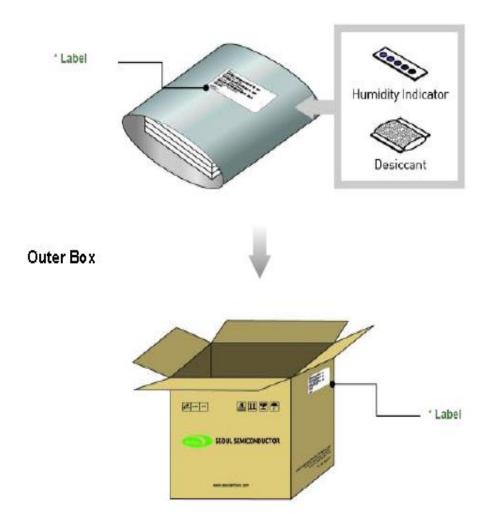
(1) Quantity: 3500pcs/Reel

(2) All dimensions are in millimeters (tolerance :  $\pm 0.3)\,$ 

(3) Scale none

# **Emitter Tape & Reel Packaging**

### Aluminum Bag



#### Notes:

(1) Heat Sealed after packing (Use Zipper Bag)

### **Product Nomenclature**

Table 5. Part Numbering System :  $X_1X_2X_3X_4X_5X_6X_7$ - $X_8$ 

Part Number Code	Description	Part Number	Value
<b>X</b> <sub>1</sub>	Company	S	SSC Code
X <sub>2</sub>	Top View LED series	Т	Top view
X <sub>3</sub> X <sub>4</sub>	Color	F0	Full Color
X <sub>5</sub> X <sub>6</sub> X <sub>7</sub>	Package information	A36	
X <sub>89</sub>	Product Revision	ZA	-
X <sub>10</sub>	Code	В	

Table 6. Lot Numbering System  $: Y_1Y_2Y_3Y_4Y_5Y_6Y_7Y_8Y_9Y_{10} - Y_{11}Y_{12}Y_{13}Y_{14}Y_{15}Y_{16}Y_{17}$ 

Lot Number Code	Description	Lot Number	Value
Y <sub>1</sub> Y <sub>2</sub>	Y <sub>1</sub> Y <sub>2</sub> Year		
Y <sub>3</sub>	Month		
Y <sub>4</sub> Y <sub>5</sub>	Day		
Y <sub>6</sub>	Top View LED series		
Y <sub>7</sub> Y <sub>8</sub> Y <sub>9</sub> Y <sub>10</sub> Mass order			
Y <sub>11</sub> Y <sub>12</sub> Y <sub>13</sub> Y <sub>14</sub> Y <sub>15</sub> Y <sub>16</sub> Y <sub>17</sub>	Internal Number		

### Handling of Silicone Resin for LEDs

(1) During processing, mechanical stress on the surface should be minimized as much as possible. Sharp objects of all types should not be used to pierce the sealing compound.



(2) In general, LEDs should only be handled from the side. By the way, this also applies to LEDs without a silicone sealant, since the surface can also become scratched.



These conditions must be considered during the handling of such devices. Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust. As mentioned previously, the increased sensitivity to dust requires special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning

(3) Silicone differs from materials conventionally used for the manufacturing of LEDs.

- solution must be applied to the surface after the soldering of wire.

  (4) Seoul Semiconductor suggests using isopropyl alcohol for cleaning. In case other solvents are
  - assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.
- (5) Please do not mold this product into another resin (epoxy, urethane, etc) and do not handle this product with acid or sulfur material in sealed space.
- (6) Avoid leaving fingerprints on silicone resin parts.

used, it must be

### **Precaution for Use**

#### (1) Storage

To avoid the moisture penetration, we recommend store in a dry box with a desiccant.

The recommended storage temperature range is 5°C to 30°C and a maximum humidity of RH50%.

#### (2) Use Precaution after Opening the Packaging

Use SMT techniques properly when you solder the LED as separation of the lens may affect the light output efficiency.

Pay attention to the following:

- a. Recommend conditions after opening the package
  - Sealing
  - Temperature : 5 ~ 30°C Humidity : less than RH60%
- b. If the package has been opened more than 4 week(MSL\_2a) or the color of the desiccant changes, components should be dried for 10-24hr at  $65\pm5^{\circ}$ C

#### (3) For manual soldering

Seoul Semiconductor recommends the soldering condition

(ZC series product is not adaptable to reflow process)

- a. Use lead-free soldering
- b. Soldering should be implemented using a soldering equipment at temperature lower than 350°C.
- c. Before proceeding the next step, product temperature must be stabilized at room temperature.
- (4) Components should not be mounted on warped (non coplanar) portion of PCB.
- (5) Radioactive exposure is not considered for the products listed here in.
- (6) It is dangerous to drink the liquid or inhale the gas generated by such products when chemically disposed of.
- (7) This device should not be used in any type of fluid such as water, oil, organic solvent and etc. When washing is required, IPA (Isopropyl Alcohol) should be used.
- (8) When the LEDs are in operation the maximum current should be decided after measuring the package temperature.
- (9) LEDs must be stored properly to maintain the device. If the LEDs are stored for 3 months or more after being shipped from Seoul Semiconductor,
  - a sealed container with vacuum atmosphere should be used for storage.
- (10) The appearance and specifications of the product may be modified for improvement without notice.

### **Precaution for Use**

- (11) Long time exposure of sun light or occasional UV exposure will cause silicone discoloration.
- (12) Attaching LEDs, do not use adhesive that outgas organic vapor.
- (13) The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.
- (14) Please do not touch any of the circuit board, components or terminals with bare hands or metal while circuit is electrically active.
- (15) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues.
- (16) LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS). Below is a list of suggestions that Seoul Semiconductor purposes to minimize these effects.
- a. ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is the defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to an LEDs may cause the product to demonstrate unusual characteristics such as:

- Increase in reverse leakage current lowered turn-on voltage
- Abnormal emissions from the LED at low current

The following recommendations are suggested to help minimize the potential for an ESD event. One or more recommended work area suggestions:

- Ionizing fan setup
- ESD table/shelf mat made of conductive materials
- ESD safe storage containers

One or more personnel suggestion options:

- Antistatic wrist-strap
- Antistatic material shoes
- Antistatic clothes

#### Environmental controls:

- Humidity control (ESD gets worse in a dry environment)

### **Precaution for Use**

#### b. EOS (Electrical Over Stress)

Electrical Over-Stress (EOS) is defined as damage that may occur when an electronic device is subjected to a current or voltage that is beyond the maximum specification limits of the device. The effects from an EOS event can be noticed through product performance like:

- Changes to the performance of the LED package
  (If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker show severe performance degradation.)
- Changes to the light output of the luminaire from component failure
- Components on the board not operating at determined drive power

Failure of performance from entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures. It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes have been investigated to vary, but there are some common signs that will indicate an EOS event has occurred:

- Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
- Damage to the bond pads located on the emission surface of the LED package (shadowing can be noticed around the bond pads while viewing through a microscope)
- Anomalies noticed in the encapsulation and phosphor around the bond wires.
- This damage usually appears due to the thermal stress produced during the EOS event.
- c. To help minimize the damage from an EOS event Seoul Semiconductor recommends utilizing:
  - A surge protection circuit
  - An appropriately rated over voltage protection device
  - A current limiting device

### **Company Information**

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#### **Company Information**

Seoul Semiconductor (www.SeoulSemicon.com) manufacturers and packages a wide selection of light emitting diodes (LEDs) for the automotive, general illumination/lighting, Home appliance, signage and back lighting markets. The company is the world's fifth largest LED supplier, holding more than 10,000 patents globally, while offering a wide range of LED technology and production capacity in areas such as "nPola", "Acrich", the world's first commercially produced AC LED, and "Acrich MJT - Multi-Junction Technology" a proprietary family of high-voltage LEDs.

The company's broad product portfolio includes a wide array of package and device choices such as Acrich and Acirch2, high-brightness LEDs, mid-power LEDs, side-view LEDs, and through-hole type LEDs as well as custom modules, displays, and sensors.

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