Preliminary Specification (RoHS Compliant & Halogen Free)

CUSTOMER	:	
CUSTOMER'S PART NO.	:	
DESCRIPTION	:	Multi-layer Chip Diplexer
PART NO.	:	LTD-2012-DHXKM-A2
DATE	:	
AUTHORIZED BY	:	Derek Wei

	FULLY APPROVED	PARTIALLY APPROVED	REJECTED
SIGN			
SUGGESTION			

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Revision History

Version	Date	Description	Approved by	Prepared by
1	2020/11/18	Initial specification.	CF	Chang



APPLICATION

For LTE application.

FEATURES

Compact Size

Miniaturized SMD packaged in low profile and lightweight.

Low loss

Low insertion loss, high attenuation.

High Soldering Heat Resistance

High quality termination allows both flow and re-flow soldering methods to be applied.

Characteristics

Eliminate noise over a wide frequency range. Idea for high frequency and space limited designs.

Available in tape and reel packaging for automatic mounting

PRODUCT IDENTIFICATION



- ① Product Code
- ② Dimension Code
- ③ Series Type (### represents center frequency and xx represents material type)
- ④ Design Code
- ⑤ Pattern Code



ELECTRICAL REQUIREMENTS

Low-Band

Item	Frequency Range(MHz)	Min.	Тур.	Max.
Incortion Loss (dP)	617~746	-	0.26	0.40
Insertion Loss (dB)	746~960	-	0.38	0.50
Return Loss (dB)	617~960	10	20.1	-
	1710~2170	30	31.6	-
Attenuation (dB)	2170~2400	30	39.0	-
	2400~2700	35	38.3	-

High-Band

Item	Frequency Range(MHz)	Min.	Тур.	Max.
Insertion Loss (dB)	1710~2700	-	0.41*	0.50
Return Loss (dB)	1710~2700	10	13.5*	-
Attenuation (dB)	698~960	25	26.0	-

Common

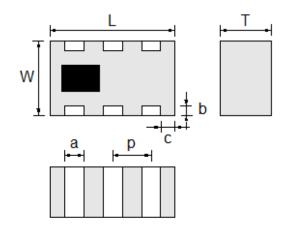
Item	Frequency Range(MHz)	Min.	Тур.	Max.
Poturn Loop (dP)	617~960	10	19.8	-
Return Loss (dB)	1710~2700	10	13.3*	-

*Impedance for port1 and port3 = Match to client's chipset

Operating Temperature Range : -40~85°C Power Capacity : 3W max.



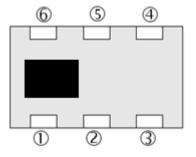
PRODUCT DIMENSION



L	W	Т	а
2.00±0.15	1.25±0.15	0.90±0.10	0.30±0.15
b	С	р	
0.30±0.20	0.20±0.15	0.65±0.05	

NOTE : Dimensions in mm.

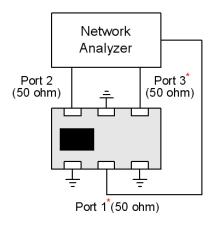
TERMINAL CONFIGURATION



① GND

- ② Common Port
- 3 GND
- ④ High Frequency Port
- 5 GND
- 6 Low Frequency Port

MEASURING DIAGRAM

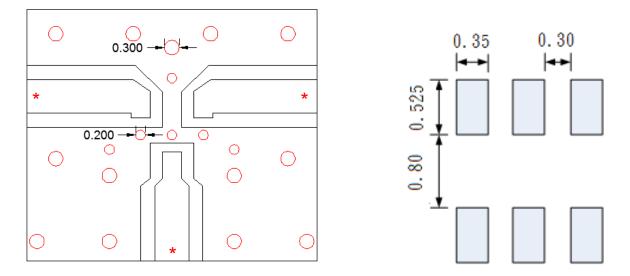


Test Instrument : Agilent E5071C Network Analyzer or equivalent.

*Impedance for port1 and port3 = Match to client's chipset



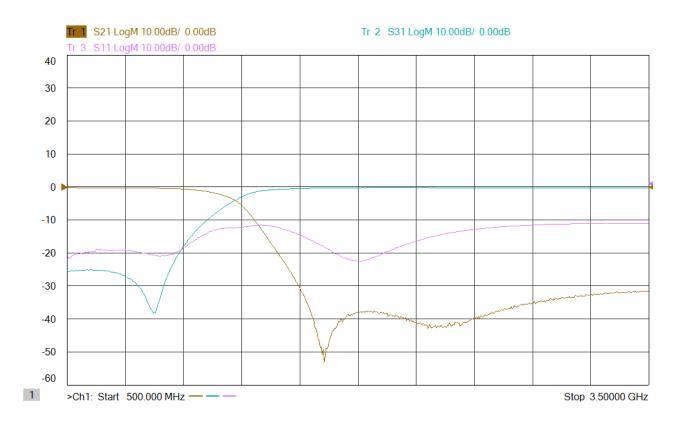
RECOMMENDED PCB LAYOUT



Unit : mm

*Line width should be designed to match 50Ω characteristic impedance, depending on PCB material and thickness.

ELECTRICAL CHARACTERISTICS (T=25°C)





RELIABILITY TEST

Mechanical Test

Item	Test Condition	Specification
Vibration	10 Hz/min~55 Hz/min~10 Hz/min vibration frequency with 1.5 mm amplitude for two hours in x, y, z directions	No apparent damage
Drop shock	Dropped onto printed circuit board from 100cm height three times in x, y, z directions. The terminals shall be protected.	No apparent damage
Soldering heat resistance	Preheating temperature : 150±10°C Preheating time : 1 to 2 minutes Solder bath temperature : 260±5°C Bathing time : 10±1 seconds	Loss of metallization on the edges of each electrode shall not exceed 25%.
Bending test onto printed circuit board	Solder specimen LTCC components on the test printed circuit board (L: 100 x W: 40 x T: 1.6mm) in appended recommended PCB pattern. Apply the load in direction of the arrow until bending reaches 2 mm. 60sec holding time.	No apparent damage
Solderability	*Solder bath temperature:245±5°C *Immersion time:3±1 seconds. Solder:Sn3Ag0.5Cu for lead-free	At least 95% of a surface of each terminal electrode must be covered by fresh solder.
Adhesive strength	Standard is as follows 0605~1005 >0.1KgF 1109~2016 >0.5KgF 2520~>1KgF	No apparent damage

Environment Test

Thermal shock	-40 $^\circ\!\!\!C$ ~85 $^\circ\!\!\!C$ for 100 cycles each cycle being 30 min	No apparent damage Fulfill the electrical spec. after test
Humidity resistance	85±2℃,80~90% R.H. for 1000 hours	No apparent damage Fulfill the electrical spec. after test
High temperature resistance	$85\pm2^{\circ}$ C for 1000 hours	No apparent damage Fulfill the electrical spec. after test
Low temperature resistance	-40±3℃ for 1000 hours	No apparent damage Fulfill the electrical spec. after test



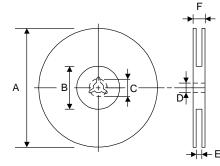
PACKAGING FOR SMC

Peel-off force



The force for peeling off cover tape is 10 grams in the arrow direction.

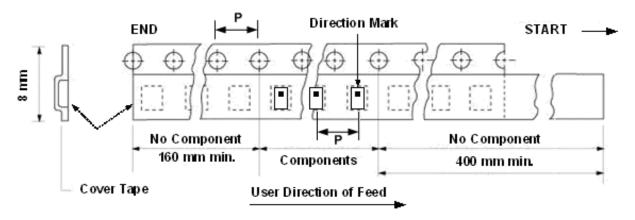
Dimension (Unit: mm)



TYPE	Α	В	С	D	E	F
8 mm	178±1	60+0.5 -0	-	13±0.2	9±0.5	12±0.5
12 mm	178±0.3	60±0.2	19.3±0.1	13.5±0.1	13.6±0.1	-

Taping quantity

SERIES	5824 5724	5320 5220	4532	4516	3225	3216 2520	2012 1608	1005
PCS/Reel	5000	3000	1000	2000	2500	3000	4000	10000

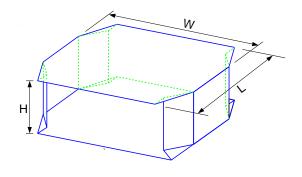


P=4 mm



TAPE PACKING CASE

Unit:cm



No. of Reels	W	L	н
2	18±0.5	18±0.5	2.4±0.2
3	18±0.5	18±0.5	3.6±0.2
4	18±0.5	18±0.5	4.8±0.2
5	18±0.5	18±0.5	6.0±0.2

MSL RATING Level 1

OPERATION TEMPERATURE

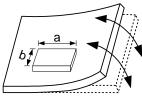
-40°C~85°C

STORAGE CONDITION

The temperature should be within -40~85°C and humidity should be less than 75% RH. The product should be used within 6 months from the time of delivery.

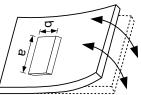
ATTENTION REGARDING PCB BENDING

(a) PCB shall be designed so that products are not subjected to the mechanical stress for board wrapage. Product shall be located in the sideway direction to the mechanical stress.



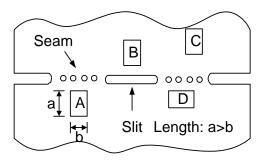
(Poor example)

Length: a>b

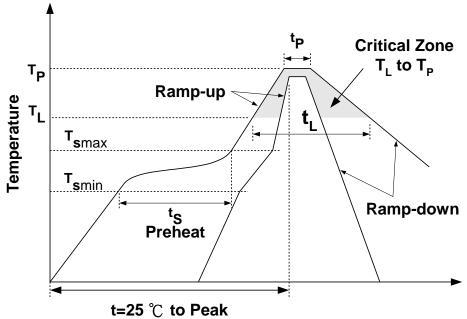


(Good example)

(b) Products (A,B,C,D) shall be located carefully so that products are not subjected to the mechanical stress due to warping the board. Because they may be subjected to the mechanical stress in order of A>C>B = D.







Time

Profile Feature		Sn-Pb	Pb-Free
Preheat	ts	60~120 seconds	60~180 seconds
	T _{smin}	100 ℃	150 ℃
	T _{smax}	150 ℃	200 ℃
Average ramp-up rate (T _{smax} to T _P)		3°C/second max.	3°C/second max.
Time main above	Temperature (T∟)	183 ℃	217 ℃
	Time (t∟)	60~150 seconds	60~150 seconds
Peak temperature (T _P)		230 ℃	250~260 ℃
Time within 5℃ of actual peak temperature (t⊵)		10 seconds	10 seconds
Ramp-down rate		6°C/sec max.	6°C/sec max.
Time 25 $^\circ\!\!\mathbb{C}$ to peak temperature		6 minutes max.	8 minutes max.

NOTES

The contents of this data sheet are subject to change without notice. Please confirm the specifications and delivery conditions when placing your order.



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