

N-Channel Enhancement Mode Power MOSFET

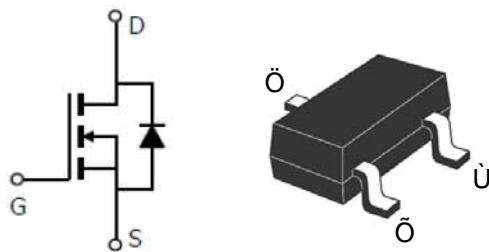
- Features

$V_{DS} = 100V$
 $I_D = 2A$
 $R_{DS(ON)} \leq 240m\Omega (V_{GS}=10V)$

- General Description

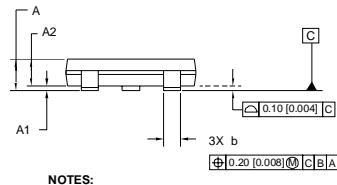
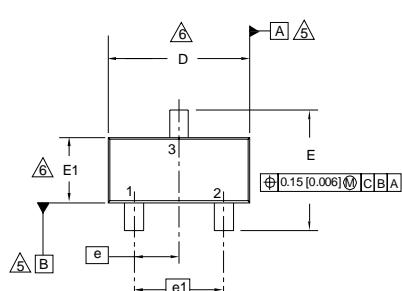
The **ATNM03S100S** uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications. The package form is SOT-23, which accords with the RoHS standard.

- Pin Configurations

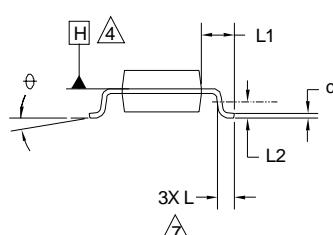


- Package Information

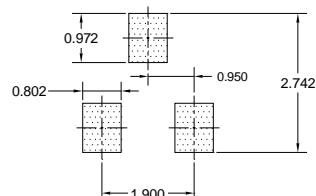
SOT-23



NOTES:



Recommended Footprint



SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
	0.89	1.12	0.035	0.044
A	0.01	0.10	0.0004	0.004
A1	0.88	1.02	0.035	0.040
A2	0.30	0.50	0.012	0.020
b	0.08	0.20	0.003	0.008
c	2.80	3.04	0.110	0.120
D	2.10	2.64	0.083	0.104
E	1.20	1.40	0.047	0.055
E1	0.95	BSC	0.037	BSC
e	1.90	BSC	0.075	BSC
e1	0.40	0.60	0.016	0.024
L	0.54	REF	0.021	REF
L1	0.25	BSC	0.010	BSC
L2	0.0	8	0	8



JYELECTRONICS®

TNM03K100K

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● Absolute Maximum Ratings (@TA=25°C unless otherwise noted)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V _{DSS}	100	V
Gate Source Voltage	V _{GSS}	±20	V
Drain Current (Continuous) *AC	I _D	2	A
Drain Current (Pulse) *B	I _{DM}	5	A
Power Dissipation	P _D	1.25	W
Operating Temperature/ Storage Temperature	T _{J/T_{STG}}	-55~175	°C

● Thermal Characteristics

Parameter	Symbol	Ratings	Unit
Thermal Resistance ,Junction-to-Ambient	R _{θJA}	100	°C/W

● Electrical Characteristics (@TA=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250uA	100	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V	--	--	1	uA
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250uA	1.2	--	2.5	V
Gate Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
Drain-Source On-state Resistance	R _{DS(on)}	V _{GS} =10V, I _D =3A	--	--	240	mΩ
Forward Transconductance	g _{fs}	I _{SD} =1A, V _{DS} =5V	1	--	--	S
Total Gate Charge	Q _g	V _{GS} =10V, V _{DD} =50V, I _D =1.3A	--	5.2	--	nC
Gate- Source Charge	Q _{gs}		--	0.75	--	nC
Gate- Drain Charge	Q _{gd}		--	1.4	--	nC
Turn-on Delay Time	t _{d(on)}	V _{GS} =10V, V _{DD} =50V, I _D =1.3A R _L =39Ω, R _{GEN} =1Ω	--	6	--	ns
Turn-on Rise Time	t _r		--	10	--	ns
Turn-off Delay Time	t _{d(off)}		--	10	--	ns
Turn-off Fall Time	t _f		--	6	--	ns
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =50V, f=1MHZ	--	190	--	pF
Output Capacitance	C _{oss}		--	22	--	pF
Reverse Transfer Capacitance	C _{rss}		--	13	--	pF



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- Reverse Diode Characteristics (@TA=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Continuous Diode Forward Current	I _S	V _G =V _D =0V , Force Current	--	--	2	A
Diode Forward Voltage	V _{SD}	I _{SD} =1.3A, V _{GS} =0V	--	--	1.2	V

A: The value of R_{θJA} is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with TA=25C. The value in any given application depends on the user's specific board design.

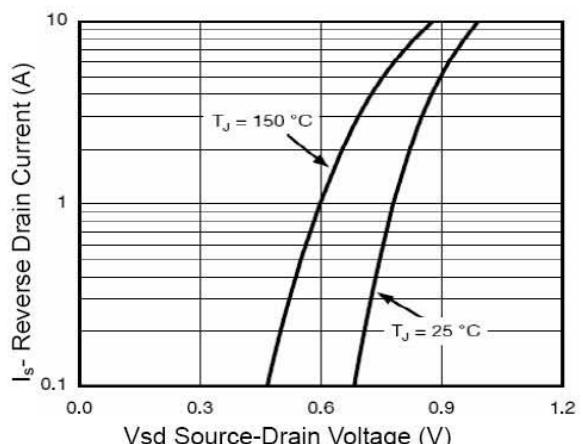
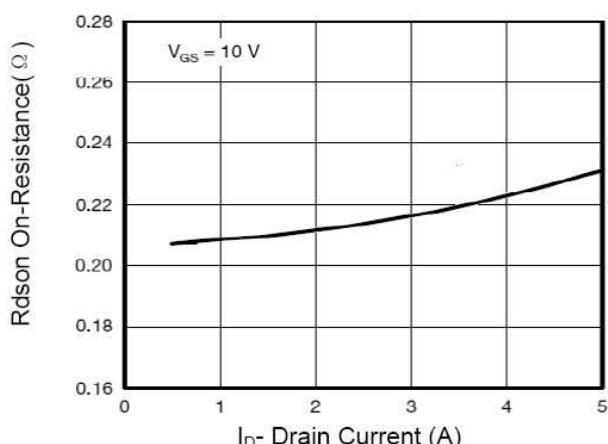
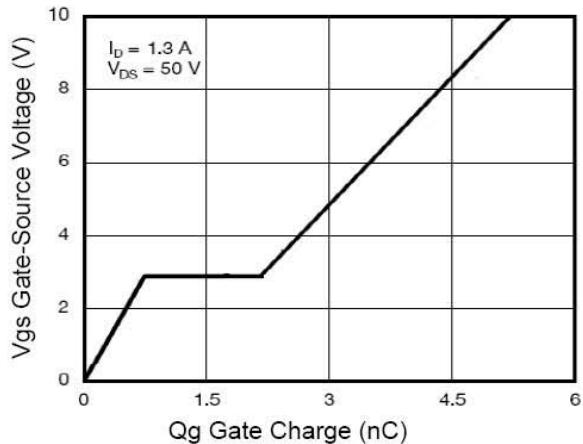
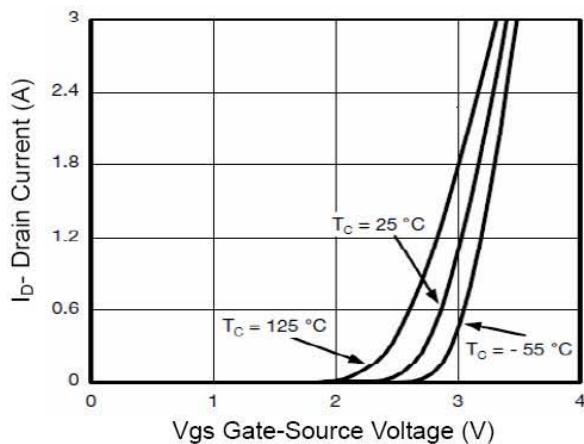
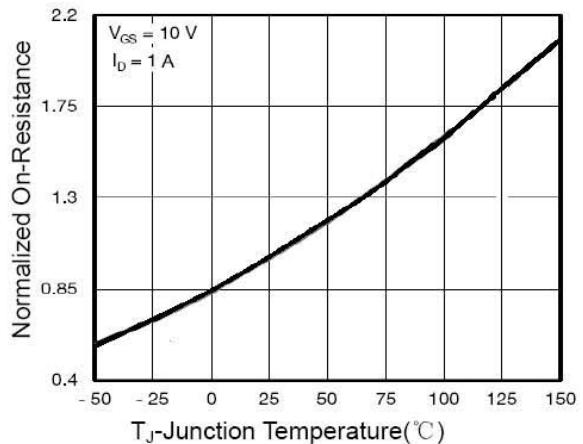
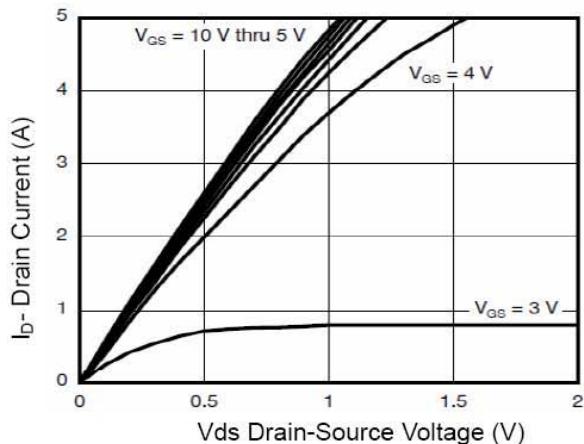
B: Repetitive rating, pulse width limited by junction temperature .

C: The current rating is based on the t< 10s junction to ambient thermal resistance rating.



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● TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS





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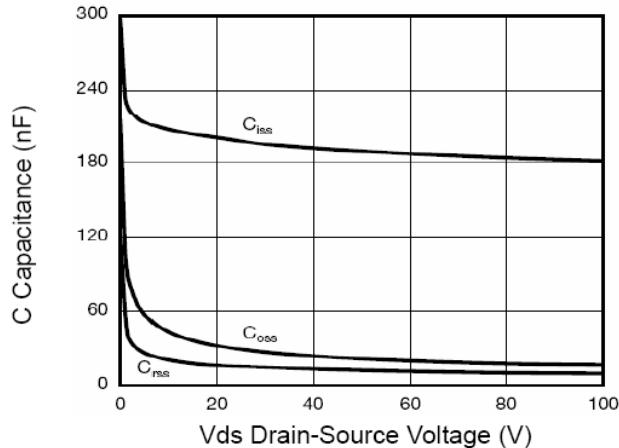


Figure 7 Capacitance vs Vds

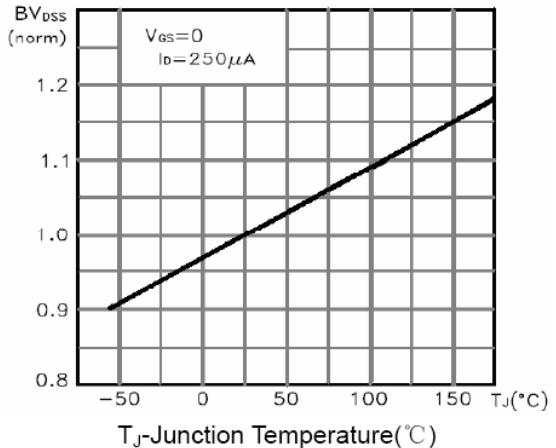
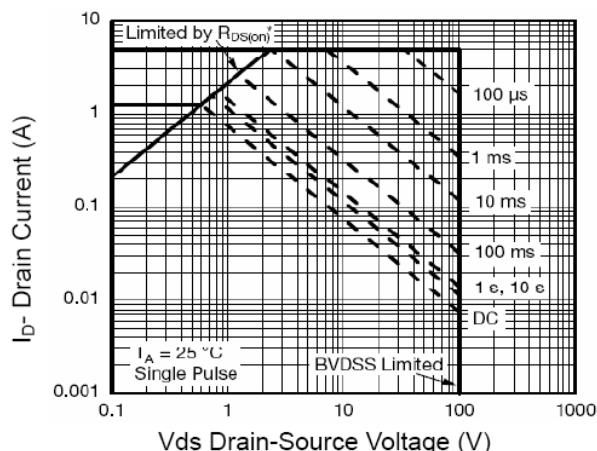
Figure 9 BV_{DSS} vs Junction Temperature

Figure 8 Safe Operation Area

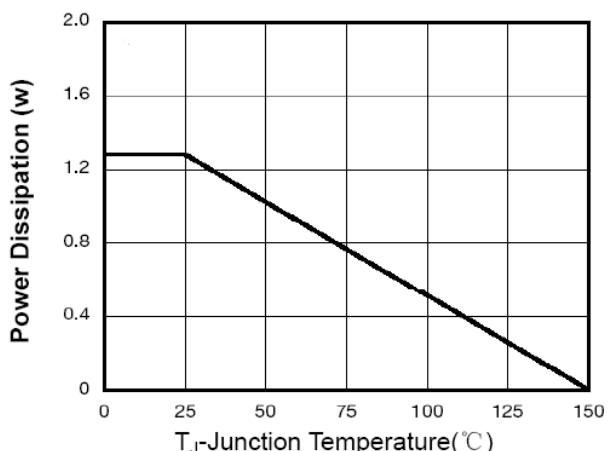


Figure 10 Power De-rating

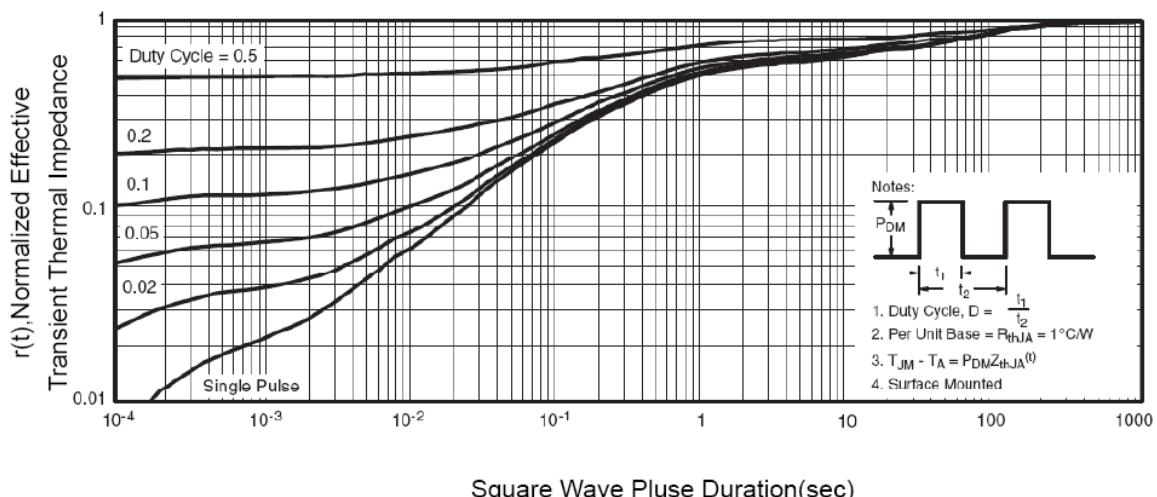
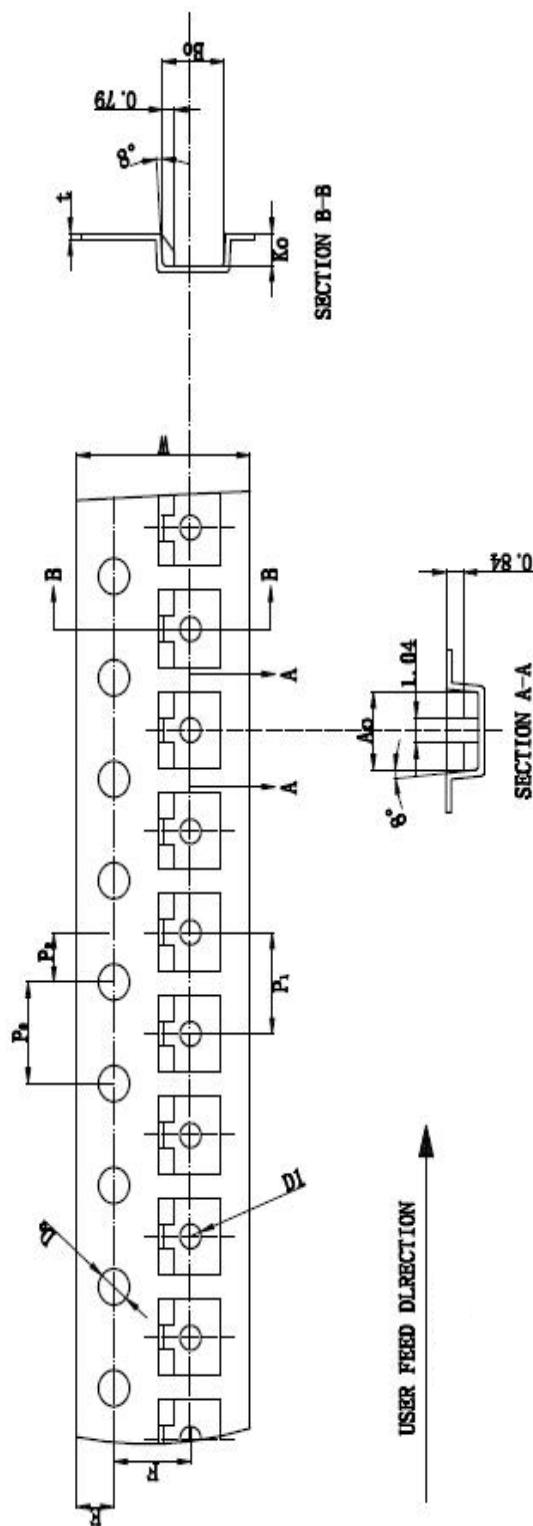


Figure 11 Normalized Maximum Transient Thermal Impedance

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Tape and Reel Specification


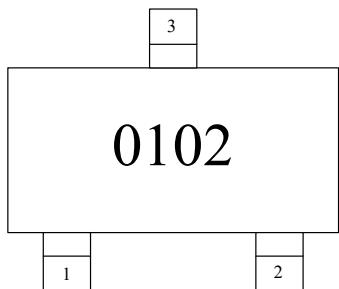
W	P	E	F	D	D1	P _o	P _{o10}	P ₂	A ₀	B ₀	K ₀	T
8.00	4.00	1.75	3.50	1.50	1.00	4.00	40.00	2.00	3.15	2.77	1.22	0.20
+0.3/-0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.05	±0.05	±0.1	±0.1	±0.1	±0.02



TNM03K100K

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Marking Codes



Ordering Information

Part Number	Working Voltage	Quantity Per Reel	Reel Size
TNM02K100K	100V	3,000	7 Inch