FH8612G7

One Cell Li-ion and Li-poly Battery Protection IC

Features

- Protection of Charger Reverse Connection
- Protection of Battery Cell Reverse Connection
- Over-temperature Protection
- Overcharge Current Protection
- Two-step Overcurrent Detection:
  Over Discharge Current
  Load Short Circuiting
- Charger Detection Function
- 0V Battery Charging Function
- 50mΩ Low $R_{SS(ON)}$ Internal Power MOSFET
- Delay Times are generated inside
- High-accuracy Voltage Detection
- Low Current Consumption
  Operation Mode: 0.7μA typ.
  Power-down Mode: 0.1μA typ.
- Only One External Capacitor Required
- Available in DFN1.6*1.6-6L Package
- -40°C to +85°C Temperature Range

Applications

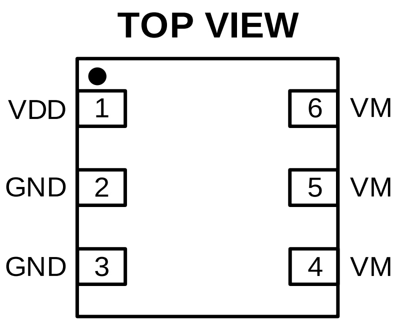
- One-Cell Li-ion Battery Pack
- Power Bank
- One-Cell Li-poly Battery Pack
- IOT Sensor/Electronic Toys/ Wearable Devices

General Description

The FH8612G7 is a high integration solution for lithium-ion/polymer battery protection. FH8612G7 contains internal power MOSFET, high-accuracy voltage detection circuits and delay circuits. FH8612G7 has all the protection functions required in the battery application including overcharging, over discharging, overcurrent and load short circuiting protection etc. The accurate overcharging detection voltage ensures safe and full utilization charging. The low standby current drains little current from the cell while in storage. The device is not only targeted for digital cellular phones, but also for any other Li-Ion and Li-Poly battery-powered information appliances requiring long-term battery life.

The FH8612G7 requires a minimal number of readily available, external components and is available in a pace saving DFN1.6*1.6-6L package.

PIN Configuration



DFN1.6*1.6-6L

Figure 1. Pin Configuration

PIN Description

Pin	Name	Function
1	VDD	Power Supply Pin
2	GND	Grounding end, battery core negative pole
3	GND	Grounding end, battery core negative pole
4	VM	Charger minus voltage input pin
5	VM	Charger minus voltage input pin
6	VM	Charger minus voltage input pin

Typical Application

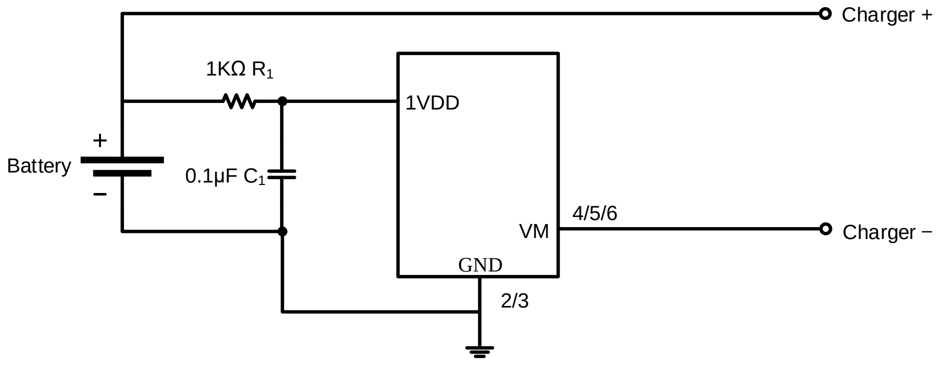


Figure 2. Basic Application Circuit

Absolute Maximum Ratings

Item	Min	Max	Unit
VDD Input Voltage	-0.3	6	V
VM Input Voltage	-6	10	V
Operating Temperature Range	-40	85	°C
Operating junction temperature, T_J	-40	150	°C
Storage temperature, T_{stg}	–55	150	°C
Lead Temperature (Soldering, 10sec.)		260	°C

Note (1): Exceeding these ratings may damage the device.

Note (2): The device is not guaranteed to function outside of its operating conditions.

ESD Ratings

Item	Description	Value	Unit
$V_{(ESD-HBM)}$	Human Body Model (HBM) ANSI/ESDA/JEDEC JS-001-2014 Classification, Class: 2	±2000	V
$V_{(ESD-CDM)}$	Charged Device Mode (CDM) ANSI/ESDA/JEDEC JS -002-2014 Classification, Class: C0b	±200	V
$I_{LATCH-UP}$	JEDEC STANDARD NO.78E APRIL 2016 Temperature Classification, Class: I	±150	mA

Thermal Information

Item	Description	Value	Unit
$R_{\theta JA}$	Junction-to-ambient thermal resistance (1)(2)	95	°C/W
$R_{\theta JC(top)}$	Junction-to-case (top) thermal resistance	49.5	°C/W
$R_{\theta JB}$	Junction-to-board thermal resistance	15.5	°C/W
ψ_{JT}	Junction-to-top characterization parameter	3.2	°C/W
ψ_{JB}	Junction-to-board characterization parameter	15.5	°C/W

Note (1): The package thermal impedance is calculated in accordance to JESD 51-7.

Note (2): Thermal Resistances were simulated on a 4 -layer, JEDEC board

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Typ.	Max	Unit
Detection Voltage						
Overcharge Detection Voltage	V_{CU}		4 25	4 3	4.35	V
Overcharge Release Voltage	V_{CL}		4.05	4.1	4.15	V
Overdischarge Detection Voltage	V_{DL}		2.7	2.8	2.9	V
Overdischarge Release Voltage	V_{DR}		2.9	3.0	3.1	V
Charger Detection Voltage	*V_{CHA}			-0.12		V
Detection Current						
Overdischarge Current1 Detection	*I_{IOV1}	V_{DD} =3.6V	0.6	1.0	1.5	A
Load Short-Circuiting Detection	*I_{SHORT}	V_{DD} =3.6V	5	10	15	A
Current Consumption						
Current Consumption in Operation	I_{OPE}	V_{DD}=3.6V VM=0V		0.7	1.3	μA
Current Consumption in power Down	I_{PDN}	V_{DD}=2.0V VM floating		0.1	0.9	μA
VM Internal Resistance						
Resistance between VM and V_{DD}	*R_{VMD}	V_{DD}=3.6V VM=1.0V	100	150	200	kΩ
Resistance between VM and GND	*R_{VMS}	V_{DD}=2.0V VM=1.0V	5	10	20	kΩ
FET on Resistance						
Equivalent FET on Resistance	*$R_{SS(ON)}$	V_{DD}=3.6V I_{VM}=1.0A	45	50	60	mΩ
Over Temperature Protection						
Over Temperature Protection	*T_{SHD+}			130		℃
Over Temperature Recovery Degree	*T_{SHD-}			100		℃
Detection Delay Time						
Overcharge Voltage Detection Delay Time	t_{CU}		50	100	200	mS
Overdischarge Voltage Detection Delay Time	t_{DL}		30	60	120	mS
Overdischarge Current Detection Delay Time	*t_{IOV}	V_{DD}=3.6V	2	5	10	mS
Load Short-Circuiting Detection Delay Time	*t_{SHORT}	V_{DD}=3.6V	20	50	150	μS

Note (1): *The parameter is guaranteed by design.

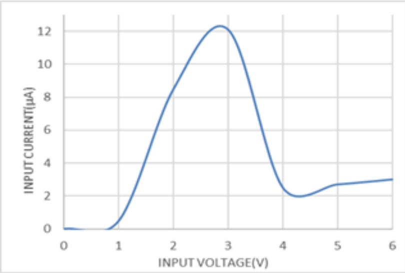
Typical Performance Characteristics (1) (2)

Note 4: Performance waveforms are tested on the evaluation board.

Note 5: V_{IN} =4.5V, R_1=1K Ω, C_1=0.1uF, T_A = +25ºC, unless otherwise noted.

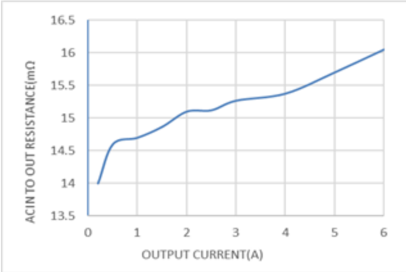
Supply Current vs. VIN

V_{DD}=0-6V



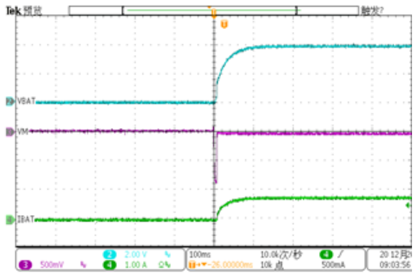
VM to GND On Resistance Vs. IOUT

V_{DD}=3.6V



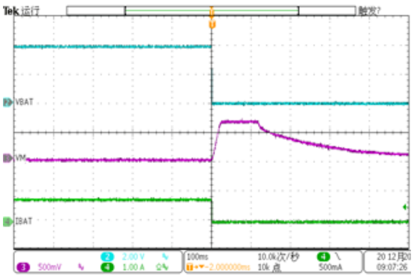
Power On

R_{BAT}=5 Ω

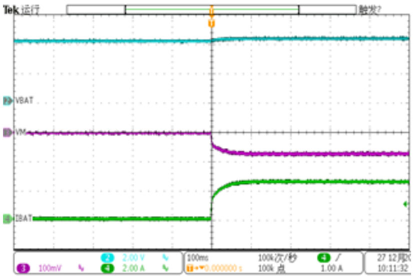


Power Off

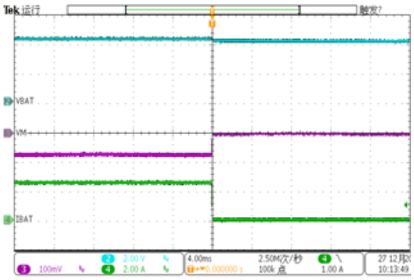
R_{BAT}=5 Ω



Battery Charging On

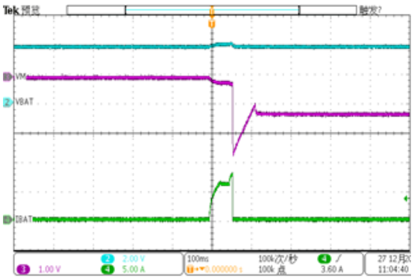


Battery Charging Off



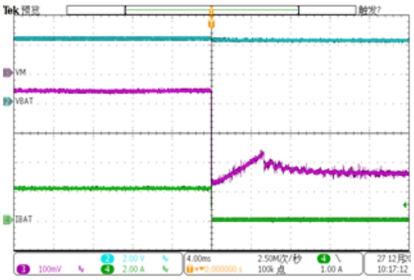
Battery Charging, OCP

I_{BAT}≥7A



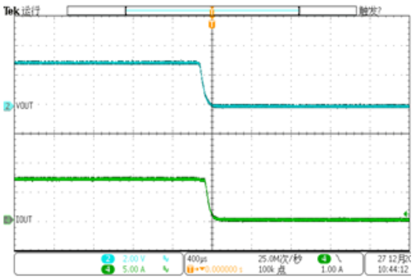
Battery Charging OVP

V_{BAT}≥4.3V



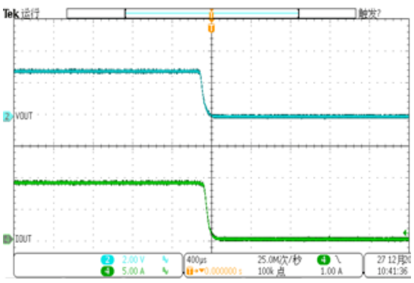
Overdischarge Protection UVP

Reduce V_{OUT} to UVP Point



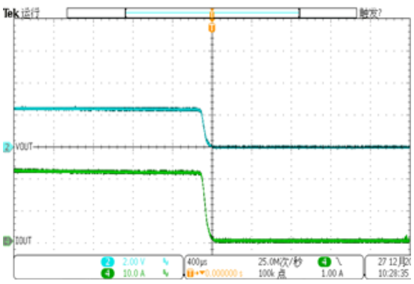
Overdischarge Protection OCP +

Increase IOUT to OCP Point



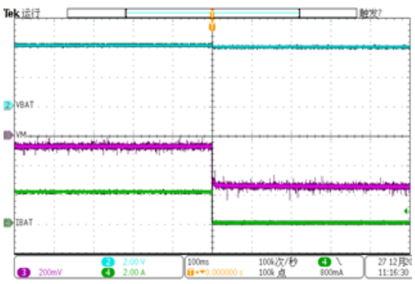
SCP Entry

Short Charger+ to Charger-



OTP

I_{BAT}=2A, Increase T_{SHD+} to 130℃



System Block Diagram

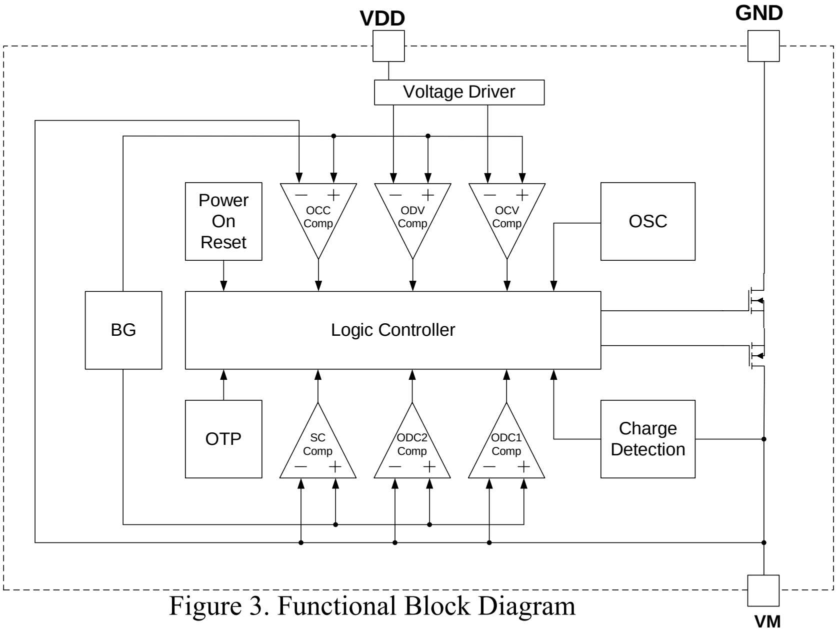


Figure 3. Functional Block Diagram

Functional Description

The FH8612G7 monitors the voltage and current of a battery and protects it from being damaged due to overcharge voltage, over discharge voltage, over discharge current, and short circuit conditions by disconnecting the batter from the load or charger. These functions are required in order to operate the battery cell within specified limits. The device requires only one external capacitor. The MOSFET is integrated and its $R_{SS(ON)}$ is as low as 50mΩ typical.

Normal operating mode

If no exception condition is detected, charging and discharging can be carried out freely. This condition is called the normal operating mode.

Overcharge Condition

When the battery voltage becomes higher than the overcharge detection voltage (V_{CU}) during charging under normal condition and the state continues for overcharge detection delay time (t_{CU}) or longer, FH8612G7 turns the charging control FET off to stop charging. This condition is called the overcharge status

The overcharge condition is released in the following two cases:

1. When the battery voltage drops below the overcharge release voltage (V_{CL}), the FH8612G7 turns the charging control FET on and returns to the normal condition.
2. When a load is connected and discharging starts, the FH8612G7 turns the charging control FET on and returns to the normal condition. The release mechanism is as follows: the discharging current flows through an internal parasitic diode of the charging FET immediately after a load is connected and discharging starts, and the VM pin voltage increases about 0.7V (forward voltage of the diode) from the GND pin voltage momentarily. The FH8612G7 detects this voltage and releases the overcharge condition. Consequently, in the case that the battery voltage is equal to or lower than the overcharge detection voltage (V_{CU}), the FH8612G7 returns to the normal condition immediately, but in the case the battery voltage is higher than the overcharge detection voltage (V_{CU}), the chip does not return to the normal condition until the battery the voltage drops below the overcharge detection voltage the (V_{CU}) even if the load is connected. In addition, if the VM pin voltage is equal to or lower than the overcurrent 1 detection voltage when a load is connected and discharging starts, the chip does not return to the normal condition.

Remark *If the battery is charged to a voltage higher than the overcharge detection voltage (V_{CU}), and even if a large load causing an overcurrent is connected, the battery voltage will not fall below the overcharge detection voltage (V_{CU}), and the overcurrent or load short detection will be detected in the battery. It does not work until the voltage is lower than the* *overcharge detection voltage (V_{CU}). However, since the internal impedance of the actual battery has several tens of mΩ, and the battery voltage immediately drops after the connection of the heavy load causing the overcurrent, the overcurrent operates. The load short-circuit detection works properly regardless of the battery voltage.*

Over-discharge Condition

When the battery voltage drops below the over-discharge detection voltage (V_{DL}) during discharging under normal condition and it continues for the over-discharge detection delay time (t_{DL}) or longer, the FH8612G7 turns the discharging control FET off and stops discharging. This condition is called over-discharge status. After the discharging control FET is turned off, the VM pin is pulled up by the R_{VMD} resistor between VM and VDD in FH8612G7. Meanwhile when VM is bigger than 1.5V (typ.) (the load short-circuiting detection voltage), the current of the chip is reduced to the power-down current (I_{PDN}). This condition is called power-down condition. The VM and VDD pins are shorted by the R_{VMD} resistor in the IC under the over-discharge and power-down conditions. The power-down condition is released when a charger is connected and the potential difference between VM and VDD becomes 1.3V (typ.) or higher (load short-circuiting detection voltage). At this time, the FET is still off. When the battery voltage becomes the over-discharge detection voltage (V_{DL}) or higher (see note), the FH8612G7 turns the FET on and changes to the normal condition from the over-discharge condition.

Remark *If the VM pin voltage is not less than the charger detection voltage (V_{CHA}) and the battery voltage reaches the over-discharge release voltage (V_{DR}) or higher, the over-discharge condition is released when the battery under over-discharge condition is connected to the charger (The discharge control FET is turned on).*

Over-current Condition

When the discharging current becomes equal to or higher than a specified value (the VM pin voltage is equal to or higher than the overcurrent detection voltage) during discharging under normal condition and the state continues for the overcurrent detection delay time or longer, the FH8612G7 turns off the discharging control FET to stopdischarging. This condition is called overcurrent status. (The overcurrent includes overcurrent, or loadshortcircuiting.) The VM and GND pins are shorted internally by the R_{VMS} resistor under the overcurrent condition.When a load is connected, the VM pin voltage equals the VDD voltage due to the load. The overcurrent condition returns to the normal condition when the load is released and the impedance between the B+ and B- pins becomes higher than the automatic recoverable impedance. When the load is removed, the VM pin goes back to the GND potential since the VM pin is shorted the GND pin with the R_{VMS} resistor. Detecting that the VM pin potential is lower than the overcurrent detection voltage (V_{IOV}), the IC returns to the normal condition.

Abnormal Charge Current Detection

the VM pin voltage drops below the charger detection voltage (V_{CHA}) during charging under the normal condition and it continues for the overcharge detection delay time (t_{CU}) orlonger, the FH8612G7 turns the charging control FET off and stops charging. This action is called abnormal charge current detection. Abnormal charge current detection works when the discharging control FET is on and the VM pin voltage drops below the charger detection voltage (V_{CHA}). When an abnormal charge current flows into a battery in the overdischarge condition, the FH8612G7 consequently turns the charging control FET off and stops charging after the battery voltage becomes the overdischarge detection voltage and the overcharge detection delay time (t_{CU}) elapses.

Abnormal charge current detection is released when the voltage difference between VM pin and GND pin becomes lower than the charger detection voltage (V_{CHA}) by separating the charger. Since the 0V battery charging function has higher priority than the abnormal charge current detection function, abnormal charge current may not be detected by the product with the 0V battery charging function while the battery voltage is low.

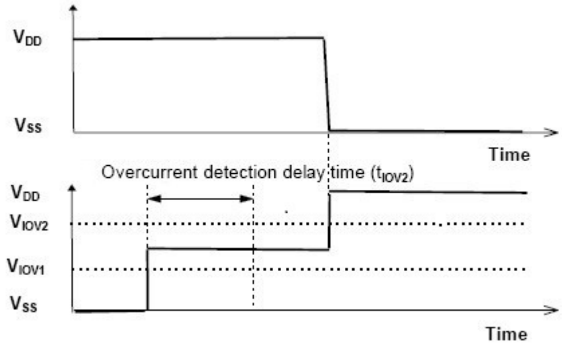


Figure 4. Overcurrent delay time

Load Short-circuiting condition

If voltage of VM pin is equal or below short-circuiting protection voltage (V_{SHORT}), the FH8612G7 will stop discharging and battery is disconnected from load. The maximum delay time to switch current off is t_{SHORT} . This status is released when voltage of VM pin is higher than short protection voltage (V_{SHORT}), such as when disconnecting the load.

Delay Circuits

The detection delay time for over-discharge current and load short-circuiting starts when over-discharge current isdetected. As soon as over-discharge current or load short-circuiting is detected over detection delay time for over-discharge current or load short-circuiting,the FH8612G7 stops discharging. When battery voltage falls below over-discharge detection voltage due to over-discharge current, the FH8612G7stop discharging by over-discharge currentdetection.In this case the recovery of battery voltage is so slow that if battery voltage after over-discharge voltage detection delay time is still lower than over-discharge detection voltage, the FH8612G7 shifts to power-down.

0V Battery Charging Function

This function enables the charging of a connected battery whose voltage is 0V by self-discharge.
When a charger having 0V battery start charging charger voltage (V_{0CHA}) or higher is connected between B+ and B- pins, the charging control FET gate is fixed to VDD potential. When the voltage between the gate and the source of the charging control FET becomes equal to or higher than the turn-on voltage by the charger voltage, the charging control FET is turned on to start charging. At this time, the discharging control FET is off and the charging current flows through the internal parasitic diode in the discharging control FET. If the battery voltage becomes equal to or higher than the overdischarge release voltage (V_{DU}), the normal condition returns.

Note (1): Some battery providers do not recommend charging of completely discharged batteries. Please refer to battery providers before the selection of 0V battery charging function.

Note (2): The 0V battery charging function has higher priority than the abnormal charge current detection function.Consequently, a product with the 0V battery charging function charges a battery and abnormal charge current cannot be detected during the battery voltage is low (at most 1.8V or lower).

Note (3): When a battery is connected to the IC for the first time, the IC may not enter the normal condition in which discharging is possible. In this case, set the VM pin voltage equal to the GND voltage (short the VM and GND pins or connect a charger) to enter the normal condition.

Timing Chart

Overcharge and overdischarge detection

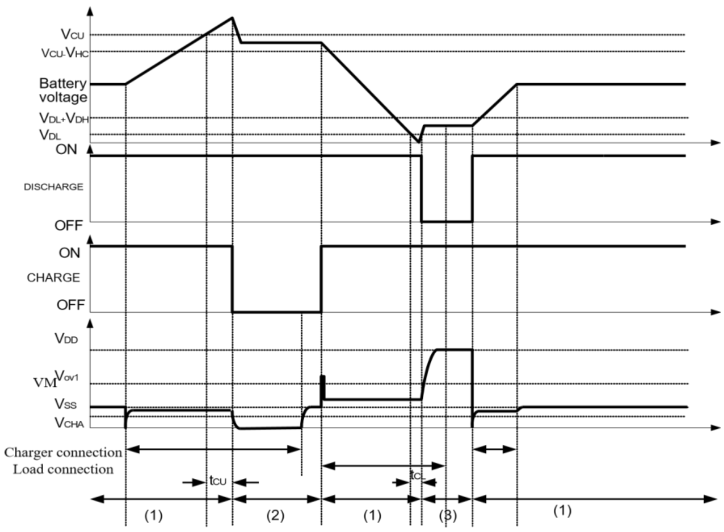


Figure5-1 Overcharge and Overdischarge Voltage Detection

Remark:

(1) Normal condition *(2) Overcharge voltage condition*

(3) Overdischarge voltage condition *(4) Overcurrent condition*

Overdischarge current detection

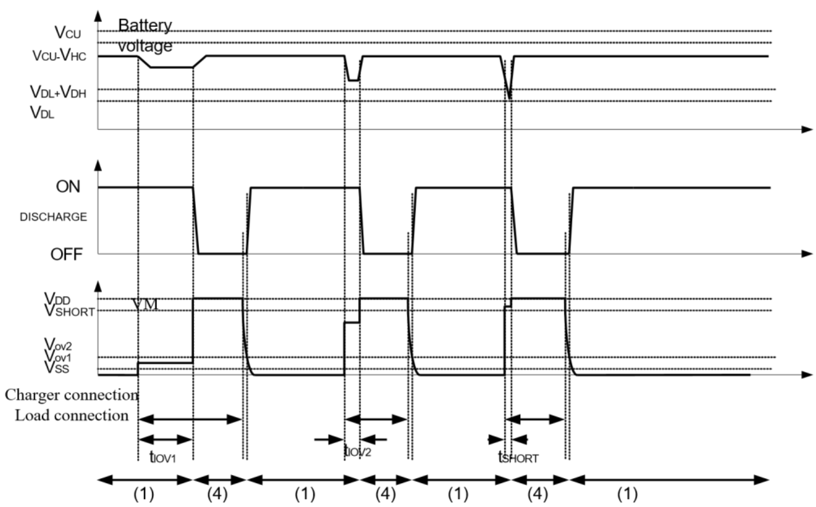


Figure5-2 Overdischarge Current Detection

Remark:

(1) Normal condition *(2) Overcharge voltage condition*

(3) Overdischarge voltage condition *(4) Overcurrent condition*

Charger Detection

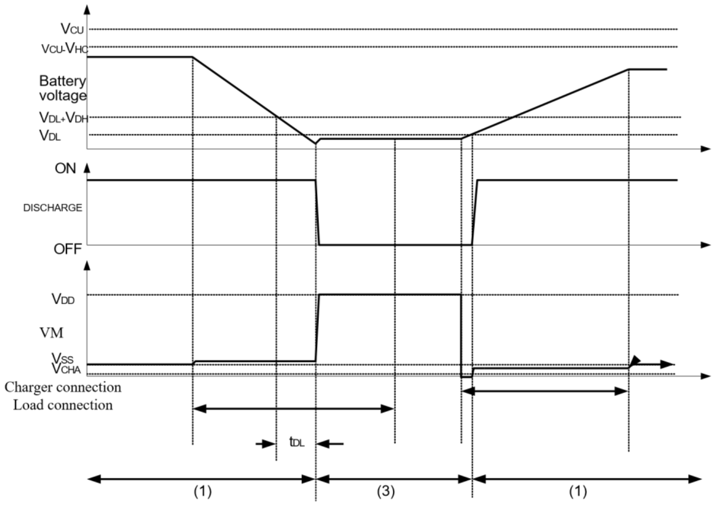


Figure5-3 Charger Detection

Remark:

(1) Normal condition (2) Overcharge voltage condition

(3) Overdischarge voltage condition (4) Overcurrent condition

Abnormal Charger Detection

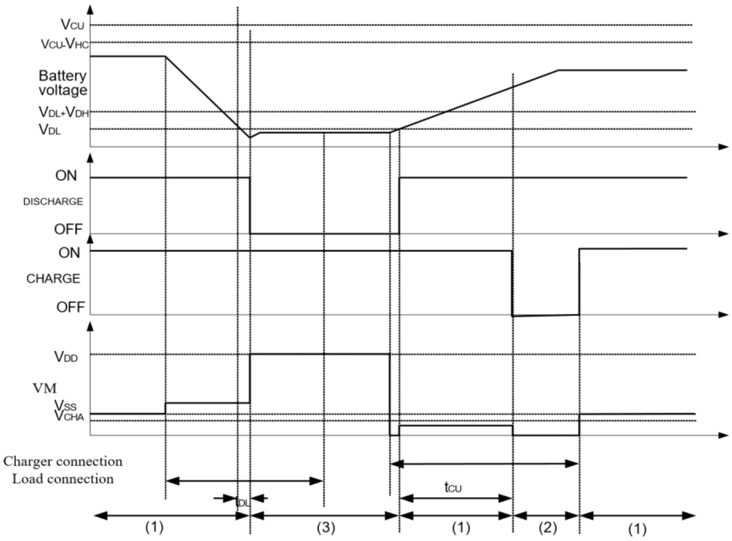


Figure5-4 Abnormal Charger Detection

Remark:

(1) Normal condition (2) Overcharge voltage condition

(3) Overdischarge voltage condition (4) Overcurrent condition

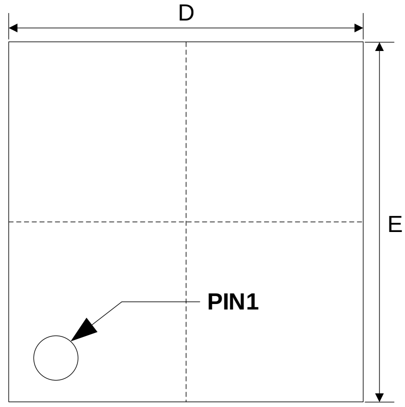
Typical Application

As shown in Figure 1, the bold line is the high density current path which must be kept as short as possible. For thermal management, ensure that these trace widths are adequate.C1& R1 is a decoupling capacitor & resistor which should be placed as close as possible to FH8612G7.

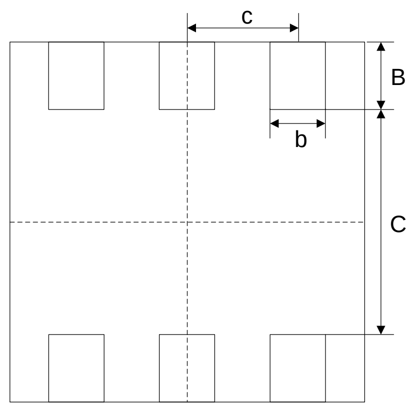
Precautions

- Pay attention to the operating conditions for input/output voltage and load current so that the power loss in FH8612G7 does not exceed the power dissipation of the package.
- Do not apply an electrostatic discharge to this FH8612G7 that exceeds the performance ratings of the built-in electrostatic protection circuit.

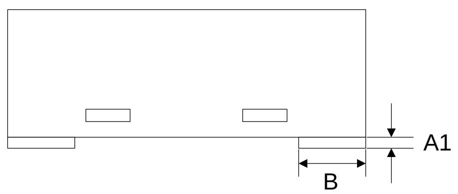
■ Package Information: DFN1.6x1.6-6L



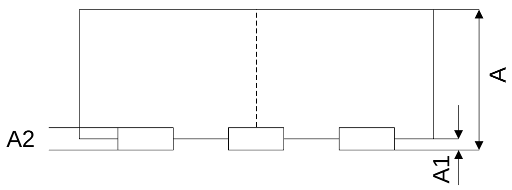
TOP VIEW



BOTTOM VIEW

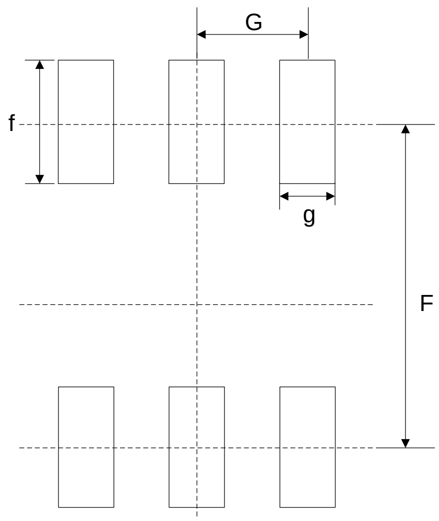


SIDE VIEW A



SIDE VIEW B

SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.72	0.76	0.80
A1	0.00	0.03	0.05
A2	0.08	0.13	0.18
B	0.20	0.30	0.40
b	0.17	0.22	0.27
C		1.00	
c		0.50	
D	1.50	1.60	1.70
E	1.50	1.60	1.70
F		1.40	
f		0.55	
G		0.50	
g		0.30	



RECOMMENDED LAND PATTERN

NOTE:
1. CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET IS IN MILLIMETERS.
2. PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
3. PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
4. LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.004" INCHES MAX.
5. DRAWING CONFORMS TO JEDEC MS-012, VARIATION BA.
6. DRAWING IS NOT TO SCALE.