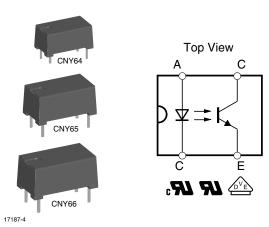


# Optocoupler, Phototransistor Output, Very High Isolation Voltage



#### **LINKS TO ADDITIONAL RESOURCES**









#### **DESCRIPTION**

The CNY64, CNY65, and CNY66 consist of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin plastic package.

The single components are mounted opposite one another, providing a distance between input and output for highest safety requirements of > 3 mm.

#### **APPLICATIONS**

Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):

- for appl. class I to IV at mains voltage ≤ 300 V
- for appl. class I to IV at mains voltage ≤ 600 V
- for appl. class I to III at mains voltage ≤ 1000 V according to DIN EN 60747-5-5 (VDE 0884-5), suitable for:
  - Switch-mode power supplies
  - Line receiver
  - Computer peripheral interface
  - Microprocessor system interface

#### **FEATURES**

Rated recurring peak voltage (repetitive)
 V<sub>IORM</sub> = 1450 V<sub>peak</sub>



• Thickness through insulation ≥ 3 mm

 Creepage current resistance according to VDE 0303 / IEC 60112 comparative tracking index: CTI ≥ 200



 Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

### **VDE STANDARDS**

These couplers perform safety functions according to the following equipment standards:

DIN EN 60747-5-5 (VDE 0884-5)
 Optocoupler for electrical safety requirements

• IEC 60950/EN 60950 Office machines

VDE 0804

Telecommunication apparatus and data processing

IEC 60065

Safety for mains-operated electronic and related household apparatus

VDE 0700/IEC 60335
 Household equipment

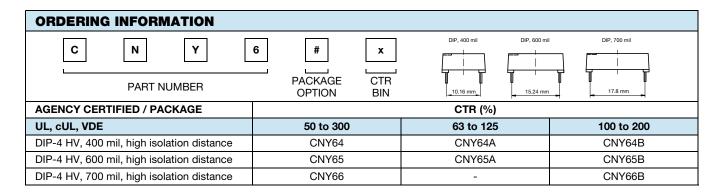
• VDE 0160

Electronic equipment for electrical power installation

VDE 0750/IEC60601
 Medical equipment

#### **AGENCY APPROVALS**

- UL / cUL 1577
- DIN EN 60747-5-5 (VDE 0884-5)





<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
Reverse voltage		$V_{R}$	5	V				
Forward current		I <sub>F</sub>	75	mA				
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	1.5	Α				
Power dissipation		P <sub>diss</sub>	120	mW				
Junction temperature		T <sub>j</sub>	100	°C				
OUTPUT								
Collector emitter voltage		$V_{CEO}$	32	V				
Emitter collector voltage		V <sub>ECO</sub>	7	V				
Collector current		I <sub>C</sub>	50	mA				
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA				
Power dissipation		P <sub>diss</sub>	130	mW				
Junction temperature		Tj	100	°C				
COUPLER								
Total power dissipation		P <sub>tot</sub>	250	mW				
Ambient temperature range		T <sub>amb</sub>	-55 to +85	°C				
Storage temperature range		T <sub>stg</sub>	-55 to +100	°C				
Soldering temperature	2 mm from case, ≤ 10 s	T <sub>sld</sub>	260	°C				

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
input						
Forward voltage	I <sub>F</sub> = 50 mA	$V_{F}$	-	1.25	1.6	V
Junction capacitance	V <sub>R</sub> = 0, f = 1 MHz	C <sub>j</sub>	-	50		pF
output						
Collector emitter voltage	I <sub>C</sub> = 1 mA	V <sub>CEO</sub>	32	-	-	V
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7	-	-	V
Collector emitter leakage current	V <sub>CE</sub> = 20 V, I <sub>F</sub> = 0 A	I <sub>CEO</sub>	-	-	200	nA
coupler						
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V <sub>CEsat</sub>	-	-	0.3	V
Cut-off frequency	$V_{CE}$ = 5 V, $I_F$ = 10 mA, $R_L$ = 100 $\Omega$	f <sub>c</sub>	-	110	-	kHz
Coupling capacitance	f = 1 MHz	C <sub>k</sub>	-	0.3	-	pF

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements



CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART SYMBOL MIN. TYP.				MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>		CNY64, CNY65, CNY66	CTR	50	-	300	%
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 10 mA	CNY64A	CTR	63	-	125	%
		CNY65A	CTR	63	-	125	%
		CNY64B	CTR	100	-	200	%
		CNY65B	CTR	100	-	200	%
		CNY66B	CTR	100	1	200	%

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	t <sub>d</sub>	-	2.6	-	μs
Rise time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	t <sub>r</sub>	-	2.4	-	μs
Fall time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega$ , (see Fig. 3)	t <sub>f</sub>	-	2.7	-	μs
Storage time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	ts	-	0.3	-	μs
Turn-on time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega, \text{ (see Fig. 3)}$	t <sub>on</sub>	-	5	-	μs
Turn-off time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega$ , (see Fig. 3)	t <sub>off</sub>	-	3	-	μs
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega, \text{ (see Fig. 4)}$	t <sub>on</sub>	-	25	-	μs
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega, \text{ (see Fig. 4)}$	t <sub>off</sub>	-	42.5	-	μs

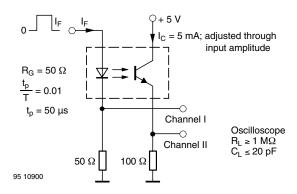


Fig. 1 - Test Circuit, Non-Saturated Operation

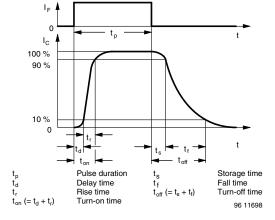


Fig. 3 - Switching Times

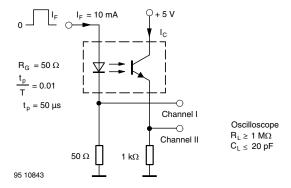


Fig. 2 - Test Circuit, Saturated Operation

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		55 / 85 / 21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	200	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V <sub>ISO</sub>	8200	$V_{RMS}$
Tested withstanding isolation voltage	According to UL1577, t = 1 s	V <sub>ISO</sub>	13 900	V <sub>peak</sub>
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	12 000	V <sub>peak</sub>
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V <sub>IORM</sub>	1450	V <sub>peak</sub>
	$T_{amb} = 25  ^{\circ}\text{C},  V_{IO} = 500  \text{V}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
Isolation resistance	T <sub>amb</sub> = 100 °C, V <sub>IO</sub> = 500 V	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
	$T_{amb} = TS$ , $V_{IO} = 500 \text{ V}$	R <sub>IO</sub>	≥ 10 <sup>9</sup>	Ω
Output safety power		P <sub>SO</sub>	250	mW
Input safety current		I <sub>SI</sub>	120	mA
Input safety temperature		Ts	150	°C
Creepage distance	CNY64		≥ 9.5	mm
Clearance distance	CNY64		≥ 9.5	mm
Creepage distance	CNY65		≥ 14	mm
Clearance distance	CNY65		≥ 14	mm
Creepage distance	reepage distance		≥ 17	mm
Clearance distance	CNY66	≥ 17		mm
Insulation thickness		DTI	≥ 3	mm
Input to output test voltage, method B	V <sub>IORM</sub> x 1.875 = V <sub>PR</sub> , 100 % production test with t <sub>M</sub> = 1 s, partial discharge < 5 pC	$V_{PR}$	3375	V <sub>peak</sub>
Input to output test voltage, method A	$V_{IORM}$ x 1.6 = $V_{PR}$ , 100 % sample test with $t_M$ = 10 s, partial discharge < 5 pC	$V_{PR}$	2880	V <sub>peak</sub>

### Note

 According to DIN EN 60747-5-5. This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits

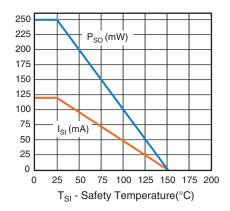


Fig. 4 - Safety Derating Diagram

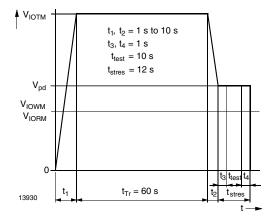


Fig. 5 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-5 (VDE 0884-5); IEC60747-5-5

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

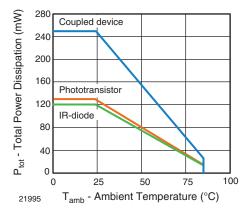


Fig. 6 - Total Power Dissipation vs. Ambient Temperature

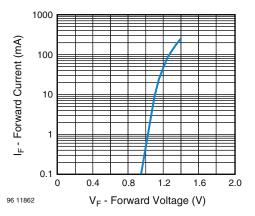


Fig. 7 - Forward Current vs. Forward Voltage

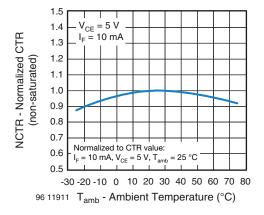


Fig. 8 - Normalized CTR (non-saturated) vs. Ambient Temperature

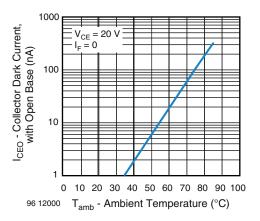


Fig. 9 - Collector Dark Current vs. Ambient Temperature

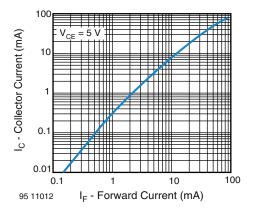


Fig. 10 - Collector Current vs. Forward Current

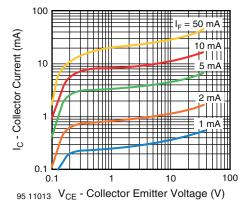


Fig. 11 - Collector Current vs. Collector Emitter Voltage

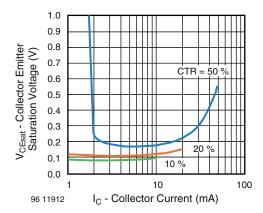


Fig. 12 - Collector Emitter Saturation Voltage vs. Collector Current

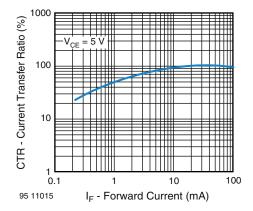


Fig. 13 - Current Transfer Ratio vs. Forward Current

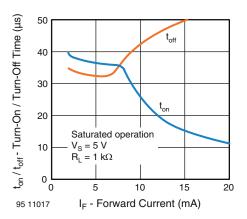


Fig. 14 - Turn-on / Turn-off Time vs. Collector Current

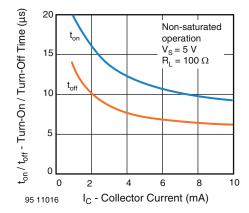
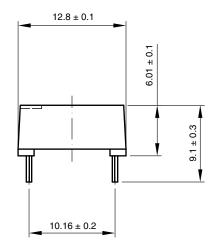


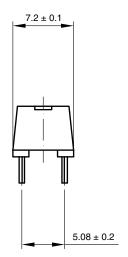
Fig. 15 - Turn-On / Turn-Off Time vs. Forward Current

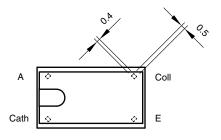
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# Vishay Semiconductors

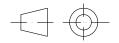
### **PACKAGE DIMENSIONS** in millimeters FOR CNY64







Weight: ca. 0.73 g



technical drawings according to DIN specifications

Drawing-No.: 6.544-5038.01-4

Issue: 2; 10.11.98

14765

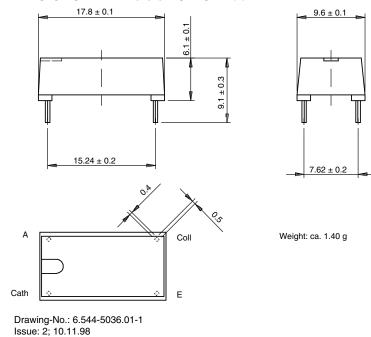
technical drawings according to DIN specifications

> technical drawings according to DIN specifications

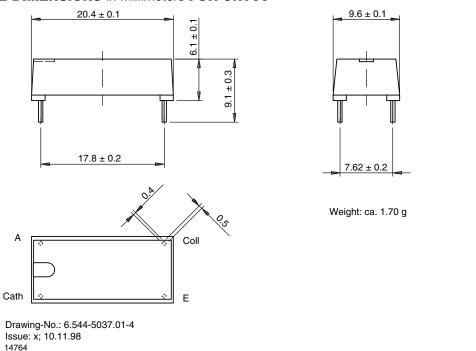
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### **PACKAGE DIMENSIONS** in millimeters FOR CNY65



### **PACKAGE DIMENSIONS** in millimeters FOR CNY66



### PACKAGE MARKING (example of CNY65A)



TUBE INFORMATION						
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX			
CNY64	40	50	2000			
CNY65	30	35	1050			
CNY66	25	35	875			

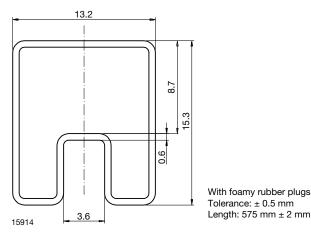


Fig. 16 - Tube Dimensions

### **SOLDER PROFILES**

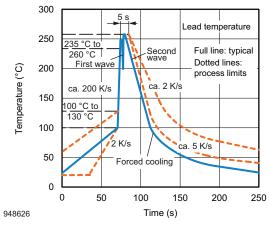


Fig. 17 - Wave Soldering Double Wave Profile According to J-STD-020 for Through-Hole Devices

### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions:  $T_{amb}$  < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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