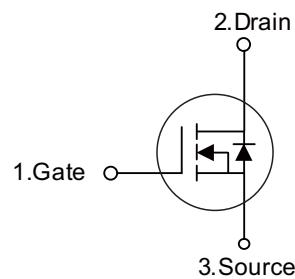


## ■ PRODUCT CHARACTERISTICS

$V_{DSS}$	30	V
$R_{DS(ON)}$ -Max	2.5 at $V_{GS}=10V$	$m\Omega$
$R_{DS(ON)}$ -Max	3.3 at $V_{GS}=4.5 V$	$m\Omega$
ID	130	A

Symbol



## ■ APPLICATIONS

Power switch circuit of adaptor and charger.

## ■ FEATURES

Fast switching

Low ON fesistance

Low Gate Charge

Low reverse transfer capacitances



TO-252



TO-251

## ■ ORDER INFORMATION

Order codes		Package	Packing
Halogen-Free	Halogen		
N/A	MOT130N03D	TO-252	2500 pieces /Reel
N/A	MOT130N03C	TO-251	70 pieces/Tube

 ■ ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ C$ , unless otherwise specified)

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	30	V
$I_D$	Continuous Drain Current $T_C = 25^\circ C$	130	A
	Continuous Drain Current $T_C = 100^\circ C$ (Package limited)	60	A
$I_{DM}^{a1}$	Pulsed Drain Current $T_C = 25^\circ C$ (Package limited)	240	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$E_{AS}^{a2}$	Single Pulse Avalanche Energy	1000	mJ
$P_D$	Power Dissipation $T_C = 25^\circ C$	83	W
	Derating Factor above $25^\circ C$	0.666	W/ $^\circ C$
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	150, -55 to 150	$^\circ C$

■ ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , unless otherwise noted)

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$V_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	30	--	--	V
$I_{DSS}$	Drain to Source Leakage Current	$V_{DS}=30\text{V}, V_{GS}=0\text{V}, T_J = 25^\circ\text{C}$	--	--	1	$\mu\text{A}$
		$V_{DS}=24\text{V}, V_{GS}=0\text{V}, T_J = 125^\circ\text{C}$	--	--	100	$\mu\text{A}$
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS} = +20\text{V}$	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS} = -20\text{V}$	--	--	-100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=10\text{V}, I_D=19\text{A}$	--	1.9	2.5	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=19\text{A}$		2.6	3.3	$\text{m}\Omega$
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.0	--	3.0	V
Pulse width $t_p \leqslant 300\mu\text{s}, \delta \leqslant 2\%$						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_g$	Gate resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	--	5.3	--	$\Omega$
$C_{iss}$	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=15\text{V}$ $f=1.0\text{MHz}$	--	4893	--	pF
$C_{oss}$	Output Capacitance		--	818	--	
$C_{rss}$	Reverse Transfer Capacitance		--	482	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$V_{GS}=10\text{V}, R_G=6\Omega$ $V_{DD}=15\text{V}, I_D=50\text{A}$	--	14.8	--	ns
$t_r$	Rise Time		--	15.2	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	119.6	--	
$t_f$	Fall Time		--	59.2	--	
$Q_g$	Total Gate Charge	$V_{GS}=10\text{V}, V_{DD}=15\text{V}$ $I_D=50\text{A}$	--	79.1	--	nC
$Q_{gs}$	Gate to Source Charge		--	13.6	--	
$Q_{gd}$	Gate to Drain ("Miller") Charge		--	16.0	--	

■ ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , unless otherwise noted)

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$I_S$	Continuous Source Current (Body Diode)		--	--	60	A
$I_{SM}$	Maximum Pulsed Current (Body Diode)		--	--	240	A
$V_{SD}$	Diode Forward Voltage	$I_S=50\text{A}, V_{GS}=0\text{V}$	--	--	1.2	V
$t_{rr}$	Reverse Recovery Time	$I_S=50\text{A}, T_j = 25^\circ\text{C}$ $dI_F/dt=100\text{A}/\mu\text{s}$ ,	--	44.4	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	34.6	--	nC
$I_{RRM}$	Reverse Recovery Current		--	1.6	--	A
Pulse width $t_p \leqslant 300\text{\mu s}$ , $\delta \leqslant 2\%$						

Symbol	Parameter	Max.	Units
$R_{\theta JC}$	Junction-to-Case	1.5	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Junction-to-Ambient	100	$^\circ\text{C}/\text{W}$

<sup>a1</sup>: Repetitive rating; pulse width limited by maximum junction temperature<sup>a2</sup>:  $L=1\text{mH}$ ,  $I_D=44.73\text{A}$ , Start  $T_j=25^\circ\text{C}$ <sup>a3</sup>: Recommend soldering temperature defined by IPC/JEDEC J-STD 020

## ■ TEST CIRCUIT AND WAVEFORM

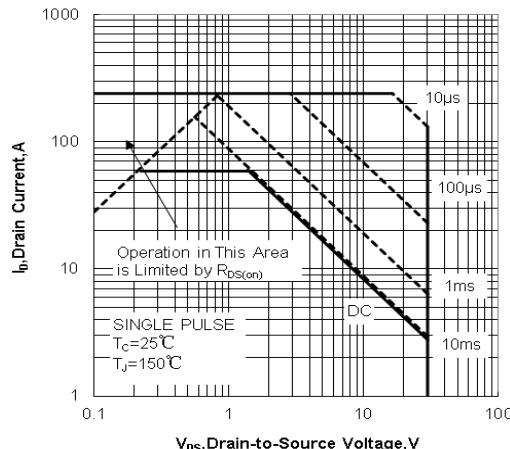


Figure 1. Maximum Forward Bias Safe Operating Area

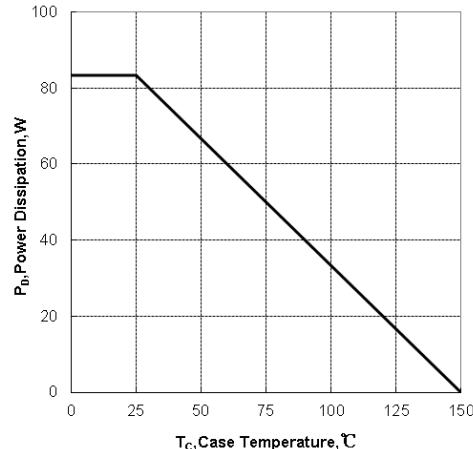


Figure 2. Maximum Power Dissipation vs Case Temperature

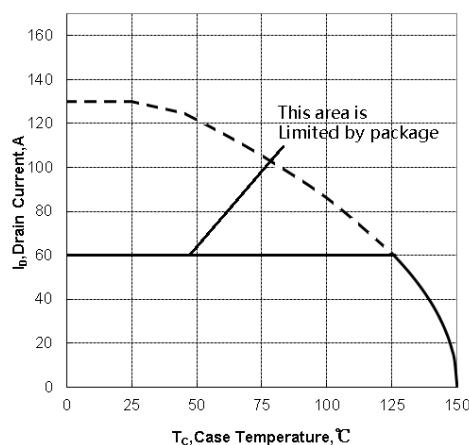


Figure 3. Maximum Continuous Drain Current vs Case Temperature

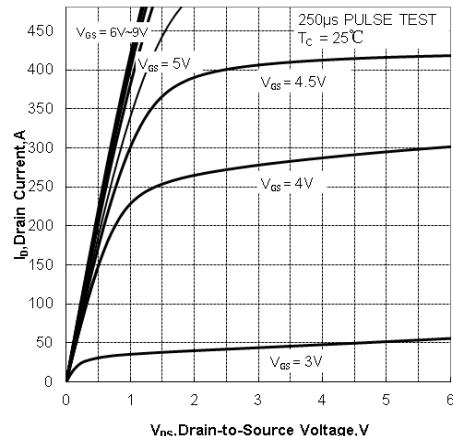


Figure 4. Typical Output Characteristics

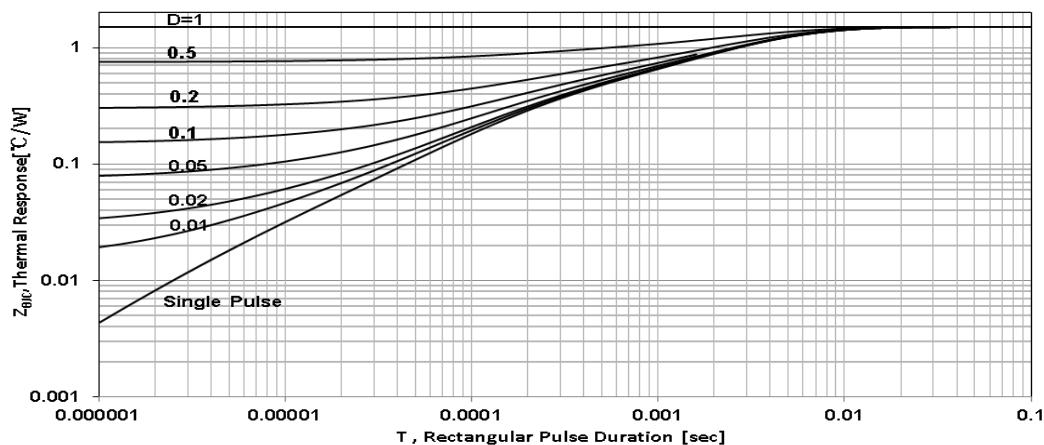
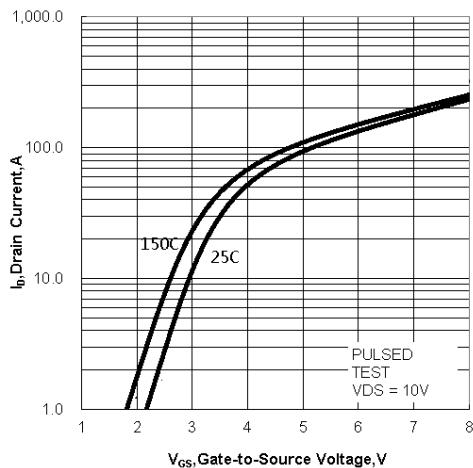
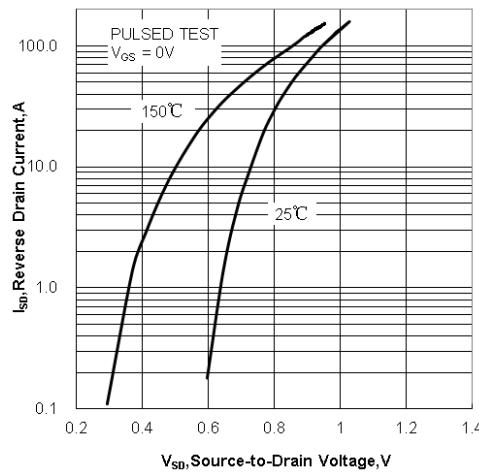


Figure 5. Maximum Effective Transient Thermal Impedance, Junction-to-Case

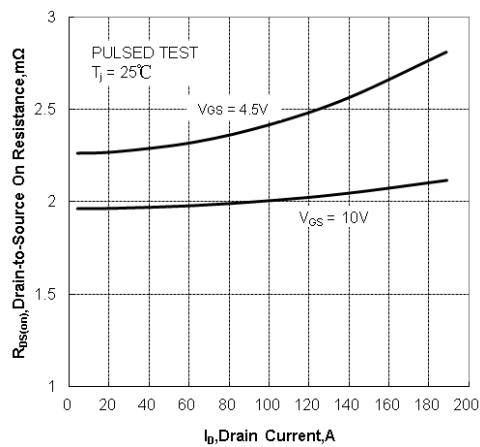
## ■ TYPICAL CHARACTERISTICS



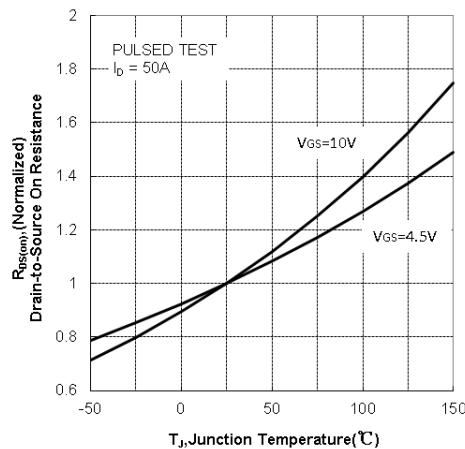
**Figure 6. Typical Transfer Characteristics**



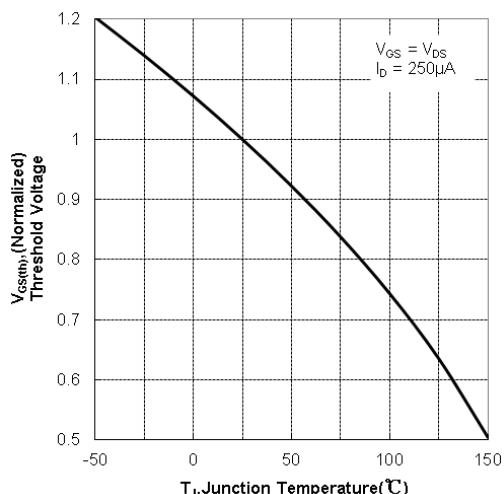
**Figure 7. Typical Body Diode Transfer Characteristics**



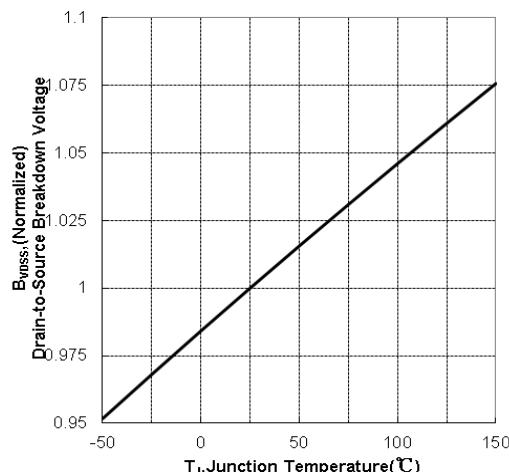
**Figure 8. Drain-to-Source On Resistance vs Drain Current**



**Figure 9. Nomalized on Resistance vs Junction Temperature**

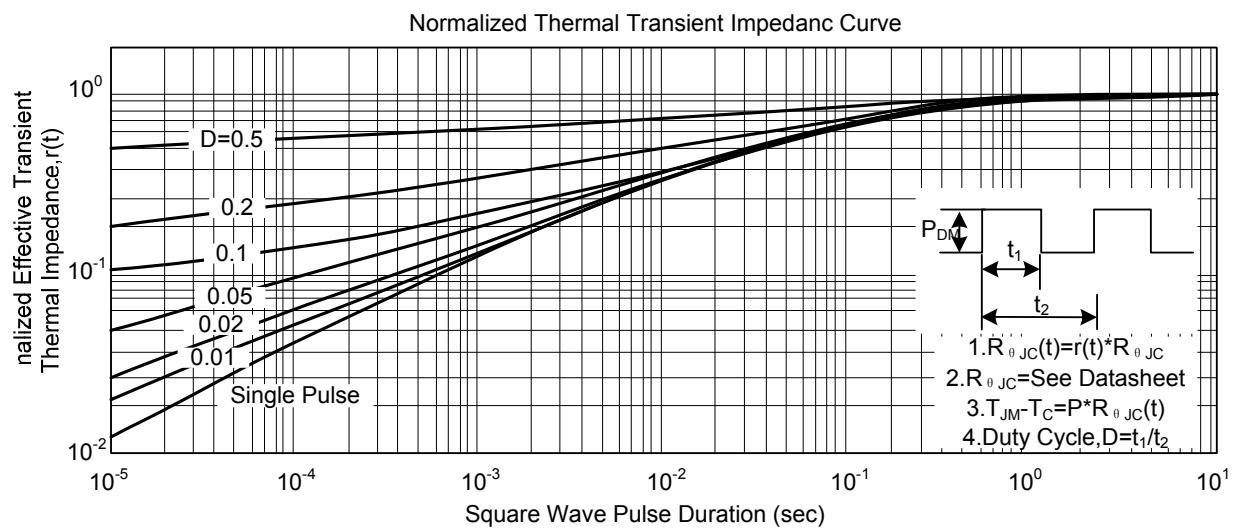
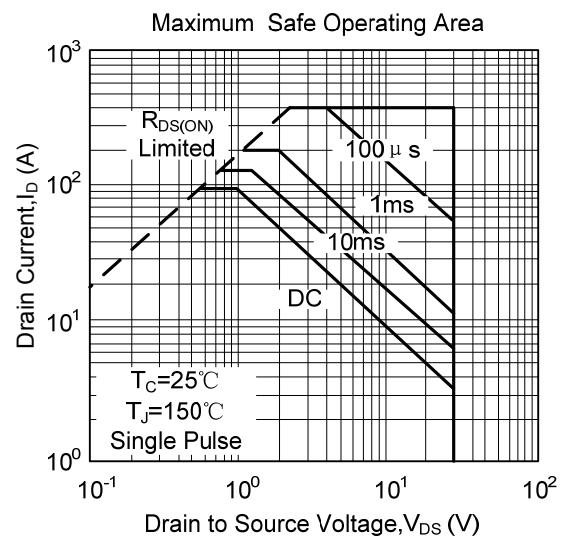
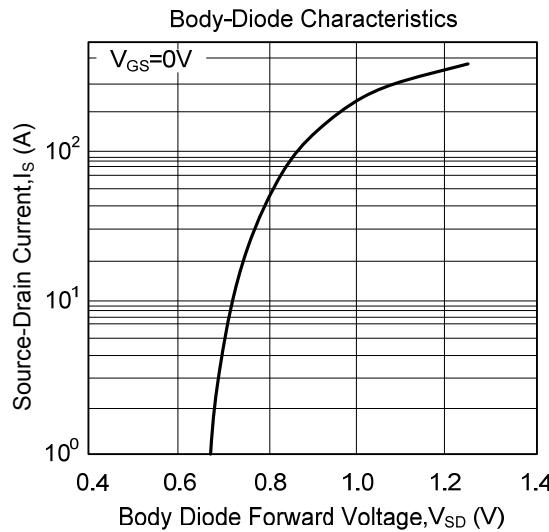


**Figure 10. Nomalized Threshold Voltage vs Junction Temperature**

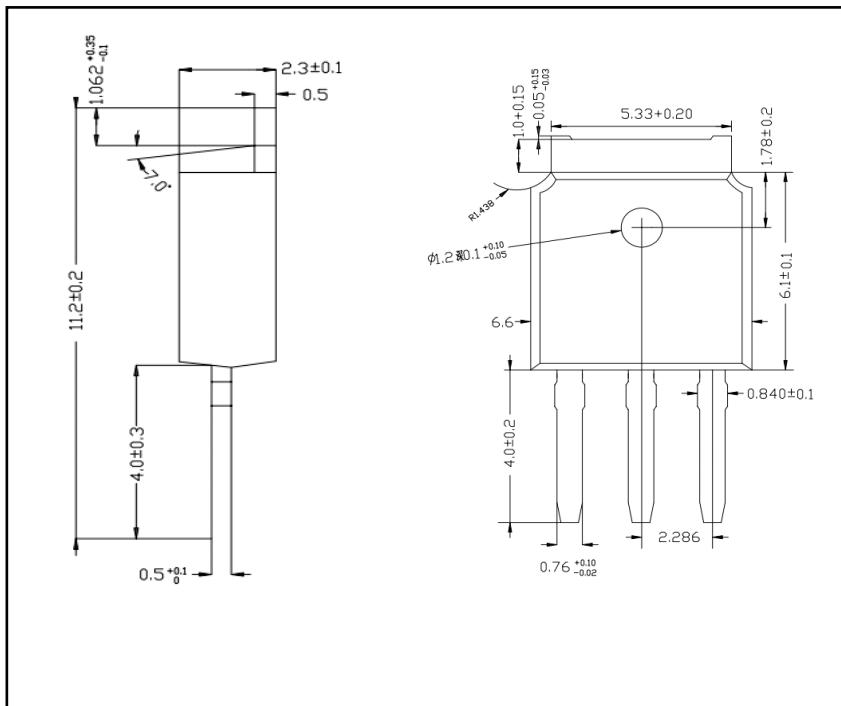


**Figure 11. Nomalized Breakdown Voltage vs Junction Temperature**

## ■ TYPICAL CHARACTERISTICS(Cont.)



## ■ TO-251 PACKAGE OUTLINE DIMENSIONS



## ■ TO-252 PACKAGE OUTLINE DIMENSIONS

